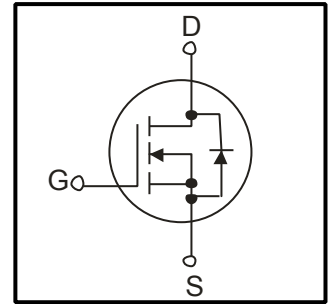


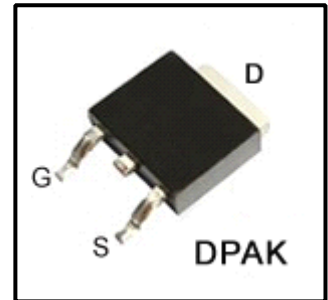
### Features

- Ultra low  $R_{dson}$
- Ultra low gate charge (typ.  $Q_g = 13nC$ )
- 100% UIS tested
- RoHS compliant



### General Description

Power MOSFET is fabricated using advanced super junction technology. The resulting device has extremely low on resistance, making it especially suitable for applications which require superior power density and outstanding efficiency.



### Absolute Maximum Ratings

Symbol	Parameter	Value	Units
$V_{DSS}$	Drain Source Voltage	650	V
$I_D$	Continuous Drain Current ( $T_c=25^\circ C$ ) ( $T_c=100^\circ C$ )	4	A
		2.5	
$I_{DM}$	Drain Current Pulsed <sup>1)</sup>	12	A
$V_{GS}$	Gate to Source Voltage	$\pm 30$	V
$E_{AS}$	Single Pulse Avalanche Energy <sup>2)</sup>	130	mJ
$I_{AR}$	Single Pulse Avalanche Current <sup>1)</sup>	4	A
$E_{AR}$	Repetitive Avalanche Energy <sup>1)</sup>	0.4	mJ
$P_D$	Total Power Dissipation (@ $T_c=25^\circ C$ ) -Derate above $25^\circ C$	50	W
		0.4	W/ $^\circ C$
$T_J$	Junction Temperature	150	$^\circ C$
$T_{stg}$	Storage Temperature	-55~150	$^\circ C$
$I_S$	Continuous diode forward current	4	A
$I_{S,pulse}$	Diode pulse current	12	A

Notes:

- 1.Repetitive Rating:Pulse width limited by maximum Junction Temperature
2. $I_{AS}=2A, V_{DD}=60V, R_G=25\Omega, Starting T_J=25^\circ C$

### Thermal Characteristics

Symbol	Parameter	Value			Units
		Min	Typ	Max	
$R_{JC}$	Thermal Resistance , Junction -to -Case	-	-	2.5	$^\circ C/W$
$R_{JA}$	Thermal Resistance , Junction-to-Ambient	-	-	62	$^\circ C/W$

## Electrical Characteristics(Tc=25°C unless otherwise noted)

Characteristics	Symbol	Test Condition	Min	Type	Max	Unit
Gate leakage current	$I_{GSS}$	$V_{GS}=\pm 30V, V_{DS}=0V$	-	-	$\pm 100$	nA
Drain cut-off current	$I_{DSS}$	$V_{DS}=650, V_{GS}=0V,$ $T_j=25^\circ C$ $T_j=125^\circ C$	-	-	1	$\mu A$
			-	10	-	
Drain -source breakdown voltage	$V_{(BR)DSS}$	$I_D=250\mu A, V_{GS}=0V$	650	-	-	V
Gate threshold voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.5	3.5	4.5	V
Drain -source ON resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=2A$ $T_j=25^\circ C$ $T_j=150^\circ C$	-	0.83	0.93	$\Omega$
			-	1.9	-	
Gate resistance	$R_G$	f=1MHz, open drain	-	0.4	-	$\Omega$
Input capacitance	$C_{iss}$	$V_{DS}=25V,$ $V_{GS}=0V,$ f=1MHz	-	450		pF
Reverse transfer capacitance	$C_{rss}$		-	5		
Output capacitance	$C_{oss}$		-	300		
Turn-on delay time	$t_d(on)$	$V_{DD} = 300V, I_D = 2A$ $R_G = 12\Omega, V_{GS}=10V$	-	13	-	ns
Rise time	$t_r$		-	12	-	
Turn-off delay time	$t_d(off)$		-	31	-	
Fall time	$t_f$		-	9	-	
Gate to source charge	$Q_{gs}$	$V_{DD}=480V, I_D=2A,$ $V_{GS}=0$ to 10 V	-	3	-	nC
Gate to drain charge	$Q_{gd}$		-	6	-	
Gate charge total	$Q_g$		-	13	-	
Gate plateau voltage	$V_{plateau}$		-	5.8	-	

## Source-Drain Ratings and Characteristics(Ta=25°C)

Characteristics	Symbol	Test Condition	Min	Type	Max	Unit
Diode forward voltage	$V_{SD}$	$V_{GS}=0V, I_F=2A$	-	-	1.2	V
Reverse recovery time	$t_{rr}$	$V_R=50V, I_F=4A,$ dI <sub>F</sub> /dt=100 A/ $\mu$ s	-	220	-	ns
Reverse recovery charge	$Q_{rr}$		-	1.6	-	$\mu c$
Peak reverse recovery current	$I_{rrm}$		-	12	-	A

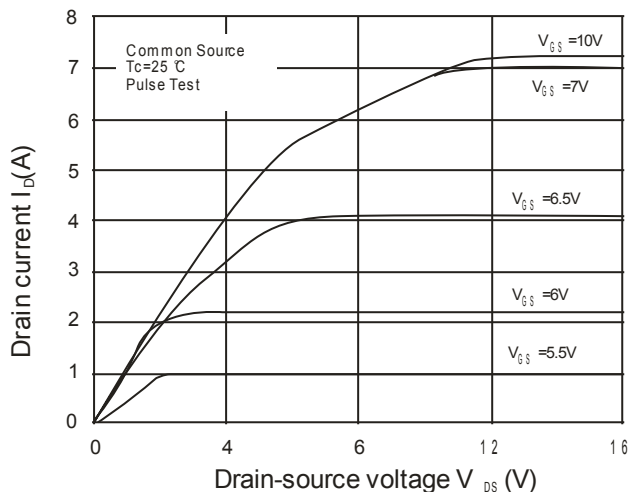


Fig.1 On-Region characteristics

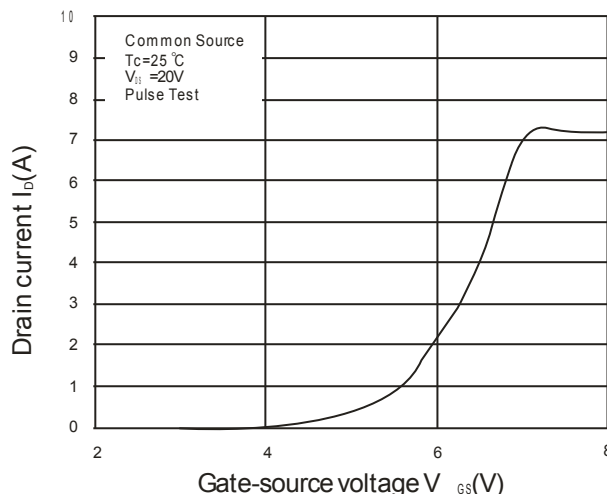


Fig.2 Transfer characteristics

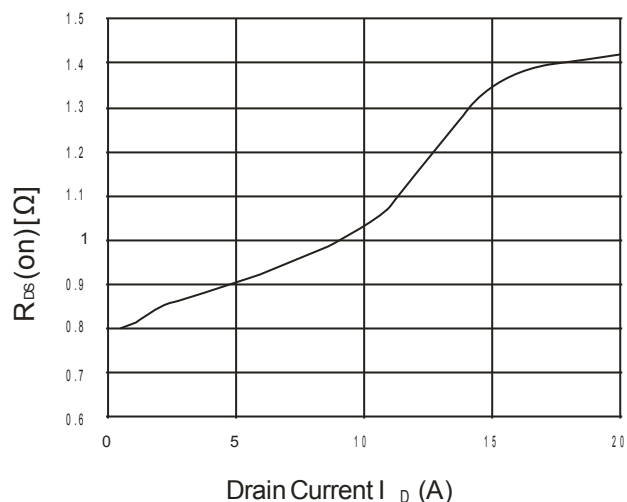


Fig.3 On-Resistance Variation vs Drain Current

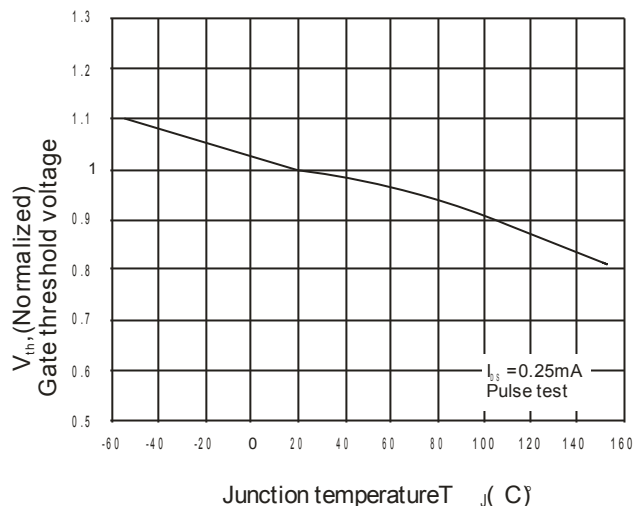


Fig.4 Threshold Voltage vs. Temperature

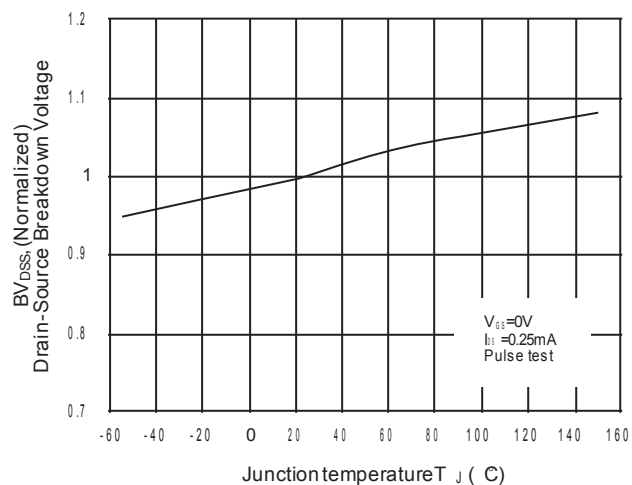


Fig.5 Breakdown Voltage vs. Temperature

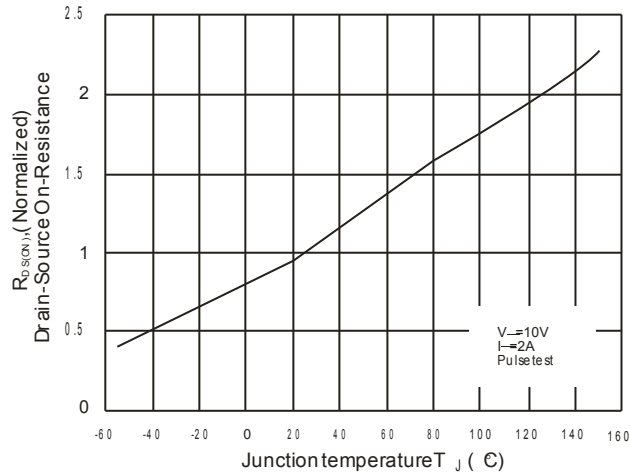


Fig.6 On-Resistance vs. Temperature

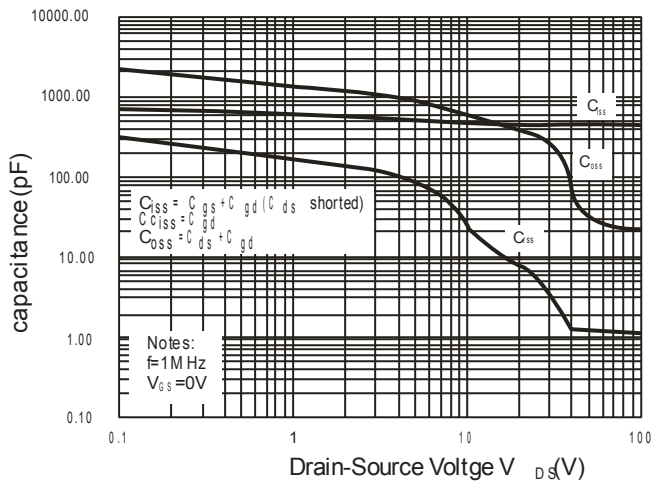


Fig.7 Capacitance Characteristics

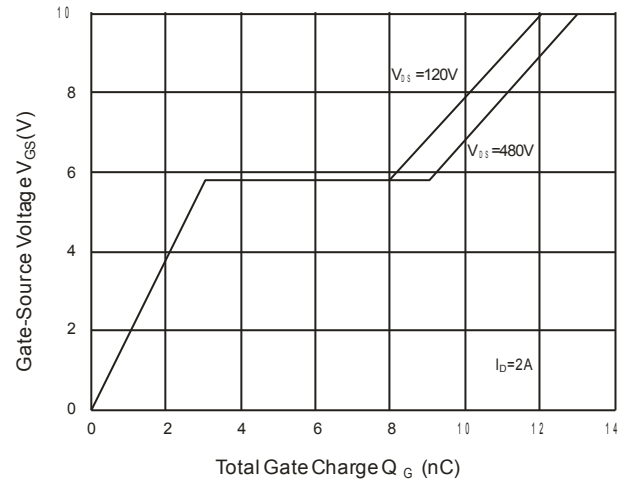


Fig.8 Gate Charge Characteristics

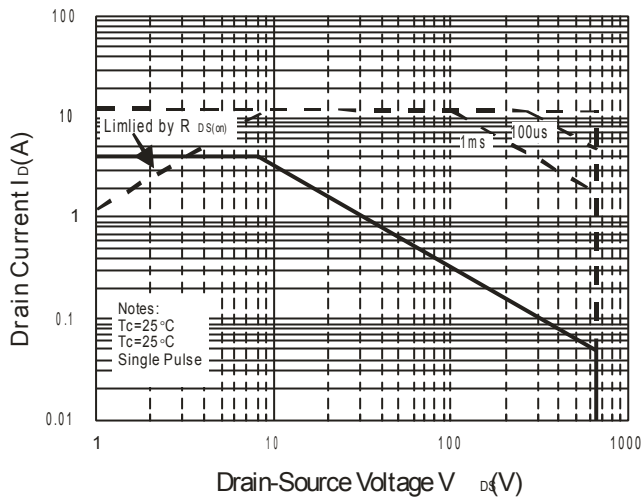


Fig.9 Maximum Safe Operation Area

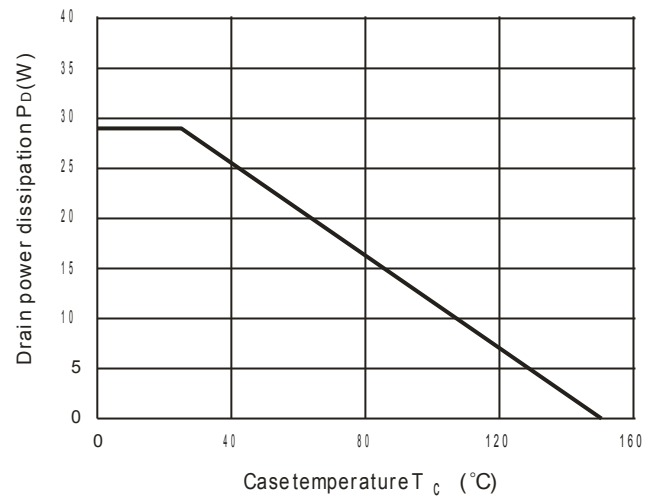


Fig.10 Power Dissipation vs. Temperature

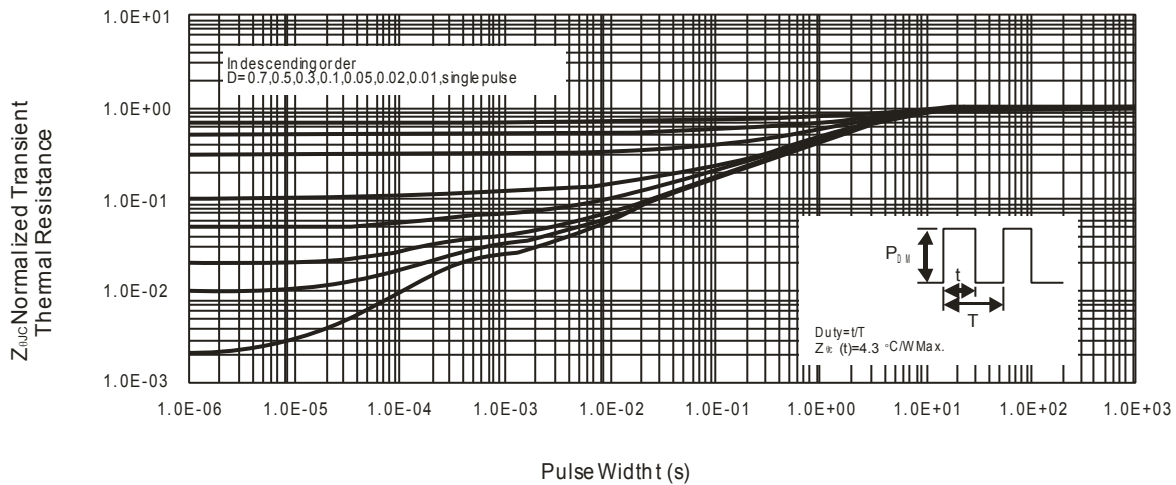


Fig.11 Transient Thermal Response Curve

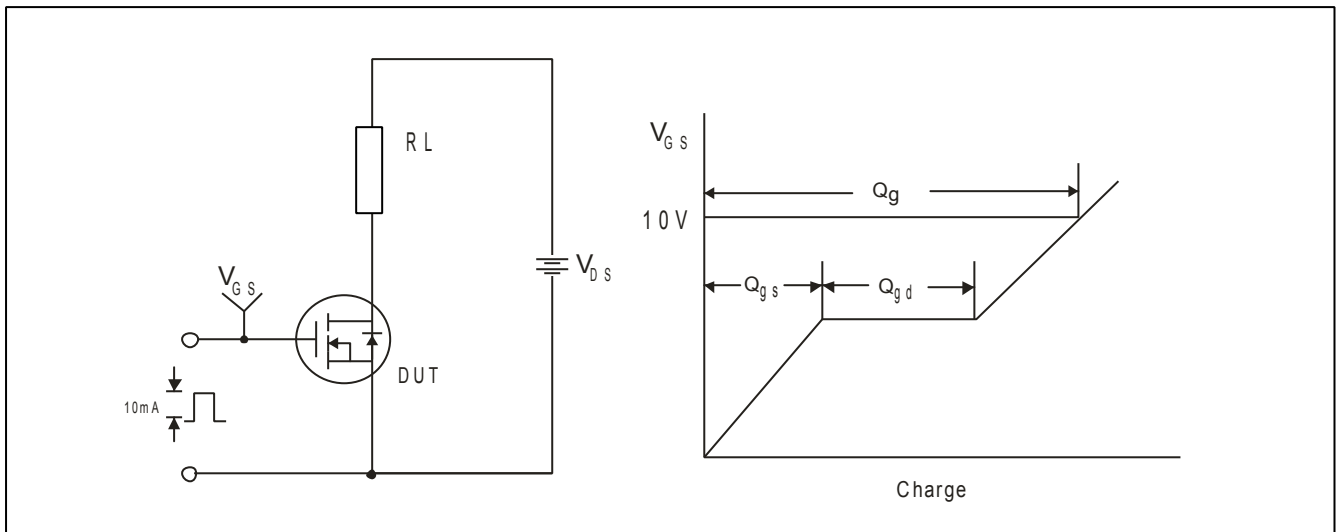


Fig.12 Gate Charge Test Circuit & Waveform

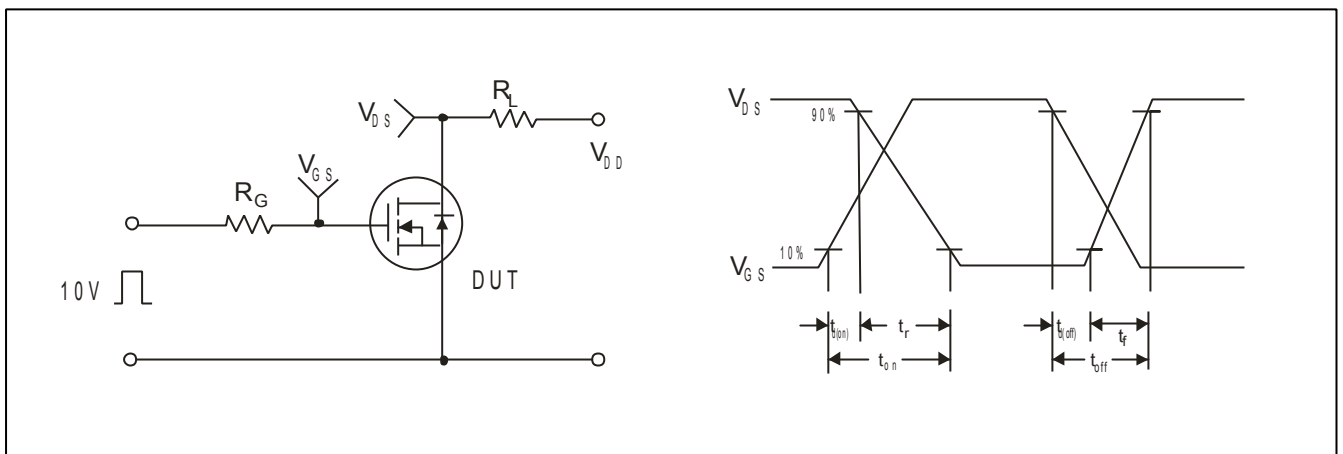


Fig.13 Switching Test Circuit & Waveforms

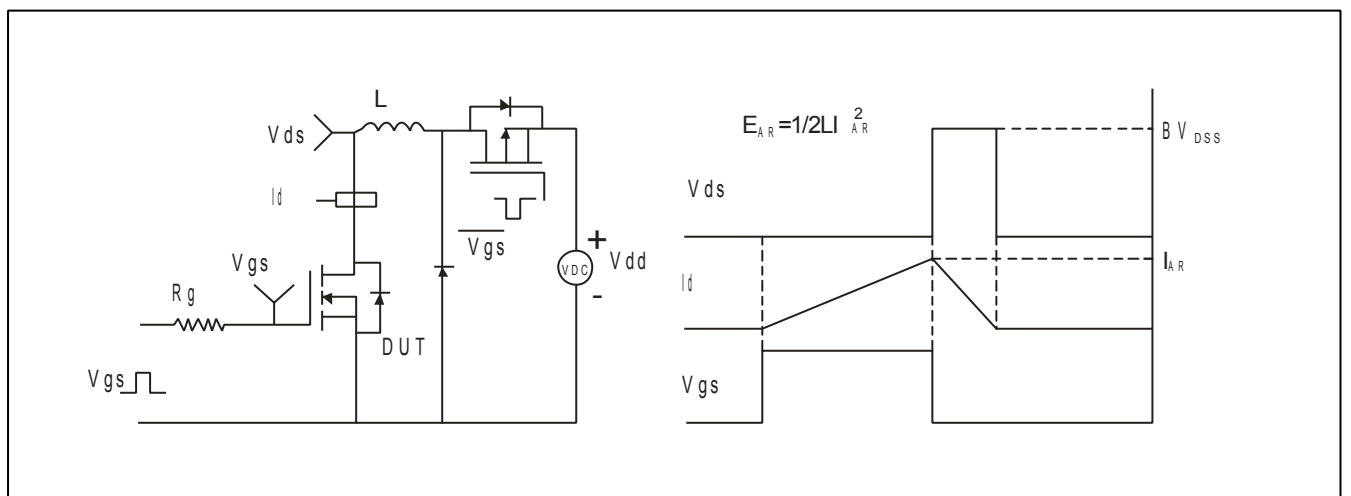
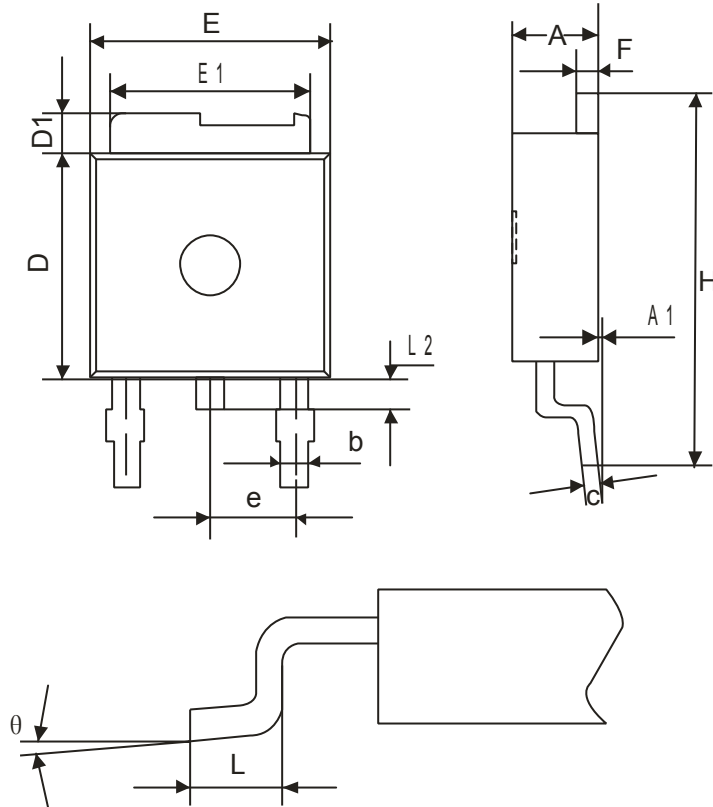


Fig.14 Unclamped Inductive Switching Test Circuit & Waveform

**DPAK Package Dimension**

Unit:mm



符号 symbol	MIN	MAX
A	2.19	2.38
A1	-	0.13
b	0.64	0.89
c	0.46	0.61
D	5.97	6.22
D1	0.89	1.27
E	6.35	6.73
E1	5.21	5.46
e	2.28TYP	
F	0.46	0.61
H	9.65	10.41
L	1.40	1.78
L2	0.64	1.01
θ	0°	8°

**NOTE:**

1. We strongly recommend customers check carefully on the trademark when buying our product, if there is any question, please don't be hesitate to contact us.
2. Please do not exceed the absolute maximum ratings of the device when circuit designing.
3. Winsemi Microelectronics Co., Ltd reserved the right to make changes in this specification sheet and is subject to change without prior notice.

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