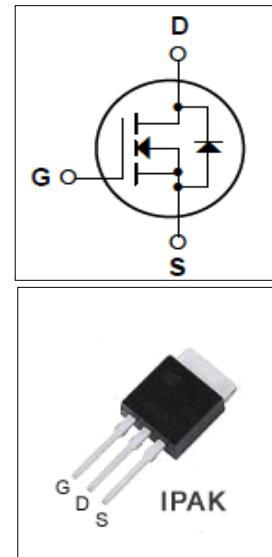


Silicon N-Channel MOSFET

Features

- 6A,700V, $R_{DS(on)}$ (Max 1.5Ω)@ $V_{GS}=10V$
- Ultra-low Gate Charge(Typical 51nC)
- High Current Capability
- 100%Avalanche Tested
- Maximum Junction Temperature Range(150°C)



General Description

This Power MOSFET is produced using Winsemi's advanced planar stripe. This latest technology has been especially designed to minimize on-state resistance, have a high rugged avalanche characteristics. This devices is specially well suited for high efficiency switch mode power supply, electronic Lamp ballasts based on half bridge and UPS.

Absolute Maximum Ratings

Symbol	Parameter	Value	Units
V_{DSS}	Drain Source Voltage	700	V
I_D	Continuous Drain Current(@ $T_c=25^\circ C$)	6	A
	Continuous Drain Current(@ $T_c=100^\circ C$)	3.8	A
I_{DM}	Drain Current Pulsed $t_p=300\mu s$	24	A
V_{GS}	Gate to Source Voltage-Continuous	± 30	V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	582	mJ
I_S	Source Current (Body Diode)	6	A
P_D	Total Power Dissipation(@ $T_c=25^\circ C$)	120	W
T_J, T_{stg}	Junction and Storage Temperature	-55~150	°C
T_L	Channel Temperature(1/8" from case for 10s)	260	°C

Thermal Characteristics

Symbol	Parameter	Value			Units
		Min	Typ	Max	
R_{QJC}	Thermal Resistance, Junction-to-Case	-	0.96	1.04	°C/W
R_{QJA}	Thermal Resistance, Junction-to-Ambient	-	62.5		°C/W

Electrical Characteristics ($T_c = 25^\circ\text{C}$)

Characteristics	Symbol	Test Condition	Min	Type	Max	Unit
Gate leakage current	I_{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	-	-	± 100	nA
Gate-source breakdown voltage	$V_{(BR)GSS}$	$I_G = 250 \mu\text{A}, V_{DS} = 0 \text{ V}$	± 30	-	-	V
Drain cut-off current	I_{DSS}	$V_{DS}=700\text{V}, V_{GS}=0\text{V}, T_c = 25^\circ\text{C}$	-	-	10	μA
Drain-source breakdown voltage	$V_{(BR)DSS}$	$I_D = 250 \mu\text{A}, V_{GS} = 0 \text{ V}$	700	-	-	V
Break Voltage Temperature Coefficient	$\Delta V_{DSS}/\Delta T_J$	$I_D=250\mu\text{A}, \text{Referenced to } 25^\circ\text{C}$	-	0.79	-	$\text{mV/}^\circ\text{C}$
Gate threshold voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	2	-	4	V
Drain-source ON resistance	$R_{DS(\text{ON})}$	$V_{GS} = 10 \text{ V}, I_D = 12\text{A}$	-	-	1.5	Ω
Forward Transconductance	g_{fs}	$V_{DS} = 50 \text{ V}, I_D = 3\text{A}$	-	4.12	-	S
Input capacitance	C_{iss}	$V_{DS} = 25 \text{ V},$ $V_{GS} = 0 \text{ V},$ $f = 1 \text{ MHz}$	-	920	1200	pF
Reverse transfer capacitance	C_{rss}		-	45	55	
Output capacitance	C_{oss}		-	100	115	
Switching time	Rise time	t_r	$V_{DD} = 350$ $I_D = 6\text{A}$ $R_G = 11.5\Omega$	-	23	55
	Turn-on time	t_{on}		-	18	45
	Fall time	t_f		-	26	60
	Turn-off time	t_{off}		-	76	160
Total gate charge (gate-source plus gate-drain)	Q_g	$V_{DS} = 560\text{V}$ $V_{GS} = 10\text{V}$ $I_D = 6\text{A}$	-	51	67	nC
Gate-source charge	Q_{gs}		-	8.3	-	
Gate-drain ("miller") Charge	Q_{gd}		-	23.1	-	

Source-Drain Ratings and Characteristics ($T_a = 25^\circ\text{C}$)

Characteristics	Symbol	Test Condition	Min	Type	Max	Unit
Continuous drain reverse current	I_{DR}	-	-	-	6	A
Pulse drain reverse current	I_{DRP}	-	-	-	24	A
Forward voltage (diode)	V_{DSF}	$I_{DR} = 6\text{A}, V_{GS} = 0 \text{ V}$	-	-	1.5	V
Reverse recovery time	t_{rr}	$I_{DR} = 6\text{A}, V_{GS} = 0 \text{ V},$ $dI_{DR} / dt = 100 \text{ A} / \mu\text{s}$	-	440	-	ns
Reverse recovery charge	Q_{rr}		-	4.05	-	μC

- Note
1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
 2. Repetitive Rating: Pulse width limited by maximum junction temperature
 3. $L = 30\text{mH}, I_{AS} = 6\text{A}, V_{DD} = 50\text{V}, R_G = 27\Omega, \text{Starting } T_J = 25^\circ\text{C}$
 4. $I_{SD} \leq 6\text{A}, di/dt \leq 140\text{A}/\mu\text{s}, V_{DD} \leq BV_{DSS}, \text{Starting } T_J = 25^\circ\text{C}$

TYPICAL PERFORMANCE CURVES

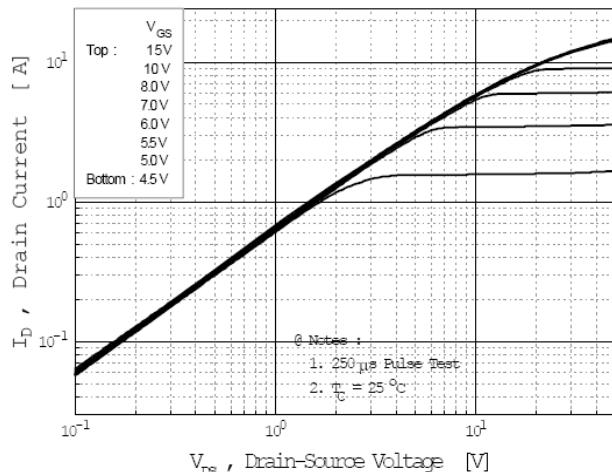


Fig 1.Output Characteristics

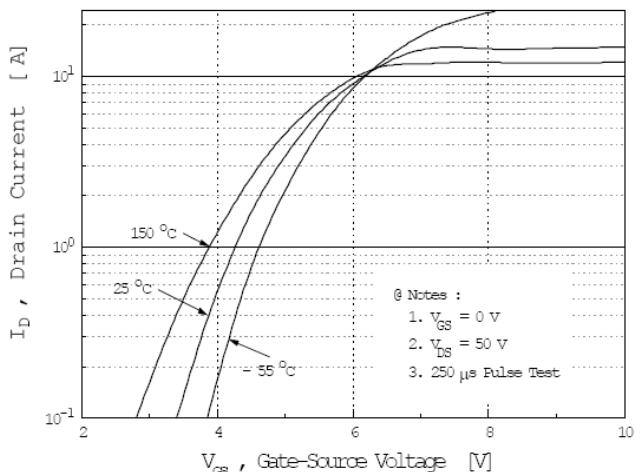


Fig 2.Transfer Characteristics

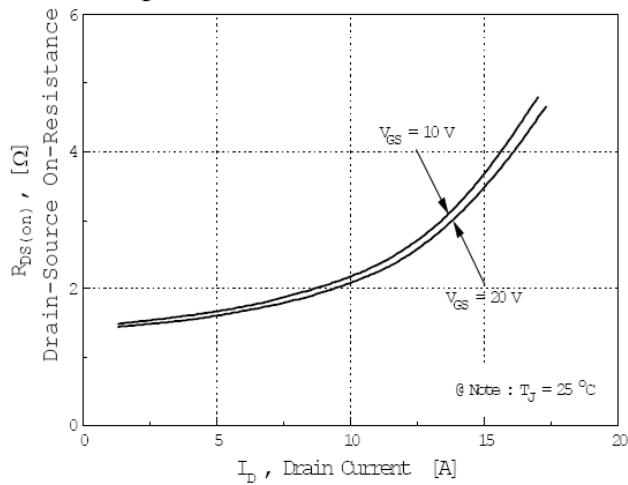


Fig 3.On-Resistance vs. Drain Current

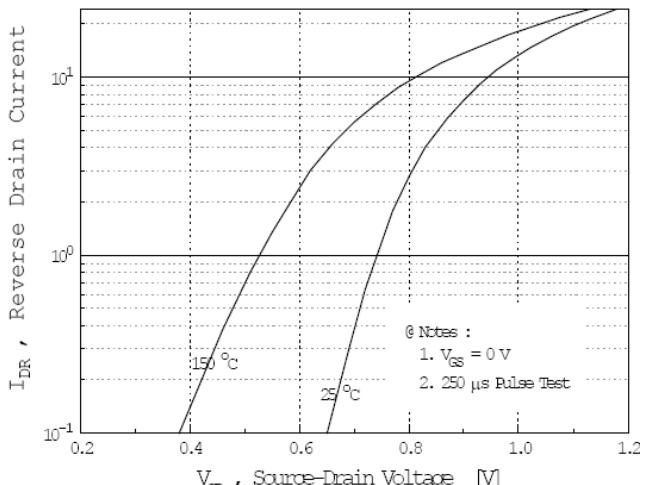


Fig 4.Source- Drain Diode Forward Voltage

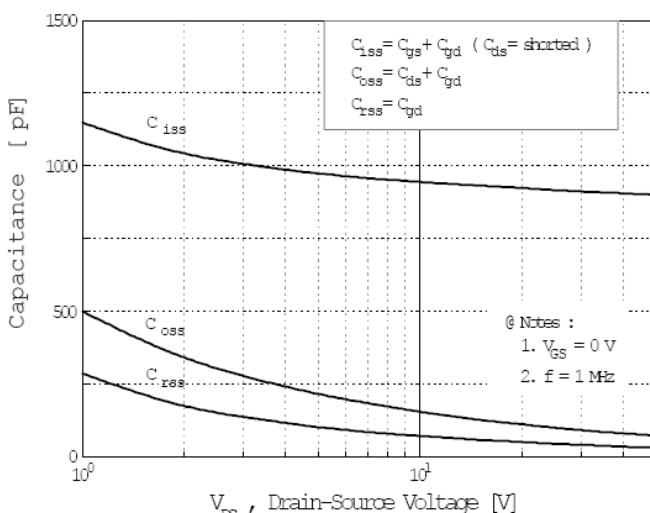


Fig 5.Capacitance vs. Drain-Source Voltage

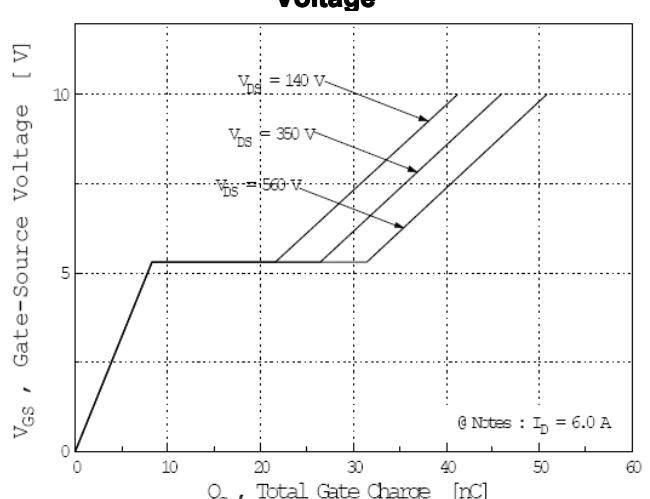


Fig 6.Gate Charge vs.Gate-Source Voltage

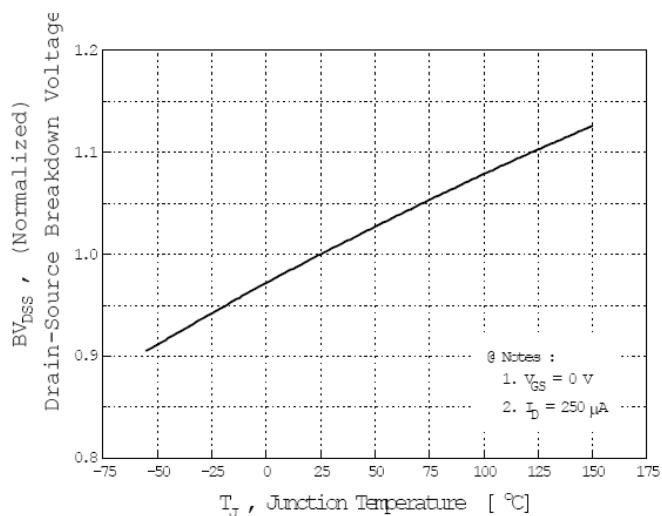


Fig 7. Breakdown Voltage vs. Temperature

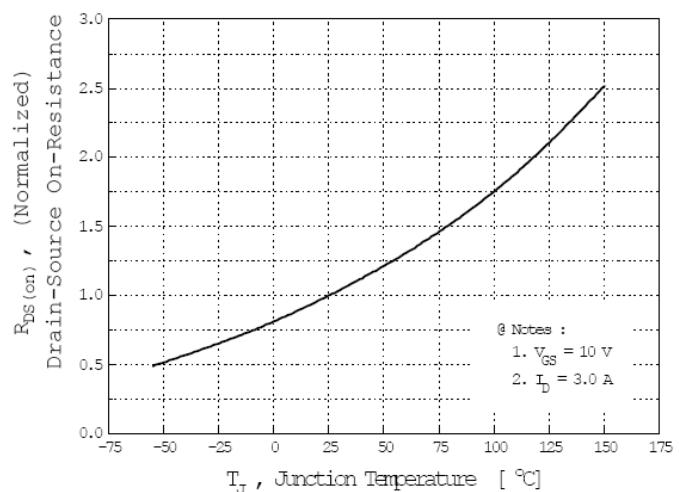


Fig 8. On-Resistance vs. Temperature

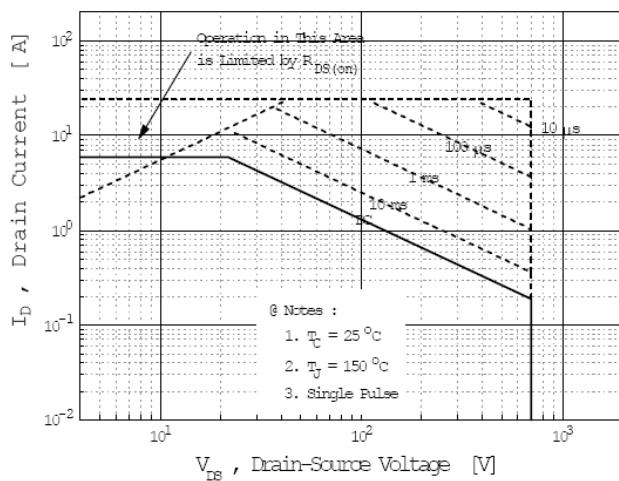


Fig 9. Max. Safe Operating Area

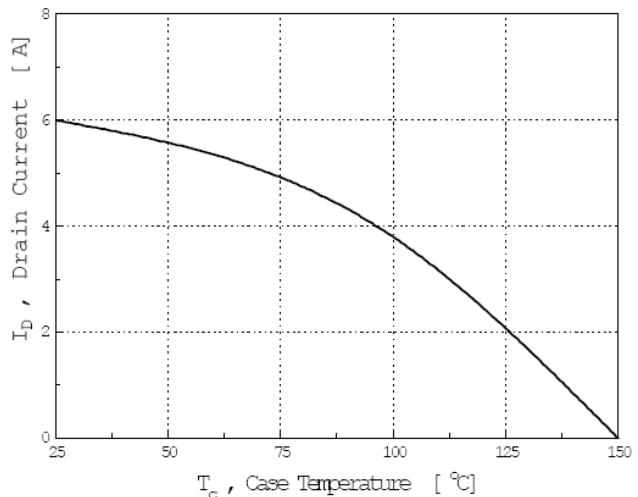


Fig 10. Max. Drain Current vs. Case Temperature

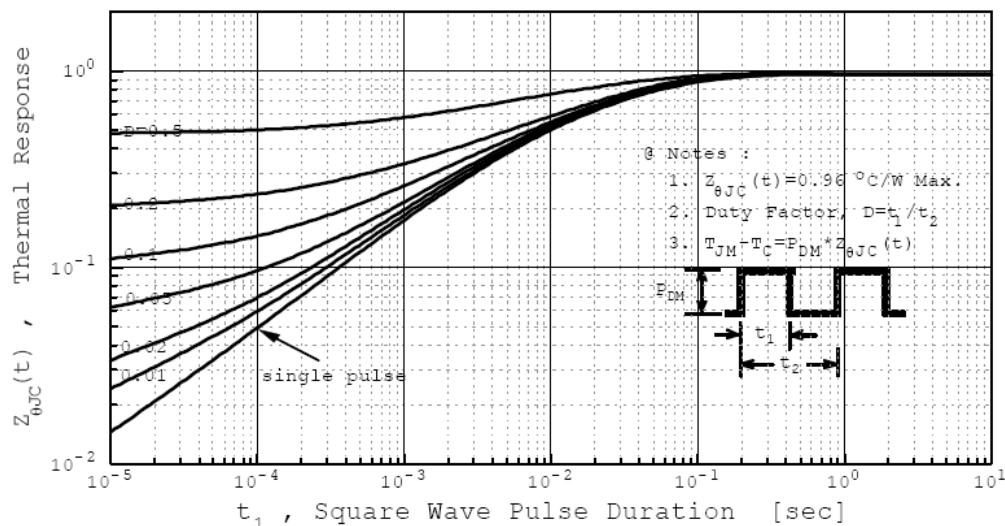
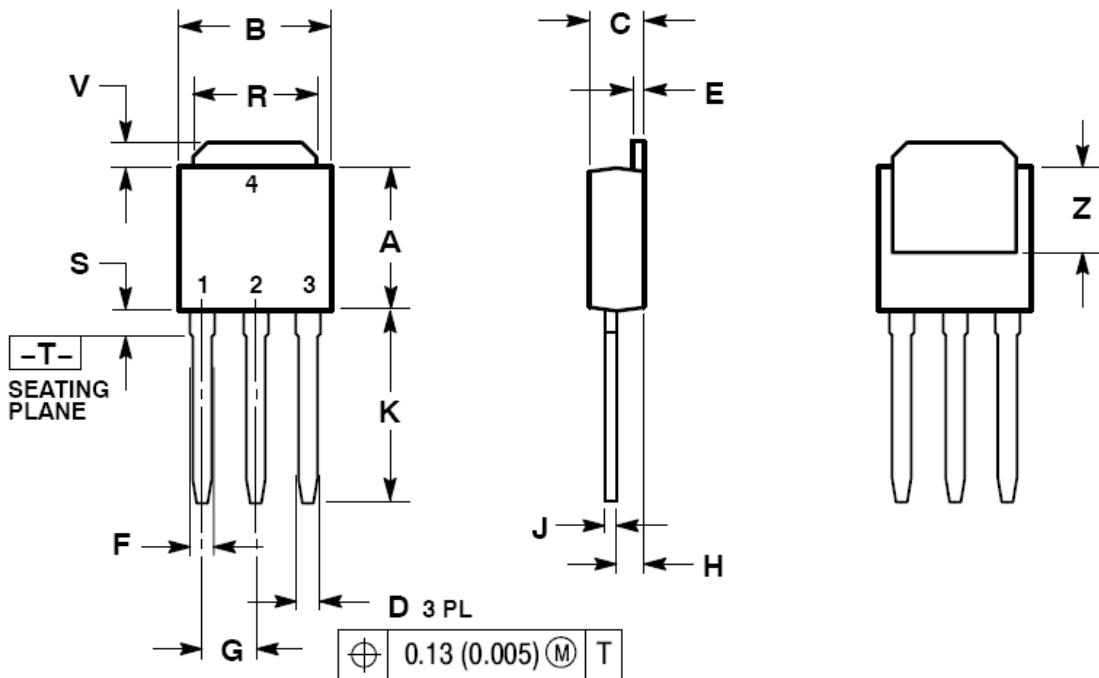


Fig 11. Thermal Response

TO-251 Package Dimension



DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.235	0.245	5.97	6.35
B	0.250	0.265	6.35	6.73
C	0.086	0.094	2.19	2.38
D	0.027	0.035	0.69	0.88
E	0.018	0.023	0.46	0.58
F	0.037	0.045	0.94	1.14
G	0.090 BSC		2.29 BSC	
H	0.034	0.040	0.87	1.01
J	0.018	0.023	0.46	0.58
K	0.350	0.380	8.89	9.65
R	0.180	0.215	4.45	5.45
S	0.025	0.040	0.63	1.01
V	0.035	0.050	0.89	1.27
Z	0.155	---	3.93	----

STYLE 2:

- PIN 1. GATE
- 2. DRAIN
- 3. SOURCE
- 4. DRAIN