SPECIFICATION

MODULE NO.:　　　WG320240CX

General Specification

Item	Dimension	Unit
Number of dots	320 x 240	—
Module dimension	148.02x 120.24x 15.6 (MAX)	mm
View area	120.14 x 92.14	mm
Active area	115.18 x 86.38	mm
Dot size	0.34 x 0.34	mm
Dot pitch	0.36 x 0.36	mm
Duty	1/240	
Backlight Type	LED	
IC	S1D13700	
Interface	8080	

Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	T_{OP}	-20	—	+70	℃
Storage Temperature	T_{ST}	-30	—	+80	℃
Input Voltage	V_{IN}	-0.3	—	V_{DD}+0.5	V
Supply Voltage For Logic	V_{DD}-V_{SS}	0	—	6.5	V
Supply Voltage For LCD	V_{DD}-V_0	0	—	32	V

Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For Logic	V_{DD}-V_{SS}	—	4.5	5.0	5.5	V
Supply Voltage For LCD	V_{DD}-V_0	Ta=-20℃	—	—	—	V
		Ta=25℃	—	—	—	V
		Ta=70℃	—	—	—	V
Input High Volt.	V_{IH}	—	3.5	—	—	V
Input Low Volt.	V_{IL}	—	—	—	1.0	V
Output High Volt.	V_{OH}	—	V_{DD}-0.4	—	—	V
Output Low Volt.	V_{OL}	—	—	—	0.4	V
Supply Current	I_{DD}	V_{DD}=5.0V	65.0	75.0	85.0	mA

Interface Pin Function

Pin No.	Symbol	Level	Description
1	V_{SS}	0V	Ground
2	V_{DD}	5.0V	Power supply for Logic
3	NC	—	No connection
4	/RD	H/L	8080 family: Read signal, 6800 family: Enable clock
5	/WR	H/L	8080 family: Write signal, 6800 family: R/W signal
6	A0	H/L	RD=L WR=H. A0=L: Data Read A0=H: Status Read RD=H WR=L.A0=L: Data Write A0=H: Command Write
7~14	DB0~DB7	H/L	Data bus line
15	$\overline{CS}$	H/L	Chip select ,Active L
16	$\overline{RES}$	H/L	Controller reset signal, Active L
17	V_{EE}	—	Negative voltage output
18	FGND	—	Frame Ground
19	NC	—	No Connection
20	WAIT	—	Check busy

Contour Drawing
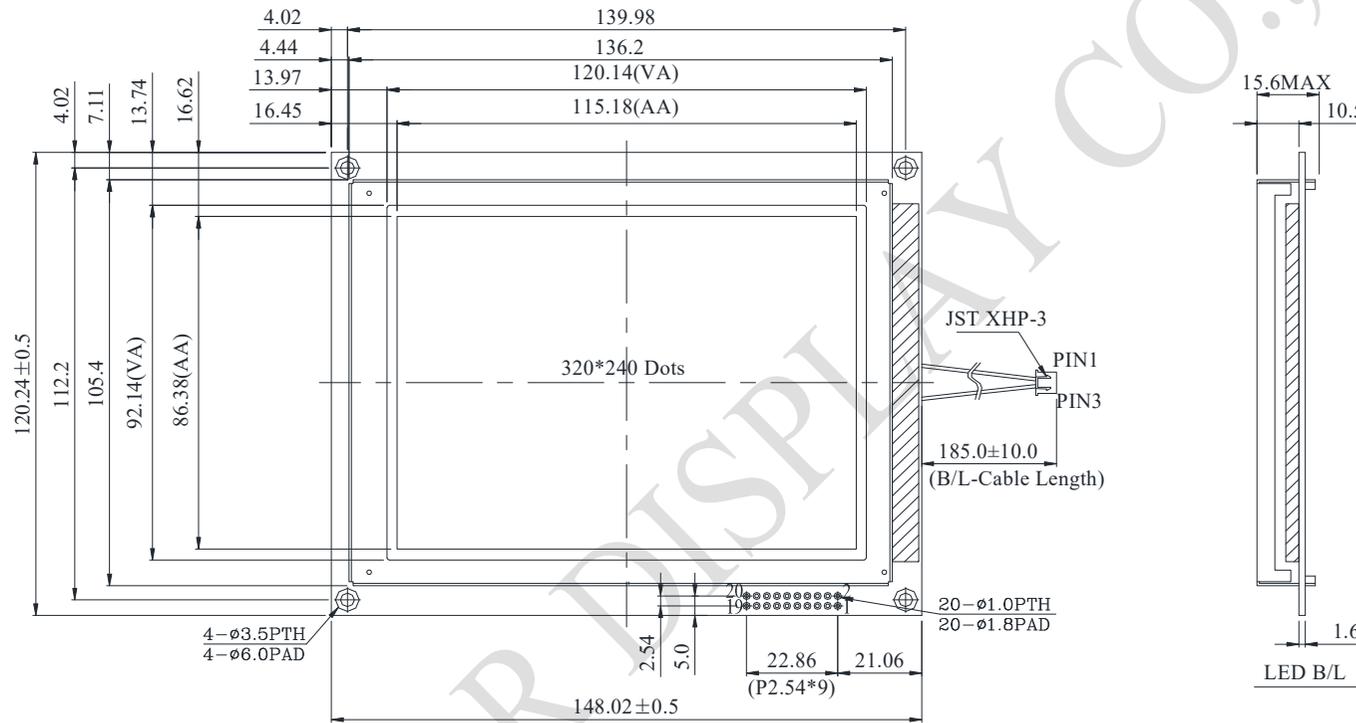
PIN NO.	SYMBOL
1	Vss
2	Vdd
3	NC
4	$\overline{RD}$
5	$\overline{WR}$
6	A0
7	DB0
8	DB1
9	DB2
10	DB3
11	DB4
12	DB5
13	DB6
14	DB7
15	$\overline{CS}$
16	$\overline{RES}$
17	Vee
18	FG
19	NC
20	WAIT

The non-specified tolerance of dimension is ±0.3mm.

DOT SIZE
SCALE 10/1