

1. General description

WG50N65HDJ2 uses advanced Fine Trench Field-stop IGBT technology with anti-parallel diode in TO3PF package to provide extremely low $V_{CE(sat)}$, and excellent switching performance. This device offers Best-in-Class efficiency in hard switching and resonant topology.



2. Features and benefits

- Maximum junction temperature 175 °C
- Positive temperature efficient for easy paralleling
- Very soft, fast recovery anti-parallel diode
- High speed switching
- EMI Improved Design

3. Applications

- PFC
- Solar converters
- UPS
- Welding Converters
- Mid to high range switching frequency converters

4. Quick reference data

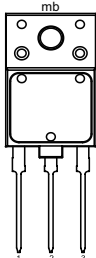
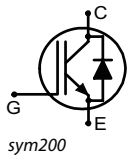
Table 1. Quick reference data

Symbol	Parameter	Notes	Value			Unit	
V_{CE}	Collector-emitter voltage, $T_j \geq 25\text{ °C}$		650			V	
I_C	DC collector current, limited by $T_{j(max)}^{(1)}$ $T_C = 100\text{ °C}$		50			A	
Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
Static characteristics							
$V_{CE(sat)}$	Collector-emitter saturation voltage	$V_{GE} = 15\text{ V}; I_C = 50\text{ A}; T_j = 25\text{ °C}$		1.1	1.6	2.1	V

Note(1): I_C and other electrical parameters definition follow TO247 package.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		 sym200
2	C	collector		
3	E	emitter		
mb	n.c.	mounting base; isolated		

6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WG50N65HDJ2	TO3PF	WG50N65HDJ2Q	Tube	30	SOT1293	01-Mar-2017

7. Marking

Table 4. Marking codes

Type number	Marking codes
WG50N65HDJ2	WG50N 65HDJ2

8. Limiting values

Table 5. Limiting values

Symbol	Parameter	Notes	Value	Unit
V_{CE}	Collector-emitter voltage, $T_j \geq 25\text{ °C}$		650	V
I_C	DC collector current, limited by $T_{j(max)}$ $T_C = 25\text{ °C}$ $T_C = 100\text{ °C}$		100 50	A
$I_{C(puls)}$	Pulsed collector current, t_p limited by $T_{j(max)}$		150	A
-	Turn off safe operating area $V_{CE} \leq 650\text{ V}$, $T_j \leq 175\text{ °C}$, $t_p = 1\text{ }\mu\text{s}$		150	A
I_F	Diode forward current, limited by $T_{j(max)}$ $T_C = 25\text{ °C}$ $T_C = 100\text{ °C}$		20 10	A
$I_{F(puls)}$	Diode pulsed current, t_p limited by $T_{j(max)}$		40	A
V_{GE}	Gate-emitter voltage		± 20	V
P_{tot}	Power dissipation $T_C = 25\text{ °C}$ Power dissipation $T_C = 100\text{ °C}$		94 47	W
T_{stg}	Storage temperature		-55 to 150	°C
T_{jmax}	Maximum operating junction temperature		175	°C
-	Peak soldering temperture		260	°C
M	Mounting Torque with washer		0.55	Nm

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
$R_{th(j-c)}$	IGBT thermal resistance from junction to case			-	1.6	-	K/W
$R_{th(j-c)}$	Diode thermal resistance from junction to case			-	3.7	-	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient			-	40	-	K/W

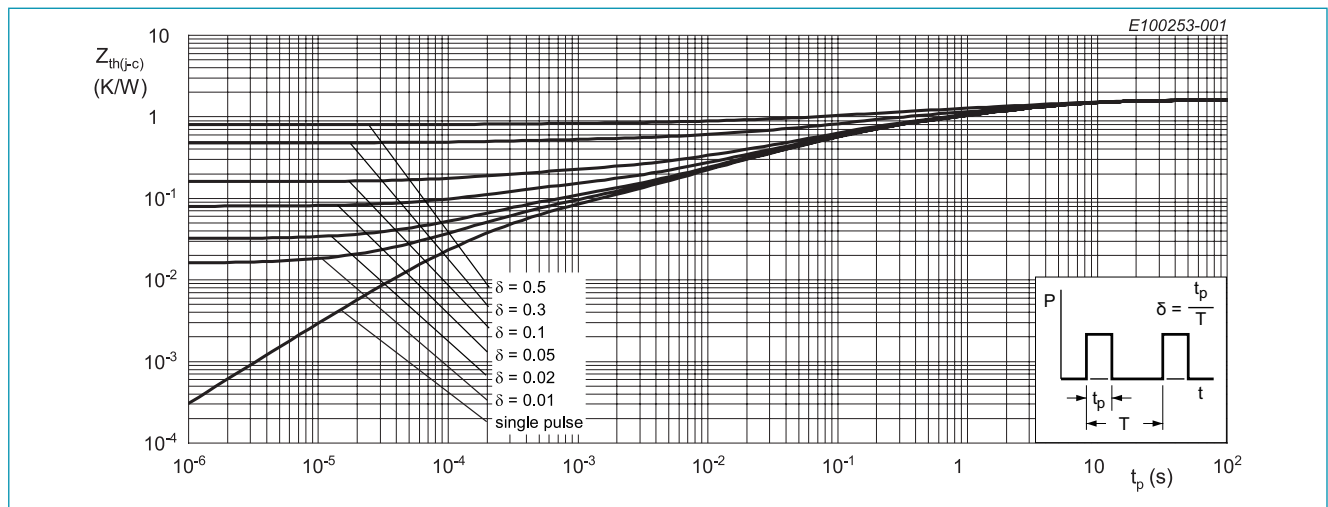


Fig. 1. Transient thermal impedance from junction to case as a function of pulse duration; IGBT

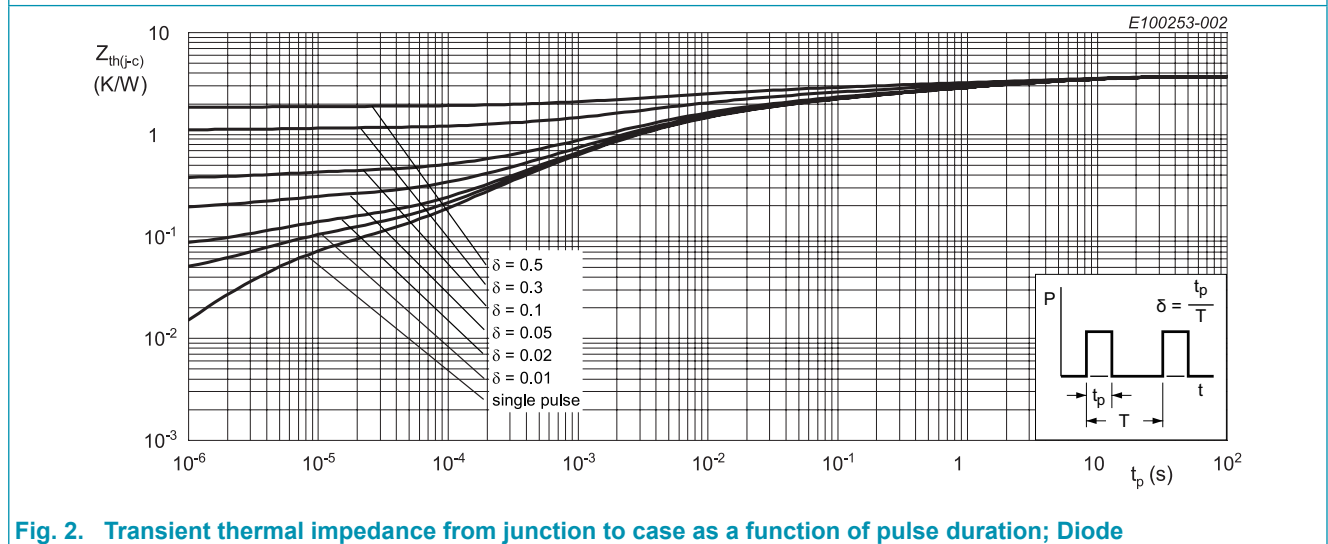


Fig. 2. Transient thermal impedance from junction to case as a function of pulse duration; Diode

10. Characteristics

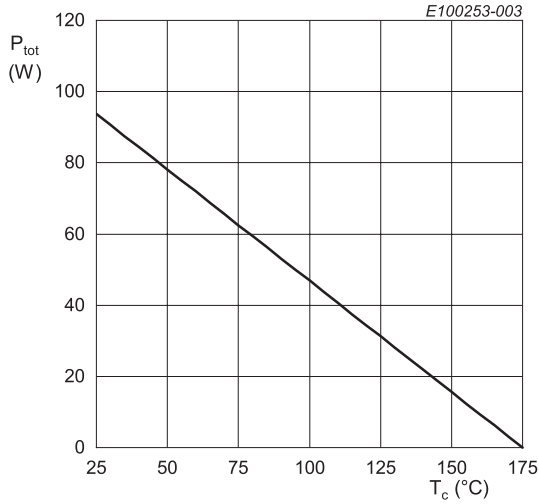
Table 7. Characteristics

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
Static characteristics							
BV_{CES}	Collector-emitter breakdown voltage	$V_{GE} = 0\text{ V}; I_C = 1.0\text{ mA}$		650	-	-	V
$V_{CE(sat)}$	Collector-emitter saturation voltage	$V_{GE} = 15\text{ V}; I_C = 50\text{ A}; T_j = 25\text{ °C}$		1.1	1.6	2.1	V
		$V_{GE} = 15\text{ V}; I_C = 50\text{ A}; T_j = 175\text{ °C}$		-	1.95	-	V
V_F	Diode forward voltage	$V_{GE} = 0\text{ V}; I_F = 10\text{ A}; T_j = 25\text{ °C}$		-	1.9	-	V
		$V_{GE} = 0\text{ V}; I_F = 10\text{ A}; T_j = 175\text{ °C}$		-	1.45	-	V
$V_{GE(th)}$	Gate-emitter threshold voltage	$I_C = 500\text{ uA}; V_{CE} = V_{GE}$		3.6	4.5	5.4	V
I_{CES}	Zero gate voltage collector current	$V_{CE} = 650\text{ V}; V_{GE} = 0\text{ V}; T_j = 25\text{ °C}$		-	-	100	uA
		$V_{CE} = 650\text{ V}; V_{GE} = 0\text{ V}; T_j = 175\text{ °C}$		-	-	1	mA
g_{fs}	Transconductance	$V_{CE} = 20\text{ V}; I_C = 50\text{ A}$		-	47	-	S
Dynamic characteristics							
C_{ies}	Input capacitance	$V_{CE} = 30\text{ V}; V_{GE} = 0\text{ V}; f = 1\text{ MHz}; T_j = 25\text{ °C}$		-	2955	-	pF
C_{oes}	Output capacitance			-	105	-	pF
C_{res}	Reverse transfer capacitance			-	41	-	pF
Q_G	Gate charge	$V_{CC} = 520\text{ V}; I_C = 50\text{ A}; V_{GE} = 15\text{ V}; T_j = 25\text{ °C}$		-	138	-	nC

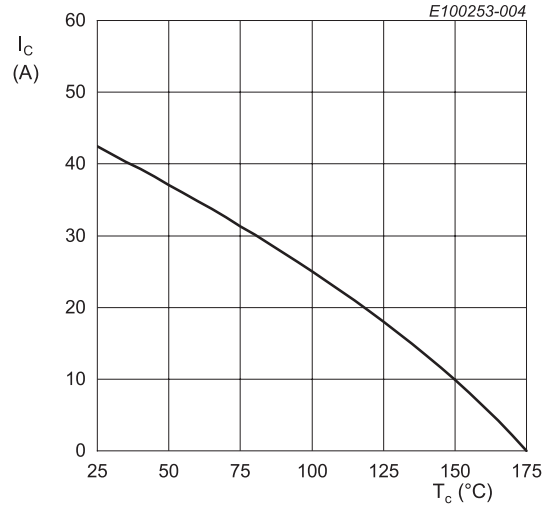
11. Switching Characteristics

Table 8. Switching Characteristics, Inductive Load

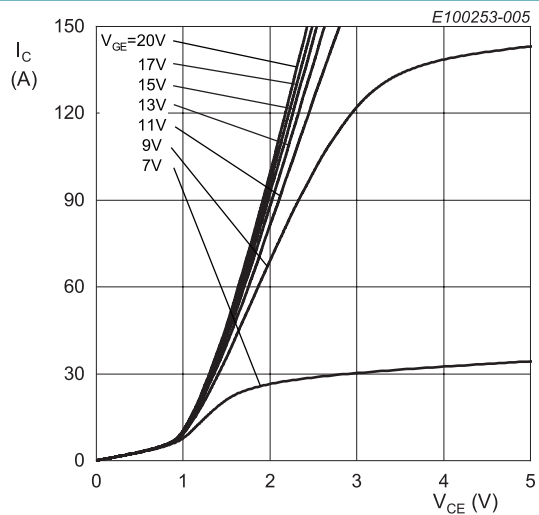
Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
IGBT characteristics							
$t_{d(on)}$	Turn-on delay time	$T_J = 25\text{ }^\circ\text{C};$ $V_{CC} = 400\text{ V}; I_C = 50\text{ A}; V_{GE} = 15\text{V} / 0\text{V};$ $R_G = 10\text{ ohm}$		-	35	-	nS
t_r	Rise time			-	40	-	nS
$t_{d(off)}$	Turn-off delay time			-	188	-	nS
t_f	Fall time			-	38	-	nS
E_{on}	Turn-on energy			-	1.1	-	mJ
E_{off}	Turn-off energy			-	0.6	-	mJ
E_{ts}	Total switching energy			-	1.7	-	mJ
$t_{d(on)}$	Turn-on delay time	$T_J = 175\text{ }^\circ\text{C};$ $V_{CC} = 400\text{ V}; I_C = 50\text{ A}; V_{GE} = 15\text{V} / 0\text{V};$ $R_G = 10\text{ ohm}$		-	33	-	nS
t_r	Rise time			-	39	-	nS
$t_{d(off)}$	Turn-off delay time			-	214	-	nS
t_f	Fall time			-	34	-	nS
E_{on}	Turn-on energy			-	1.45	-	mJ
E_{off}	Turn-off energy			-	0.7	-	mJ
E_{ts}	Total switching energy			-	2.15	-	mJ
Diode characteristics							
t_{rr}	Reverse recovery time	$T_J = 25\text{ }^\circ\text{C};$ $V_R = 400\text{ V}; I_F = 10\text{ A}; dI_F/dt = 500\text{A/us}$		-	32	-	nS
Q_r	Reverse recovery charge			-	148	-	nC
I_{RM}	Reverse recovery peak current			-	8	-	A
t_{rr}	Reverse recovery time	$T_J = 175\text{ }^\circ\text{C};$ $V_R = 400\text{ V}; I_F = 10\text{ A}; dI_F/dt = 500\text{A/us}$		-	71	-	nS
Q_r	Reverse recovery charge			-	508	-	nC
I_{RM}	Reverse recovery peak current			-	12	-	A



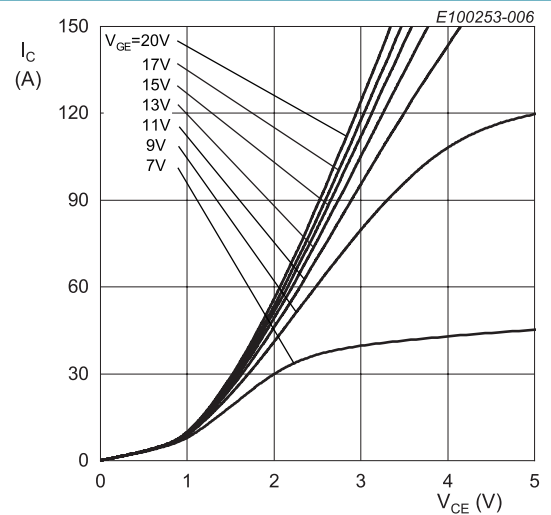
$T_j \leq 175$ °C
Fig. 3. Power dissipation as a function of case temperature



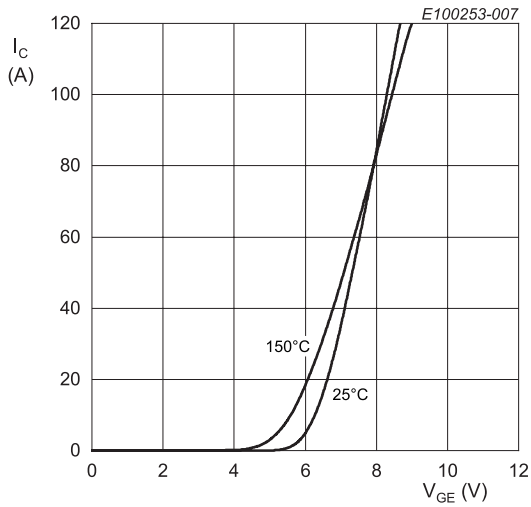
$V_{GE} \geq 15$ V; $T_j \leq 175$ °C
Fig. 4. Collector current as a function of case temperature



$T_j = 25$ °C
Fig. 5. Typical output characteristic

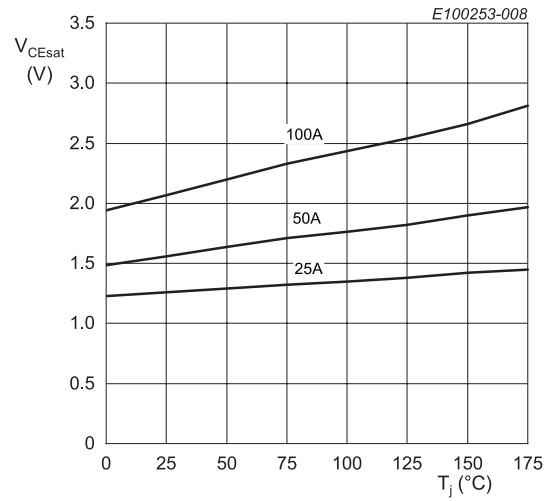


$T_j = 175$ °C
Fig. 6. Typical output characteristic



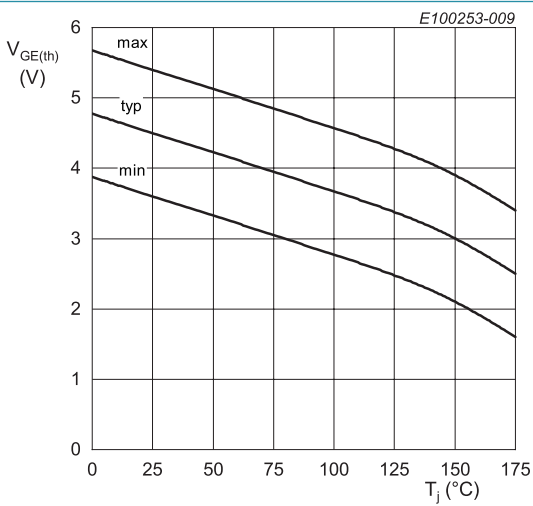
$V_{CE} = 20 V$

Fig. 7. Typical transfer characteristic



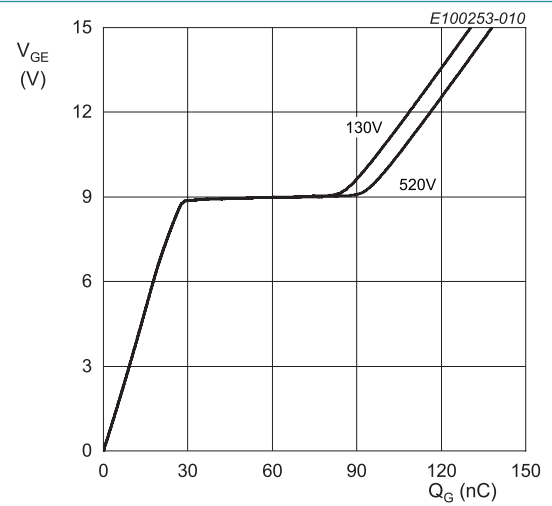
$V_{GE} = 15 V$

Fig. 8. Typical collector-emitter saturation voltage as a function of junction temperature



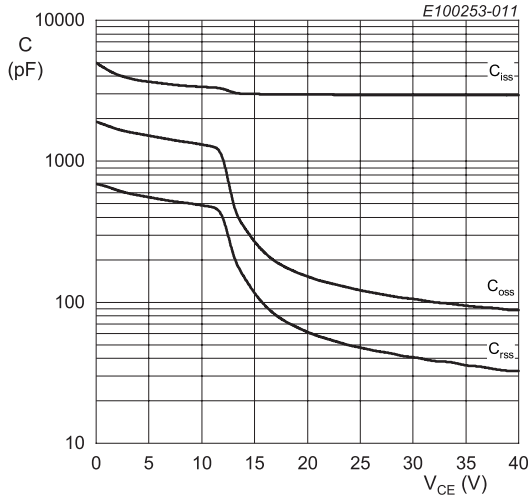
$I_C = 500 \mu A$

Fig. 9. Gate-emitter threshold voltage as a function of junction temperature



$I_C = 50 A$

Fig. 10. Typical gate charge



$V_{GE} = 0\text{ V}; f = 1\text{ MHz}$

Fig. 11. Typical capacitance as a function of collector-emitter voltage

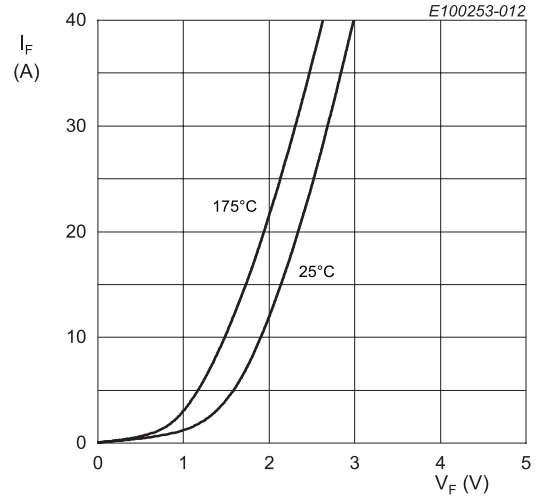
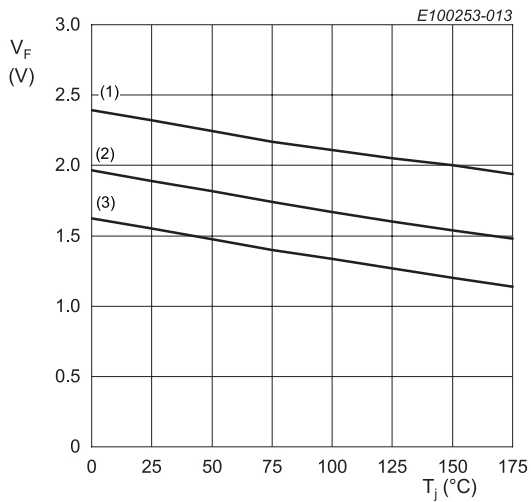
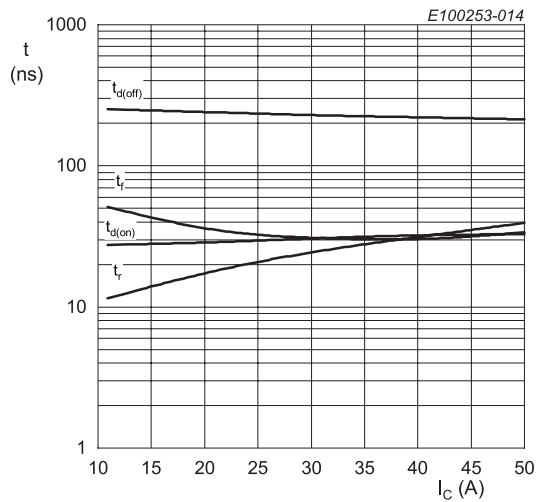


Fig. 12. Typical diode forward current as a function of forward voltage



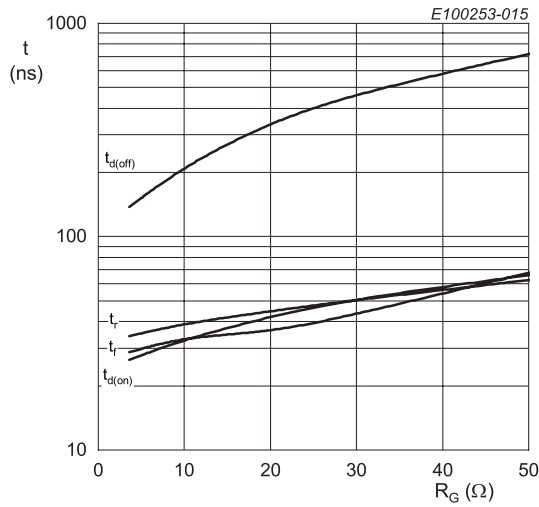
- (1) $I_F = 20\text{ A}$
- (2) $I_F = 10\text{ A}$
- (3) $I_F = 5\text{ A}$

Fig. 13. Typical diode forward voltage as a function of junction temperature



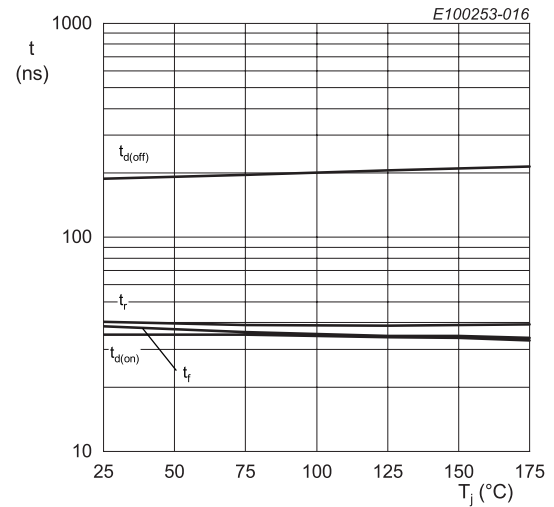
$R_g = 10\ \Omega; V_{GE} = 15\text{ V} / 0\text{ V}; T_J = 175\text{ }^\circ\text{C};$
 $V_{CE} = 400\text{ V};$ inductive load

Fig. 14. Typical switching times as a function of collector current



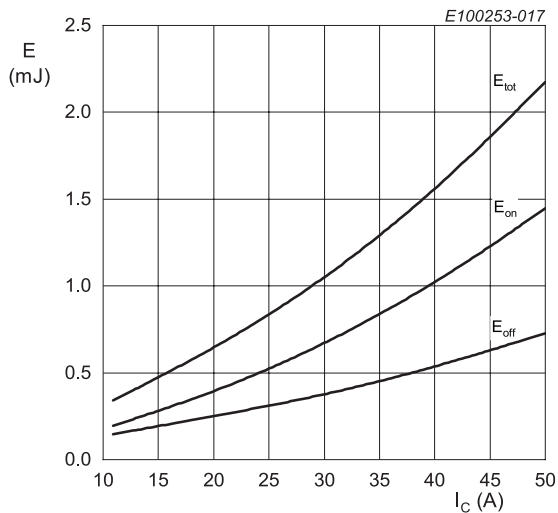
$I_C = 50 \text{ A}; V_{GE} = 15 \text{ V} / 0 \text{ V}; T_j = 175 \text{ }^\circ\text{C};$
 $V_{CE} = 400 \text{ V};$ inductive load

Fig. 15. Typical switching times as a function of gate resistance



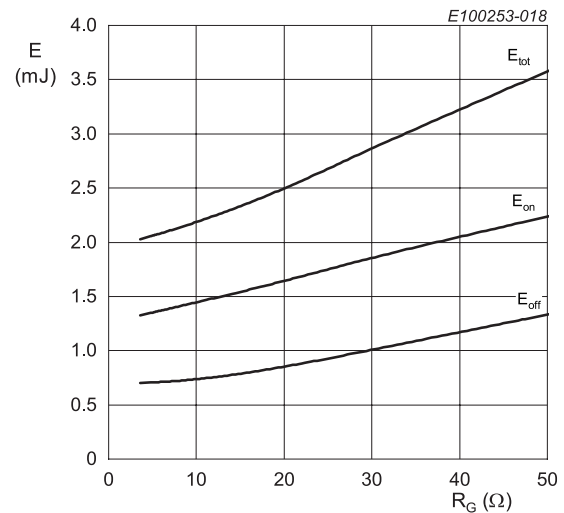
$I_C = 50 \text{ A}; V_{GE} = 15 \text{ V} / 0 \text{ V}; R_g = 10 \text{ } \Omega;$
 $V_{CE} = 400 \text{ V};$ inductive load

Fig. 16. Typical switching times as a function of junction temperature



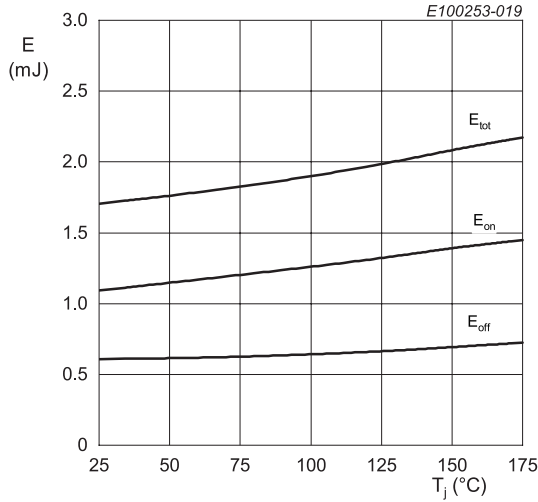
$R_g = 10 \text{ } \Omega; V_{GE} = 15 \text{ V} / 0 \text{ V}; T_j = 175 \text{ }^\circ\text{C};$
 $V_{CE} = 400 \text{ V};$ inductive load

Fig. 17. Typical switching energy losses as a function of collector current



$I_C = 50 \text{ A}; V_{GE} = 15 \text{ V} / 0 \text{ V}; T_j = 175 \text{ }^\circ\text{C};$
 $V_{CE} = 400 \text{ V};$ inductive load

Fig. 18. Typical switching energy losses as a function of gate resistance



$I_C = 50 \text{ A}$; $V_{CE} = 15 \text{ V} / 0 \text{ V}$; $R_g = 10 \Omega$;
 $V_{CE} = 400 \text{ V}$; inductive load

Fig. 19. Typical switching energy losses as a function of junction temperature

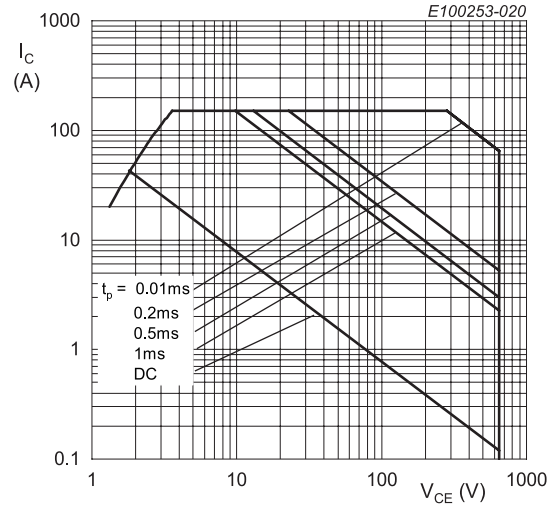


Fig. 20. Forward bias safe operating area

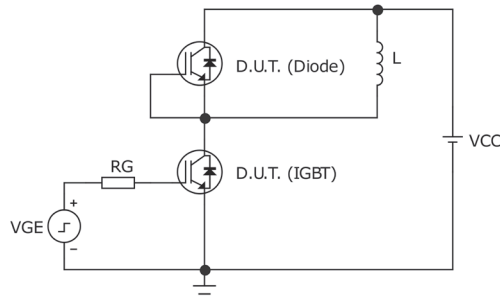


Fig. 21. Test circuit for inductive load switching

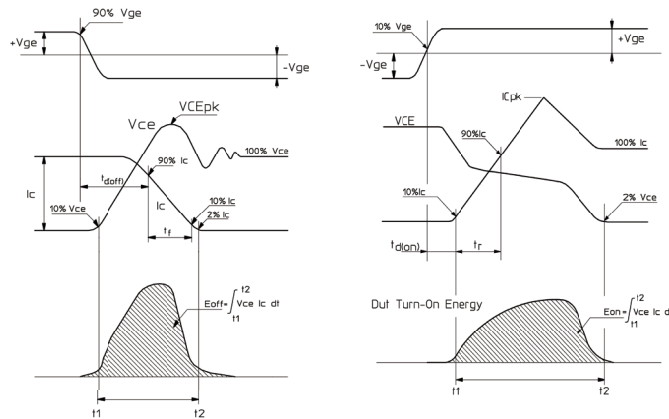
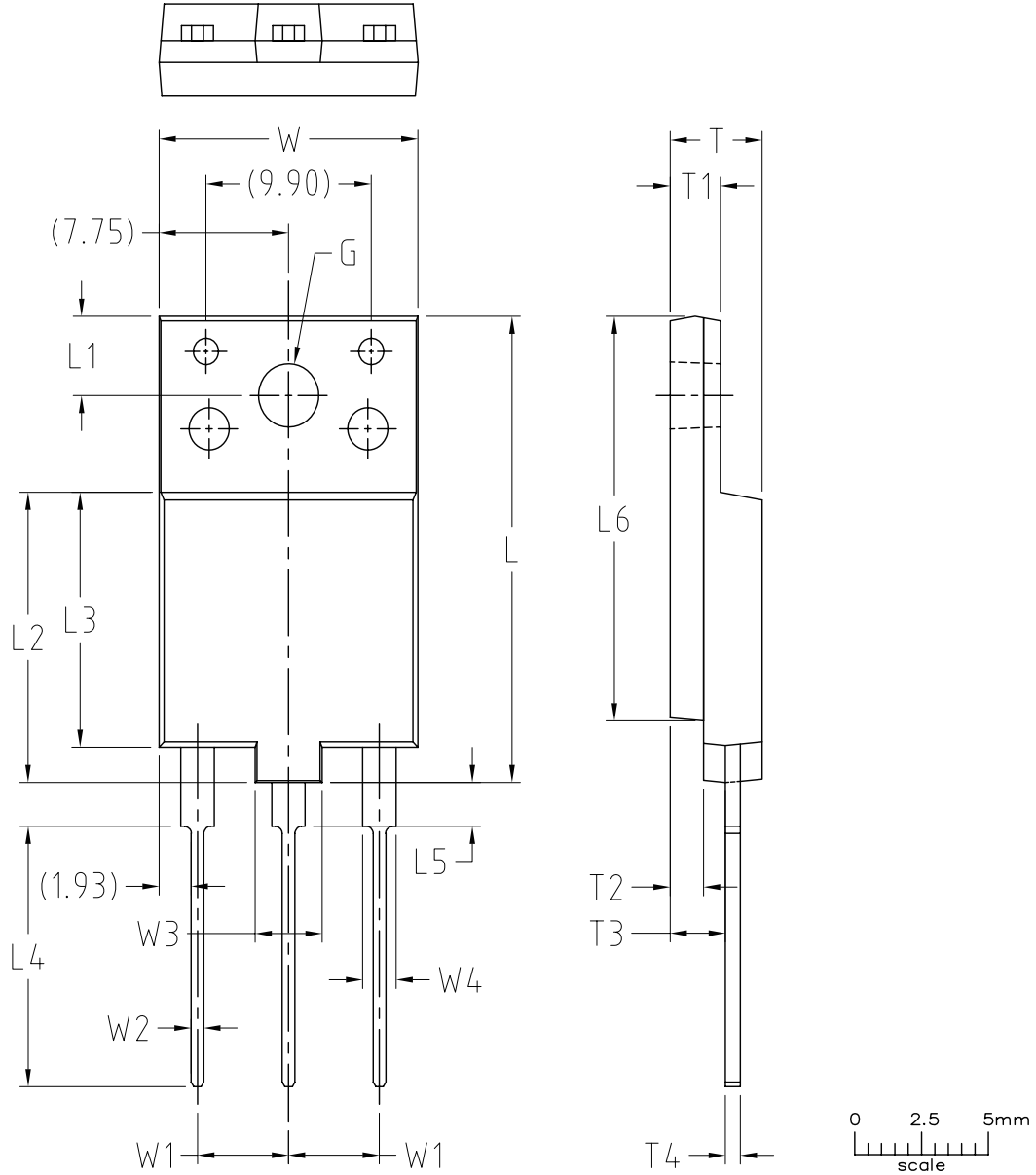


Fig. 22. Definition of switching times and losses

12. Package outline

Plastic single-ended package; isolated heatsink mounted; 1 mounting hole; 3-lead TO-3P 'full pack' TO3PF



Remark : (X) the dimension X in brackets is for reference

UNIT	W	W1	W2	W3	W4	L	L1	L2	L3	L4	L5	L6	T	T1	T2	T3	T4	G(∅)
mm	15.7	5.75	0.95	4.20	2.20	26.7	4.6	16.7	14.7	15.0	2.7	23.2	5.7	3.2	2.2	3.5	1.1	3.8
	15.3	5.15	0.65	3.80	1.80	26.3	4.4	16.3	14.3	14.6	2.3	22.8	5.3	2.8	1.8	3.1	0.8	3.4

OUTLINE VERSION	REFERENCES			EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ		
		TO-3PF			

13. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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