

# SPECIFICATIONS FOR MODULE

CUSTOMER	STD
MODEL	WM-F0116V-JFLWa VER. 1
CUSTOMER APPROVED	

APPROVED BY	CHECKED BY	ORGANIZED BY
LCM 產品部	LCM 產品部	LCM 產品部
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# **History of Version**

Version	Contents	Date	Note
1	New version	5.Aug.2010	SPEC





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# (1) LCM

# 1.1.1 Absolute Maximum Ratings

ITEM	SYMBOL	MIN	TYP	MAX	UNIT
Operating Temperature	TOP	0	-	+50	
Storage Temperature	TST	-20	-	+60	
Power Supply Voltage	VDD-VSS	-0.3	-	4.0	V
Static Electricity	Be sure th	at you are g	rounded wh	en handing	LCM.

# 1.1.2 Electrical Characteristics

(Ta=25

	ITEM		CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Volta	ge for System	VDD	-	3.0	3.3	3.6	V
Supply Curr	ent for System	*IDD	-	•	320	330	mA
Rush Currer	Rush Current			A	-	2	Α
	Common Voltage	VCM		0.9	1.2	1.5	V
Logic Input Voltage	Differential Input Voltage	VID		100	-	600	mV
(LVDS: IN+,IN-)	Threshold Voltage (HIGH)	VTH	Vcm=+1.2V	)'	-	100	mV
, ,	Threshold Voltage (LOW)	VTL	Vcm=+1.2V	-100			mV

<sup>\*</sup>IDD Measurement condition is for all pixels on



# 1.2 Interface Pin Function

#### **CN1:**

CNI.			
NO	SYMBOL	1/0	FUNCTION
1	NC	-	Not connected(Reserve)
2	VDD	Р	Power supply 3.3V(Typ.)
3	VDD	Р	Power supply 3.3V(Typ.)
4	VEDID	Р	EDID +3.3V Power
5	NC	-	Not connected(Reserve)
6	CLK_EDID	I/O	EDID Clock Input
7	DAT_EDID	I/O	EDID Data Input
8	Rin0-	I/O	- LVDS differential data input(R0-R5,G0)
9	Rin0+	1/0	+ LVDS differential data input(R0-R5,G0)
<b>1</b> 0	GND		Ground
11	Rin1-	I/O	- LVDS differential data input(G1-G5,B0-B1)
12	Rin1+	I/O	+ LVDS differential data input(G1-G5,B0-B1)
13	GND	-	Ground
14	Rin2-	I/O	- LVDS differential data input(B2-B5,HS,VS,DE)
15	Rin2+	I/O	+ LVDS differential data input(B2-B5,HS,VS,DE)
16	GND	-	Ground
17	CLKN-	1/0	-LVDS differential clock input
18	CLKN+	I/O	+LVDS differential clock input
19	GND		Ground-Shield
20	NC	4	Not connected(Reserve)
21	NC	-	Not connected(Reserve)
22	GND	-	Ground-Shield
23	NC	-	Not connected(Reserve)
24	NC	-	Not connected(Reserve)
25	GND	-	Ground-Shield
26	NC	-	Not connected(Reserve)
27	NC	-	Not connected(Reserve)
28	GND	-	Ground-Shield
29	NC	-	Not connected(Reserve)
30	NC	-	Not connected(Reserve)
31	VLED_GND	Р	LED Ground
32	VLED_GND	Р	LED Ground
33	VLED_GND	Р	LED Ground
34	NC		Not connected(Reserve)
35	VPWM_EN	I/O	System PWM Logic Input Level
36	VLED_EN	I/O	LED enable Input Level(+3.3V)
37	NC	-	Not connected(Reserve)

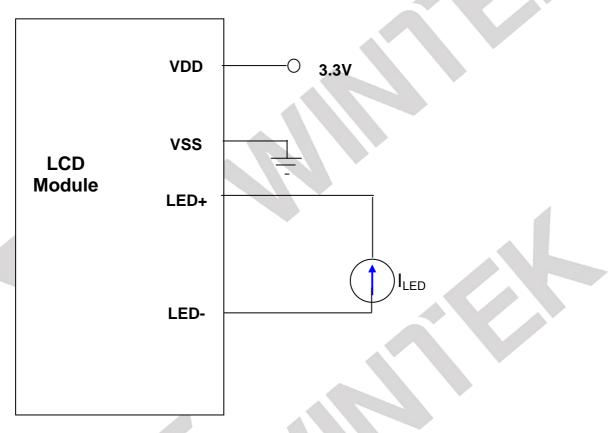


			· · · · · · · · · · · · · · · · · · ·
38	VLED	Р	LED Power Supply 6 21V
39	VLED	Р	LED Power Supply 6 21V
40	VLED	Р	LED Power Supply 6 21V





# 1.3 Power Supply for LCD Module



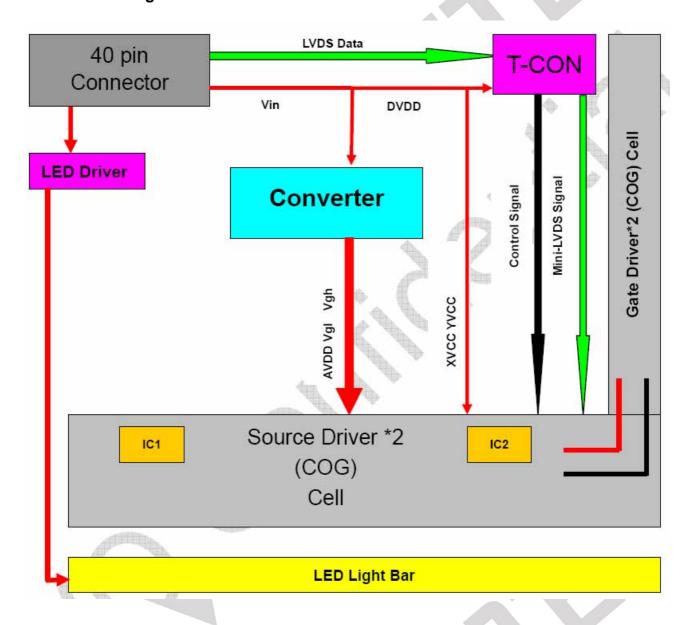
Note 1:Using Internal Voltage Generator VDD= 3.3V





# 1.4 Block Diagram with Display RAM Address

# 1.4-1. Block Diagram





# 1.5 Timing Characteristic

**Table 8 Interface Timings** 

Parameter	Symbol	Unit	min	Тур.	Max
LVDS Clock Frequency(single)	Fdck	MHz	61.87	75.44	106.59
H Total Time	Htotal	clocks	1446	1560	1936
H Active Time	Hac	clocks	1366	1366	1366
V Total Time	Vtotal	lines	778	806	888
V Active Time	Vac	lines	768	768	768
Frame Rate	Vsync	Hz	55	60	62

Vtotal  $\overline{DE}$ Htotal Vac DE DCLK Felock -DATA SIGNA FdckH FdckL DCLK 0.5AC Thd Tsu DISPLAY DATA TES

Figure 12 Timing Characteristics

Note: TES is data enable signal setup time.



## 1.6 Power ON/OFF SEQUENCE

VDD power, interface signals, and lamp on/off sequence are shown in Figure 13.Signals shall be Hi-Z state or low level when VDD is off.

VDD 10% 90% 10% T2 T5 T7

LVDS signals OV T3 T4

Figure 13 Power Sequence

**Table 10 Power Sequencing Requirements** 

Parameter	Symbol	Unit	min	Тур.	max
VDD Rise Time	J1	ms	0.5		10
VDD Good to Signal Valid	T2	ms	0		50
Signal Valid to Backlight On	T3	ms	200		
Backlight Off to Signal Disable	T4	ms	200		
Signal Disable to Power Down	T5	ms	0		50
VDD Fall Time	T6	ms	0		10
Power Off	T7	ms	400		



# (2) ATT(Advanced Touch Technology)

## 2.1 ATT Electrical Characteristics

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						,	- /
ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	Remark
Input Power Voltage	$V_{\text{USB}}$	-	1/	5	ı	V	ı
Interface	-	-		US	В		-
Touch Panel Resolution	-	-		2048*2	2048		1
Supply Current	*IDD	-	-	TBD	TBD	mA	1
Input			Finger				-

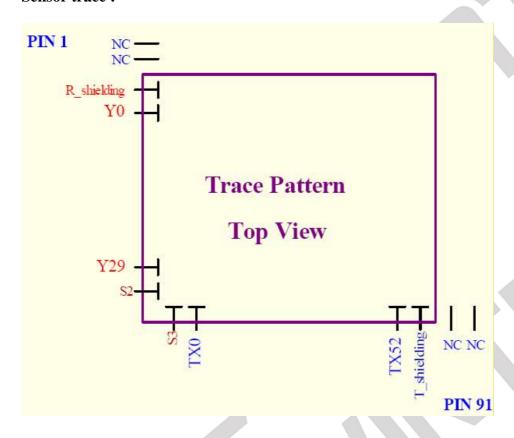
# 2.2 ATT Interface Pin Function

NO.	SYMBOL	I/O	FUNCTION
1	SGND	Р	Shielding Ground
2	VUSB_5V	Р	USB power supply_5V
3	GND	0	Ground
4	USB_D+		USB Data+
5	USB_D-	I/O	USB Data-
6	GND	Р	Ground
7	UART_TX		UART_TX
8	UART_RX	T	UART_RX
9	GND	Р	Ground
10	GPIO	-	NC



# 2.3 ATT Schematic

#### **Sensor trace:**







# (3) Electro-optical Unit

# 3.1 Electro-optical Characteristics

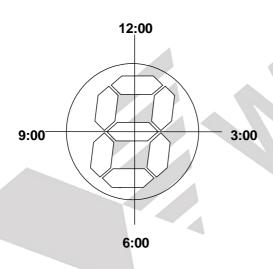
ITEM	SYN	<b>IBOL</b>	CONDITION	MIN.	TYP.	MAX.	UNIT
	ψ= 90	° (12H)		10	15	-	deg.
View Angle	ψ= 27	0 ° (6H)		30	35	-	deg.
CR>10	ψ= 180 ° (9H) ψ= 0 ° (3H)			40	45	-	deg.
				40	45	-	deg.
Contrast Ratio	C	R	Ta=25	400	-	-	-
Response Time		Γr Γd	Ta=25	-	10	-	ms
	Red	Rx		0.573	0.603	0.633	
	IXeu	Ry		0.342	0.372	0.402	-
	Green	Gx		0.316	0.346	0.376	
Color Coordinate	Green	GV     0.540   0.570	0.600				
(CIE1931)	Blue	Bx	Ta=25	0.125	0.155	0.185	
(8.2.1881)	White By Wy		0.107	0.137	0.167		
		Wx		0.283	0.313	0.343	
		Wy		0.299	0.329	0.359	
LCD Type			TFT , ( POSIT	IVE / Trai	nsmissive	)	
Gray Inversion Direction	6:00						

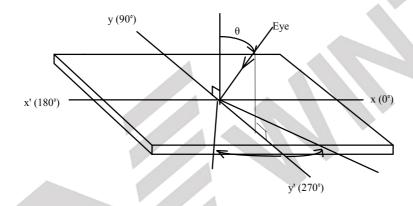
Notes: All the optical data should be measured when the display's driven under the TYP. condition.





# 3.2 Optical Definitions





View Angle



# (4) LCM Mechanical Units

## 4.1 LCM Mechanical Diagram

NO	Document Number	Attachment file
1	MF0116V-AS1-101	Ū

Double-Click the "Attachment Icon" above for opening attachment file.

## 4.2 Back-light Specification

## **LED Backlight Styles:**

The LED chips are distributed over the whole light area of the illumination unit, which gives the most uniform light.

#### 4.2-1. Data About LED Backlight

PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTE
Backlig		LE	D / WHITI	E		-	
LED Light bar Driving		VL	27.0	-	31.5	V	Ta=25[deg C] Note A
LED Current		IL	70	80	90	mA	Ta=25[deg C]
LED Input		VLED	6.0	12.0	21.0	V	Ta=25[deg C]
LED Life Time		LT	10000			Hours	Ta=25[deg C] Note C
PWM Signal	High	VPWM EN	2.2	3.3	3.6	V	
Voltage	Low	V P VV IVI_EIN	0	-	0.5	V	-
LED enable	LED enable High		2.6	3.3	3.6	V	
Voltage	Low	VLED_EN 0	-	0.5	V	-	
Output PWM frequency		FPWM	-	200	1000	Hz	-
Luminous Intensity		IV	-	175	ı	cd/m2	ILED=20 mA
Luminous Intensity Ratio		-	60	-	-	%	I <sub>LED</sub> =20 mA 13points
		-	80	-	-	%	ILED=20 mA 5points

Note A: ILED=20 mA (Per LED)

Note B: Calculator value for LED chip specification.

Note C: The LED life time define as the estimated time to 50% degradation of initial luminous.

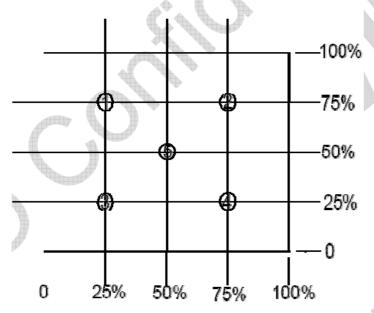
#### 4.2-2. Internal Circuit Diagram

**TBD** 



#### 4.2-3. MEASURED METHOD (X\*Y: Light Area)

A. Definition of Luminance White Measure the luminance of gray level 63 at center point

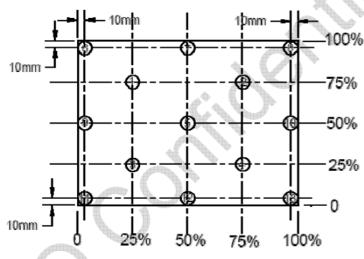


# Screen Luminance Measurement Points (5)

B. Definition of Luminance Uniformity(Variation)
Measure the luminance of gray level 63 at 13 points.

$$UNF(13pts) = \frac{\min(L1, L2, \dots L13)}{\max(L1, L2, \dots L13)}$$

Figure 6 Measurement Locations of 13 Points



Screen Uniformity Measurement Points (13)



# 4.3 Packing Method

NO	Document Number	Attachment file
1	MF0116V-M1-01	Q

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## (5) Quality Units

## 5.1 Specification of Quality Assurance

#### 5.1-1.Purpose

This standard for Quality Assurance should affirm the quality of LCD module products to supply to purchaser by WINTEK CORPORATION (Supplier).

#### 5.1-2. Standard for Quality Test

a. Inspection:

Before delivering, the supplier should take the following tests, and affirm the quality of product.

b. Electro-Optical Characteristics:

According to the individual specification to test the product.

c. Test of Appearance Characteristics:

According to the individual specification to test the product.

d. Test of Reliability Characteristics:

According to the definition of reliability on the specification for testing products.

e. Delivery Test:

Before delivering, the supplier should take the delivery test.

- (i) Test method: According to ANSI/ASQC Z1.4-2003.General Inspection Level take a single time.
- (ii) The defects classify of AQL as following:

Major defect: AQL=0.65
Minor defect: AQL=2.5
Total defects: AQL=2.5

#### 5.1-3. Nonconforming Analysis & Deal With Manners

- a. Nonconforming analysis:
  - (i) Purchaser should supply the detail data of non-conforming sample and the non-suitable state.
  - (ii) After accepting the detail data from purchaser, the analysis of nonconforming should be finished in two weeks.
  - (iii) If supplier can not finish analysis on time, must announce purchaser before two weeks.
- b. Disposition of nonconforming:
  - (i) If find any product defect of supplier during assembly time, supplier must change the good product for every defect after recognition.
- (ii) Both supplier and customer should analyze the reason and discuss the disposition of nonconforming when the reason of nonconforming is not sure.



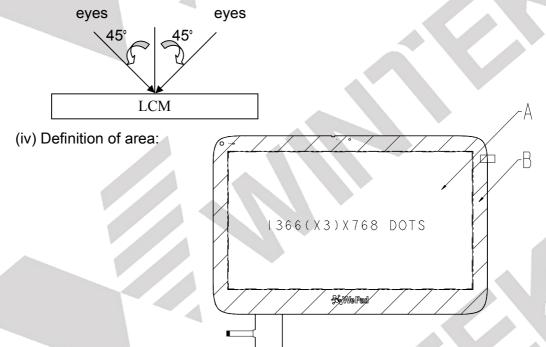
#### 5.1-4. Agreement items

Both sides should discuss together when the following problems happen.

- a. There is any problem of standard of quality assurance, and both sides think that it must be modified.
- b. There is any argument item which does not record in the standard of quality assurance.
- c. Any other special problem.

#### 5.1-5. Standard of The Product Appearance Test

- a. Manner of appearance test:
  - (i) The test must be under 20W × 2 or 40W fluorescent light, and the distance of view must be at 30 cm.
  - (ii) When display on use front-light test, while display off use back-light test.
  - (iii)The test direction is base on about around 45° of vertical line.



A Area: Viewing area.

B Area: Out of viewing area (Outside viewing area)

Any defect at area B could be ignored. If customer has particular requirement, this requirement should be clearly defined in inspection specification. If inspection specification has defined other criteria, the final judgement should follow the inspection specification.

- b. Basic principle:
  - (i) It will accord to the AQL when the standard can not be described.
  - (ii) The sample of the lowest acceptable quality level must be discussed by both supplier and customer when any dispute happened.
  - (iii) Must add new item on time when it is necessary.



5.1-6. Inspection specification

NO	Document Number	Attachment file
1	M1L070012	

Double-Click the "Attachment Icon" above for opening attachment file.

# **5.2 Standard Specification for Reliability**

NO	Document Number	Attachment file
1	M3ET090001	ū

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#### 5.3 Precautions in Use of LCM

#### 5.3-1 Handling of LCM

- Don't give external shock.
- Don't apply excessive force on the surface.
- Liquid in LCD is hazardous substance. Must not lick and swallow. when the liquid is attach to your hand, skin, cloth etc. Wash it out thoroughly and immediately.
- Don't operate it above the absolute maximum rating.
- Don't disassemble the LCM.

#### 5.3-2 Storage

- Store in an ambient temperature of 5 to 45, and in a relative humidity of 40% to 60%. Don't expose to sunlight or fluorescent light.
- Storage in a clean environment, free from dust, active gas, and solvent.
- Store in anti-static electricity container.
- Store without any physical load.

#### 5.3-3 Soldering

- Use the Sn-Ag-Cu (96.5, 3.0, 0.5) solder
- Iron: Temperature 300 and less than 5-6 sec during soldering.
- Rewiring : no more than 3 times.

#### 5.3-4 Assembly

The front polarizer is covered with a protective foil which should be removed before use.

# (6) Substance Management Units

# 6.1 Product Substances Management Documentation

NO	Document Number	Attachment file
1	Environment management standard(EMS-P-017-01)	Ū

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