

SPECIFICATIONS FOR LCD MODULE

CUSTOMER	STD
MODEL	WM-F4823V4-7FLWa
WODEL	VER. 2
CUSTOMER	
APPROVED	

APPROVED BY	CHECKED BY	ORGANIZED BY
LCM 產品部	LCM 產品部	LCM 產品部
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☐ APPROVAL FOR SPECIFICATIONS ONLY

APPROVAL FOR SPECIFICATIONS AND SAMPLE (18070566)

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History of Version

Version	Contents	Date	Note
a1	NEW VERSION	12.Jun.2008	SPEC.
a2	Modify 1.2 Electrical Characteristics Modify 1.4 Power Supply for LCD Module : cancel the net of VCOM to GND	25.Jul.2008	SPEC & Sample



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(1) Electronic Units

1.1 Absolute Maximum Ratings

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
OPERATING TEMPERATURE	T _{OP}	-20	-	+70	
STORAGE TEMPERATURE	T _{ST}	-30	-	+80	
INPUT VOLTAGE	Vin	-0.3		V _{DDIO} +0.3	V
CHARGE PUMP POWER SUPPLY	PVDD	-0.3	-	+7.0	V
INPUT I/O POWER SUPPLY	VDDIO	-0.3	-	+7.0	٧
ANALOG POWER SUPPY	VDD	-0.3	-	+7.0	V
STATIC ELECTRICITY	Be sure that you are grounded when handing LCM.				

1.2 Electrical Characteristics

(Ta=25)

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
CHARGE PUMP POWER SUPPLY	PVDD	-	3.2	3.3	3.4	V
INPUT I/O POWER SUPPLY	VDDIO	-	3.2	3.3	3.4	V
ANALOG POWER SUPPY	VDD	-	3.2	3.3	3.4	V
INPUT HIGH VOL.	V _{IH}	-	0.7VDDIO	-	VDDIO	٧
INPUT LOW VOL.	V _{IL}	-	GND	-	0.3VDDIO	٧
OUTPUT HIGH VOL.	V _{OH}	-	0.9VDDIO	-	VDDIO	V
OUTPUT LOW VOL.	V _{OL}	-	GND	-	0.1VDDIO	V
SUPPLY CURRENT	I _{PVDD} + I _{DD} +I _{DDIO}	-	-	4	5.5	mA

^{*} I_{PVDD}+ I_{DD} +I_{DDIO} Measurement condition is for all pixels on display
To avoid image residual, the same picture could not display to exceed one hour.



1.3 Interface Pin Function

NO	SYMBOL	1/0	FUNCTION
1.	VCOM	I	Common electrode driving voltage
2.	NC	-	DUMMY
3.	VGL	С	Negative low power supply for gate output
4.	C4P	С	Power setting capacitor connect pin
5.	C4N	С	Power setting capacitor connect pin
6.	VGH	С	Positive power supply for gate output
7.	FPR	0	Frame polarity output for VCOM
8.	VCAC	С	Define the amplitude of the VCOM swing
9.	VDDA	С	Intermediate voltage for charge Pump
10.	C3P	С	Power setting capacitor connect pin
11.	C3N	С	Power setting capacitor connect pin
12.	VINT	С	Intermediate voltage for charge Pump
13.	C2P	С	Power setting capacitor connect pin
14.	C2N	С	Power setting capacitor connect pin
15.	VCC	С	Intermediate voltage for charge Pump
16.	C1P	С	Power setting capacitor connect pin
17.	C1M	С	Power setting capacitor connect pin
18.	PVSS	Р	Power Pump Power GND
19.	PVDD	Р	Power Pump Power VDD
20.	DRV	0	Output the control signal for switching regulator for LED driving
21.	LED_Anode	Р	For Led Anode voltage
22.	VSSA	Р	Analog GND
23.	VFB	Р	Main booster regulator feedback input
24.	VDD	Р	Voltage supply pin for analog circuit
25.	VSS	Р	Digital GND
26.	VDDIO	Р	Digital interface power supply
27.	CS	1	Serial communication chip select

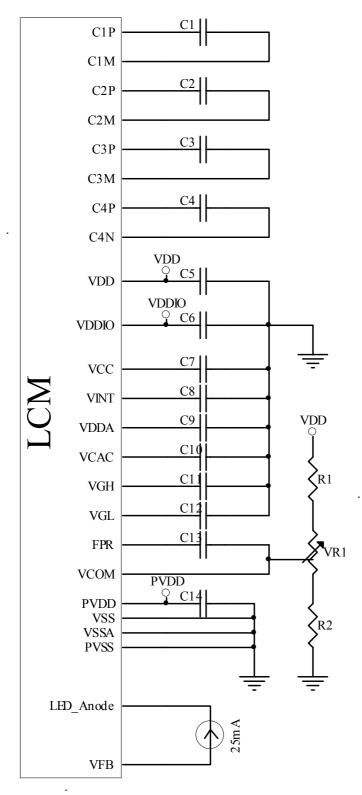


			A A AAIIAI PL
28.	SDA	I/O	Serial communication data input
29.	SCL	I	Serial communication clock input
30.	HSYNC	I	Horizontal sync input
31.	VSYNC	I	Vertical sync input
32.	DCLK	I	Clock input
33.	D7	I	Data input:MSB
34.	D6	I	Data input
35.	D5	I	Data input
36.	D4	I	Data input
37.	D3	I	Data input:
38.	D2	I	Data input
39.	D1	I	Data input:
40.	D0	I	Data input:LSB

I: Digital signal input, O: Digital signal output, G: GND, P: Power, C: Power set capacitor connect pin,



1.4 Power Supply for LCD Module



Note: PVDD=VDD=VDDIO=3.3V

C1, C5~C7, C9~C10,C14 1uF/10V

C2, C8 1uF/16V

C3, C4, C11, C12 1uF/25V

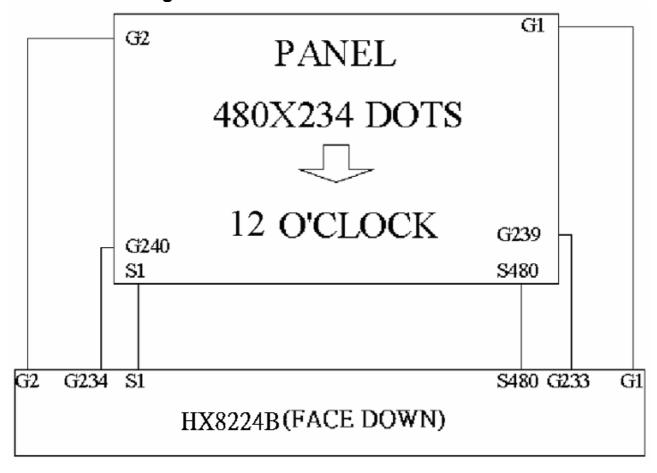
C13 4.7uF/10V

VR1:1M, R1, R2:120K



1.5 Block Diagram with Display RAM Address

1.5-1. Block Diagram





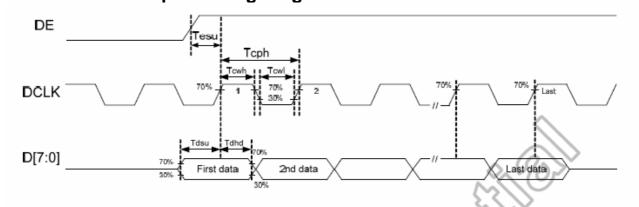
1.5-2. Initialization Table:

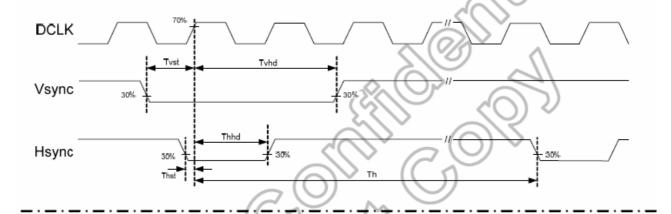
	1.3-2. Illitialization Table.							
	Instruction	Code	Description					
1.	System Control	0x0207	Charge Pump ON, DC-DC is ON ,Standby Mode OFF,					
2.	Timing Controller Function	0x0611	Delta Mode ON, Dithering ON, Set CF Format to RGB (odd line)/GBR (even line)					
3.	Operation Control	0x0a03	Normal VCOM Polarity,GO1>G234,SO1>SO480					
4.	Input Data Format Control	0x0e00	HSD Negative Polarity, VSD Negative Polarity, Serial-RGB data format (HV mode), 9.7MHz					
5.	Source Timing Delay Control	0x1200	Source Delay Timing=0 CLKIN					
6.	Gate Timing Delay Control	0X1600	Gate Delay Timing=0 CLKIN					
7.	VCOMAC Output Level Control	0X1a03	VCOMAC=4.4V					

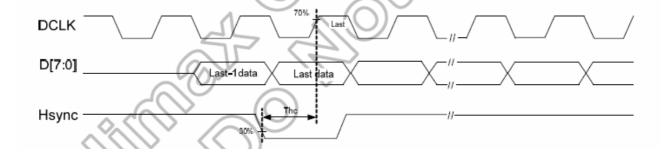


1.6 Timing Characteristic

Clock and Data Input Timing Diagram









3-Wire Timing Diagram

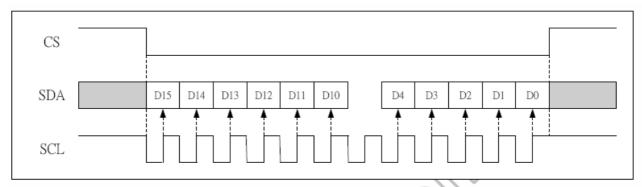


Figure 10. 1 SPI Timing Diagram

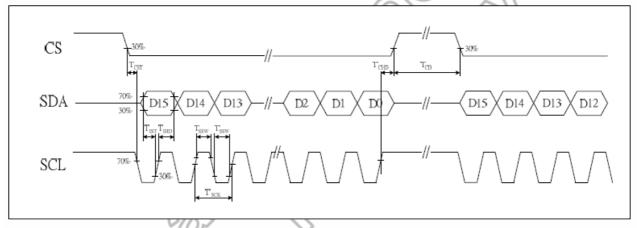


Figure 10. 2 SPI Timing Diagram



AC Electrical Characteristics

Parameter	Symbol		Spec.		Unit
Farameter	Symbol	Min.	Тур.	Max.	Offic
Delay between Hsync and DCLK	Thc	-	-	1	DCLK
Hysnc width	Twh	1	-	-	DCLK
Vsync width	Twv	1	-		DCLK
Vsync setup time	Tvst	12	-	-	ns
Vsync hold time	Tvhd	12	- ,		ns
Hsync setup time	Thst	12	- \	/\\{\\/	ns
Hsync hold time	Thhd	12	- {`		ns
Data set-up time	Tdsu	12	-6	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	ns
Data hold time	Tdhd	12	(-(/)	> -	ns
DE set-up time	Tesu	12		-	ns
Vsync period NTSC	Tv	250	262/262.5	360	Th
Vsync period PAL	Tv	300	312/312.5	360	Th
Hsync to Vsync time for ODD field	T _{HV O}	4	r - 6	+4	DCLK
Hsync to ∀sync time for E∀EN field	T _{HV E}	3/	0.5)) <u>.</u> <	Th
S/D output stable time	Tst			30	μs
G/D output stable time	Tgst	V - C		2	μs

Serial communication

Parameter	Symbol		Unit		
raianietei	Symbol	Min.	Тур.	Max.	Oilit
Serial clock period	Tsck	100	-	-	ns
Serial clock duty cycle	Tscw	40	50	60	%
Serial clock width	Tssw	40	-	-	ns
Serial data setup time	Tist	40	-	-	ns
Serial data hold time	Tihd	40	-	-	ns
SPENB setup time	Tcst	40	-	-	ns
SPENB data hold time	Tchd	40	-	-	ns
Chip select distinguish	Tcd	1	-	-	μs



1.7 Power ON/OFF SEQUENCE

1.7.1 Power ON/OFF Sequence

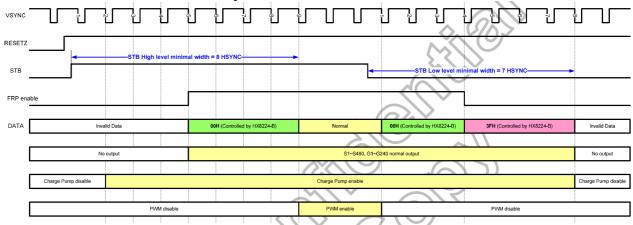


Figure 5.2 Power on/off sequence timing diagram



(2) Electro-optical Units

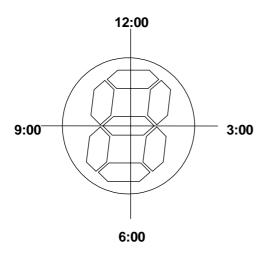
2.1 Electro-optical Characteristics

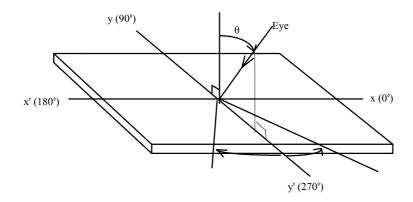
ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
		= 0 ° 6H	45	-	-	
Viewing Angle (C.R. ≥ 10)		= 180 ° 12H	15	-	-	doa
		= 90 ° 3H	45	-	-	deg.
		= 270 ° 9H	45	-	-	
CONTRAST RATIO	CR	Transmissive(Ta=25)	150	-	-	-
RESPONSE TIME	Tr+Td		-	25	-	Ms
White x-coord	Wx	Ta=25	0.25	0.31	0.37	
White y-coord	Wy		0.27	0.33	0.39	
LCD TYPE	TFT (Positive / Transmissive)					
VIEWING DIRECTION		12 O'CLC	OCK			

Notes: All the optical data should be measured when the display's driven under the "Typ" condition.



2.2 Optical Definitions



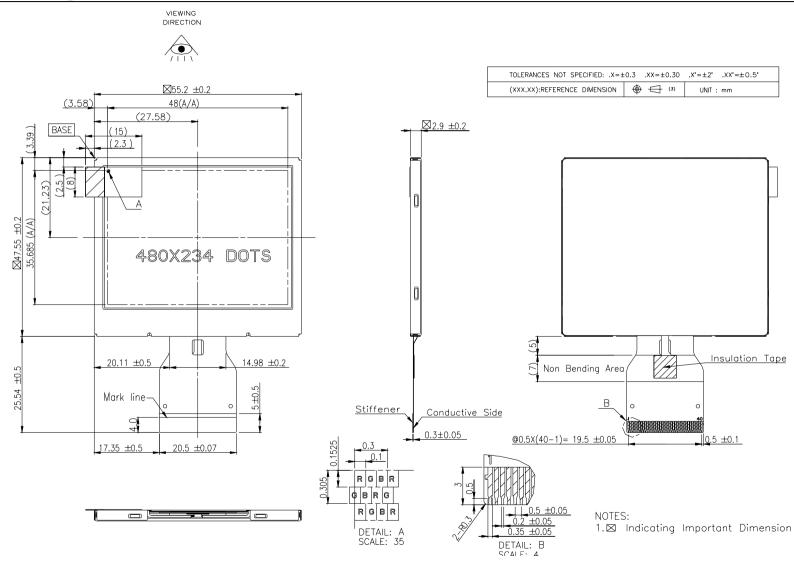


View Angle



(3) Mechanical Units

3.1 Mechanical Diagram





3.2 Back-light Specification

LED Backlight Styles:

The LED chips are distributed over the whole light area of the illumination unit, which gives the most uniform light.

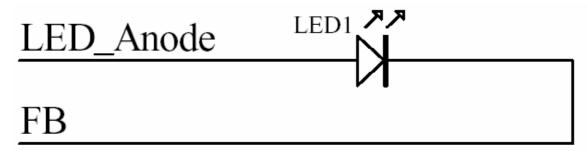
3.2-1. Data About LED Backlight

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE
Backlight type			LED	(White))		
Supply Current	I _{LED}	-	25	-	mA	-	V _{AK} ≤4.4V
Reverse Voltage (Single chip)	V _R	-	1	5.0	V	1	-
Luminous Intensity	I _V	200	250	-	cd/m ²	I _{LED} =25mA	1 (With LCD)
Luminous Intensity Ratio	-	-	1	30	%	I _{LED} =25mA	2

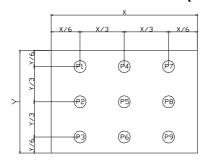
NOTE: 1. Average Luminous Intensity of P1 – P9

2. Luminous Intensity Ratio = (MAX-MIN)./ MAX.

3.2-2. Internal Circuit Diagram



3.2-3. MEASURED METHOD (X*Y: Light Area)



(Effective spatial Distribution)

Hole Diameter φ3mm;1 to 9 per Position Measured Luminous Intensity Ratio

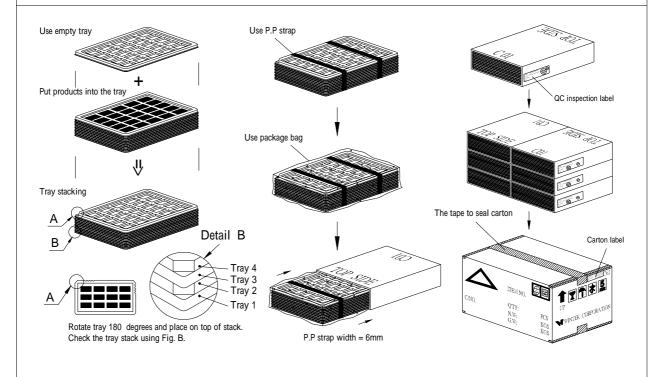


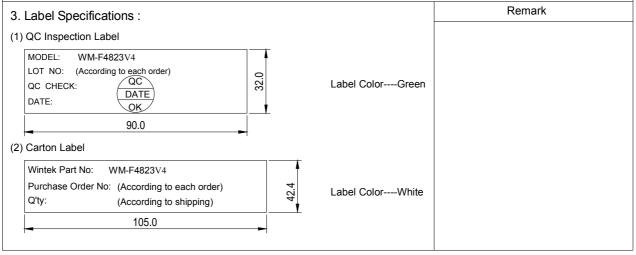
3.3 Packing Method

1.	1. Packaging Material : (per carton)						
N	IO. Item	Model		Dimensions (mm)	Unit Weight (Kg)	Quantity	
1	LCM Module	WM-F48	23V4	55.2*47.55	0.01	648	
2	2 Tray	VK28-1	PS	320*217*12*0.6	0.06	60	
3	Product Box	C0	1	320*219*70	0.131	6	
4	4 Carton	C62	2	475*345*250	0.857	1	
5	5 Package Bag	C5	5	467*321*0.08	0.023	6	
6	Total Weight	1.	1.9	Kg±	± 5%		

2. Packaging Specifications and Quantity:

(1) LCM quantity per tray : quantity per row 4 x quantity per column 3 = 12 (2) LCM quantity per box : quantity per tray 12 x quantity of trays 9 = 108 (3) Total LCM quantity in carton : quantity per box 108 x quantity of boxes 6 = 648







(4) Quality Units

4.1 Specification of Quality Assurance

4.1-1.Purpose

This standard for Quality Assurance should affirm the quality of LCD module products to supply to purchaser by WINTEK CORPORATION (Supplier).

4.1-2. Standard for Quality Test

a. Inspection:

Before delivering, the supplier should take the following tests, and affirm the quality of product.

b. Electro-Optical Characteristics:

According to the individual specification to test the product.

c. Test of Appearance Characteristics:

According to the individual specification to test the product.

d. Test of Reliability Characteristics:

According to the definition of reliability on the specification for testing products.

e. Delivery Test:

Before delivering, the supplier should take the delivery test.

- (i) Test method: According to ANSI/ASQC Z1.4-2003.General Inspection Level take a single time.
- (ii) The defects classify of AQL as following:

Major defect: AQL=0.65
Minor defect: AQL=2.5
Total defects: AQL=2.5

4.1-3. Nonconforming Analysis & Deal With Manners

- a. Nonconforming analysis:
 - (i) Purchaser should supply the detail data of non-conforming sample and the non-suitable state.
 - (ii) After accepting the detail data from purchaser, the analysis of nonconforming should be finished in two weeks.
 - (iii) If supplier can not finish analysis on time, must announce purchaser before two weeks.
- b. Disposition of nonconforming:
 - (i) If find any product defect of supplier during assembly time, supplier must change the good product for every defect after recognition.
 - (ii) Both supplier and customer should analyze the reason and discuss the disposition of nonconforming when the reason of nonconforming is not sure.



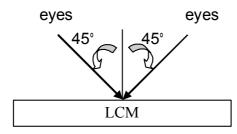
4.1-4. Agreement items

Both sides should discuss together when the following problems happen.

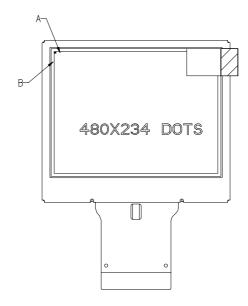
- a. There is any problem of standard of quality assurance, and both sides think that it must be modified.
- b. There is any argument item which does not record in the standard of quality assurance.
- c. Any other special problem.

4.1-5. Standard of The Product Appearance Test

- a. Manner of appearance test:
 - (i) The test must be under 20W × 2 or 40W fluorescent light, and the distance of view must be at 30 cm.
 - (ii) When display on use front-light test, while display off use back-light test.
 - (iii)The test direction is base on about around 45° of vertical line.



(iv) Definition of area:



A Area: Viewing area.

B Area: Out of viewing area.

(Outside viewing area)

- b. Basic principle:
 - (i) It will accord to the AQL when the standard can not be described.
 - (ii) The sample of the lowest acceptable quality level must be discussed by both supplier and customer when any dispute happened.
 - (iii) Must add new item on time when it is necessary.
- c. Standard of inspection:(Unit: mm)



4.1-6. Inspection specification

NO	Document Number	Attachment file
1	M1L070012	9



4.2 Standard Specification for Reliability

4.2-1.Standard Specifications for Reliability of LCD Module

No	Item	Description
01	High temperature operation	Operating temperature (high) of the sample should be allowed to stand for 240 hours under driving condition.
02	Low temperature operation	Operating temperature (low) of the sample should be allowed to stand for 240 hours under driving condition.
03	High temperature resistance	Storage temperature (high) of the sample should be allowed to stand for 240 hours under no-load condition, and then returning it to normal temperature condition, and allowing it stand for 30 minutes.
04	Low temperature resistance	Storage temperature (low) of the sample should be allowed to stand for 240 hours under no-load condition, then returning it to normal temperature condition, and allowing it stand for 24 hours.
05	Moisture resistance	The sample should be allowed to stand at 60 , 90% RH MAX for 240 hours under no-load condition excluding the polarizer, then taking it out and drying it at normal temperature.
06	Thermal shock resistance	The sample should be allowed to stand the following 50 cycles of storage: Storage temperature (low) for 30 minutes → normal temperature for 5 minutes → storage temperature (high) for 30 minutes → normal temperature for 5 minutes , as one cycle.

Notes: Please refer to section 1.1 Absolute Maximum Ratings to mention the operating temperature and storage temperature.



4.2-2. Testing Conditions and Inspection Criteria

For the final test the testing sample must be stored at room temperature for 24 hours, after the tests listed in Table 4.2, Standard specifications for Reliability have been executed in order to ensure stability.

NO	Item	Test Model	Inspection Criteria
01	Current Consumption	Refer To Specification	The current consumption should conform to the product specification.
02	Contrast	Refer To Specification	After the tests have been executed, the contrast must be larger than half of its initial value prior to the tests.
03	Appearance	Visual inspection	Defect free.

4.2-3.Life Time

Life time	Functions, performance, appearance, etc. shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature (25 ±10), normal humidity (45 ±20% RH), and in area not exposed to direct sun light. (Life time of backlight, please refer to "Data about backlight".)
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Note: From our experience the life time of high humidity operation and high temperature operation as above mentioned could be achieved.



4.3 Precautions in Use of LCM

4.3-1 Handling of LCM

- Don't give external shock.
- Don't apply excessive force on the surface.
- Liquid in LCD is hazardous substance. Must not lick and swallow. when the liquid is attach to your hand, skin, cloth etc. Wash it out thoroughly and immediately.
- Don't operate it above the absolute maximum rating.
- Don't disassemble the LCM.

4.3-2 Storage

- Store in an ambient temperature of 5 to 45, and in a relative humidity of 40% to 60%. Don't expose to sunlight or fluorescent light.
- Storage in a clean environment, free from dust, active gas, and solvent.
- Store in anti-static electricity container.
- Store without any physical load.

4.3-3 Soldering

- Use the Sn-Ag-Cu (96.5, 3.0, 0.5) solder
- Iron: Temperature 300 and less than 5-6 sec during soldering.
- Rewiring : no more than 3 times.

4.3-4 Assembly

 The front polarizer is covered with a protective foil which should be removed before use.



(5) Substance Management Units

5.1 Product Substances Management Documentation

NO	Document Number	Attachment file
1	Environment management standard (EMS-P-017-01)	

Note: Details of control substance and apply threshold for "Product Substances

Management Documentation" please refer to the following attachment file.