

WPM2045

Integrated P-Channel Power MOSFET (-20V, -2.8A) and Schottky Diode

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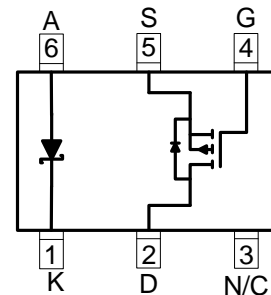
MOSFET	
V _{DS} (V)	Typical R _{dson} (Ω)
-20	0.093@ V _{GS} =-4.5V
	0.122@ V _{GS} =-2.5V
	0.160@ V _{GS} =-1.8V
Schottky	
V _R (V)	Typical V _F (V)
20	0.4@0.5A



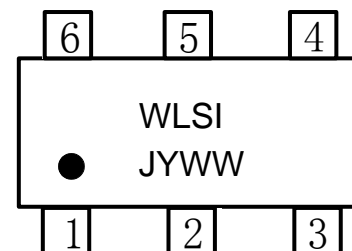
SOT-23-6L

Descriptions

The WPM2045 is the P-Channel enhancement MOS Field Effect Transistor and Schottky Diode as a single package for DC-DC converter or level shift applications, uses advanced trench technology and design to provide excellent R_{DS(ON)} with low gate charge. Standard Product WPM2045 is Pb-free.



Pin configuration (Top View)



Features

- Small package SOT-23-6L
- Featuring a MOSFET and Schottky Diode
- Independent Pin out to each Device to Ease Circuit Design
- Ultra Low V_F Schottky Diode

Applications

- Li-Ion Battery Charging
- High Side DC-DC Conversion Circuits
- High Side Drive for Small Brushless DC Motors
- Power Management in Portable, Battery Powered Products

WLSI= Willsemi

J = Device Code

YWW= Date Code

Marking

Order Information

Device	Package	Shipping
WPM2045-6/TR	SOT-23-6L	3000/Tape&Reel

Absolute Maximum Ratings (P-Channel ,T_A=25°C unless otherwise noted)

Parameter	Symbol	10 s	Steady State	Unit	
Drain-Source Voltage	V _{DS}	-20		V	
Gate-Source Voltage	V _{GS}	±8			
Continuous Drain Current ^{a d}	I _D	T _A =25°C	-2.8	-2.2	A
		T _A =70°C	-2.2	-1.8	
Maximum Power Dissipation ^{a d}	P _D	T _A =25°C	1.3	0.8	W
		T _A =70°C	0.8	0.5	
Continuous Drain Current ^{bd}	I _D	T _A =25°C	-2.5	-2.0	A
		T _A =70°C	-2.0	-1.6	
Maximum Power Dissipation ^{bd}	P _D	T _A =25°C	1.1	0.7	W
		T _A =70°C	0.7	0.4	
Pulsed Drain Current ^c	I _{DM}	-10		A	
Operating Junction Temperature	T _J	-55 to 150		°C	
Lead Temperature	T _L	260		°C	
Storage Temperature Range	T _{stg}	-55 to 150		°C	

Absolute Maximum Ratings (Schottky, T_A=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Reverse voltage (repetitive peak)	V _{RM}	20	V
Reverse voltage (DC)	V _R	20	V
Average rectified forward current	I _O	0.5	A
Peak Forward Surge Current ^(ef)	I _{FSM}	5	A
Operating temperature	T _{opr}	-40 ~ 85	°C
Storage temperature	T _{stg}	-55 to 150	°C

Thermal Resistance Ratings (T_A=25°C unless otherwise noted)

Parameter	Symbol	Typical	Maximum	Unit
Junction-to-Ambient Thermal Resistance ^a	t ≤ 10 s	74	92	°C/W
	Steady State	115	143	
Junction-to-Ambient Thermal Resistance ^b	t ≤ 10 s	90	112	
	Steady State	138	172	
Junction-to-Case Thermal Resistance	Steady State	63	78	

a Surface mounted on FR4 Board using 1 square inch pad size, 1oz copper

b Surface mounted on FR4 board using minimum pad size, 1oz copper

c Pulse width < 380µs

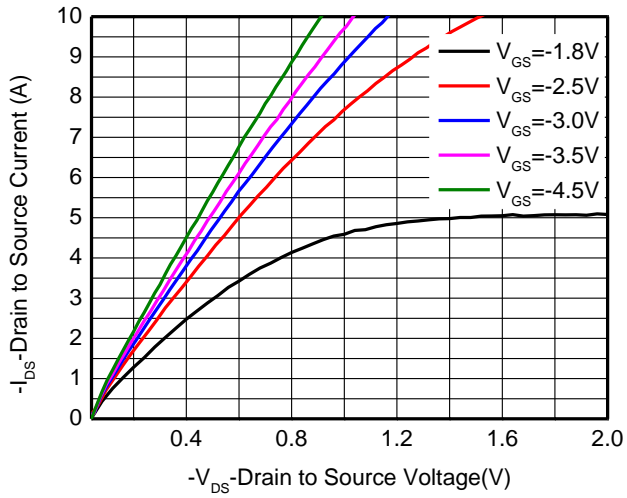
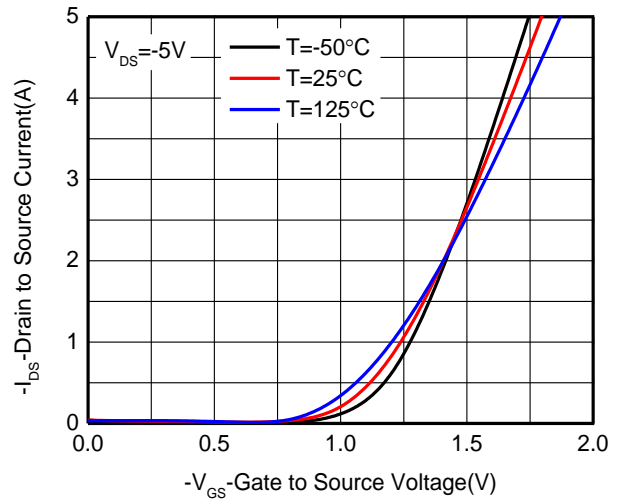
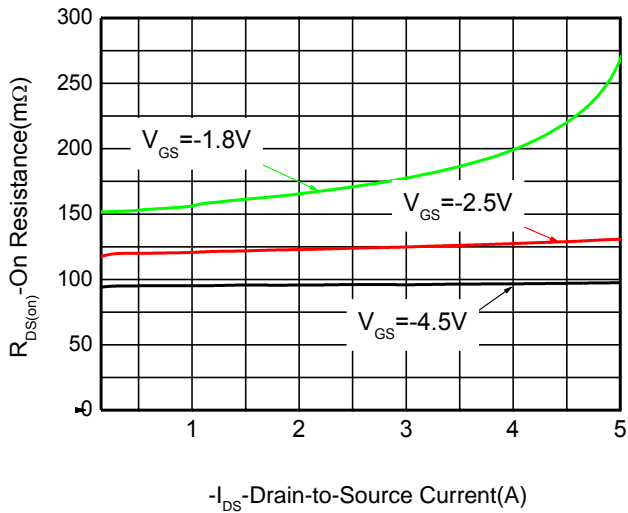
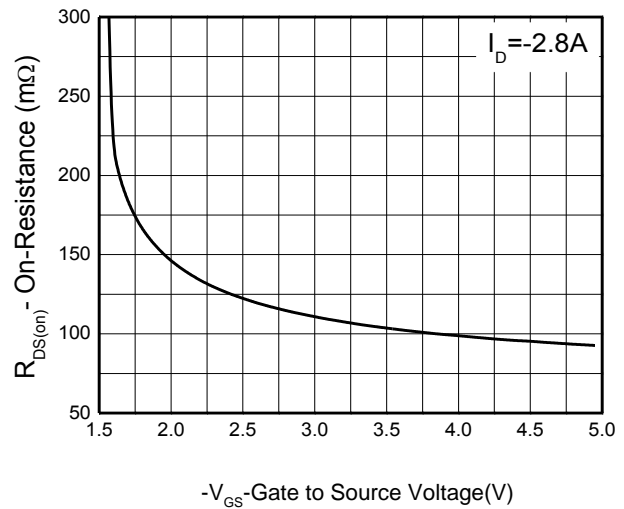
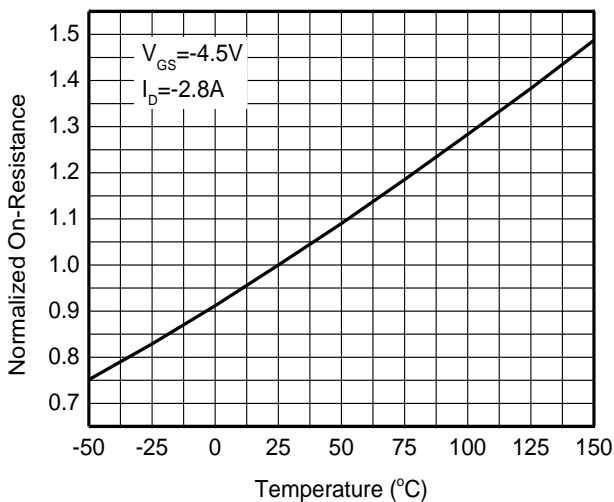
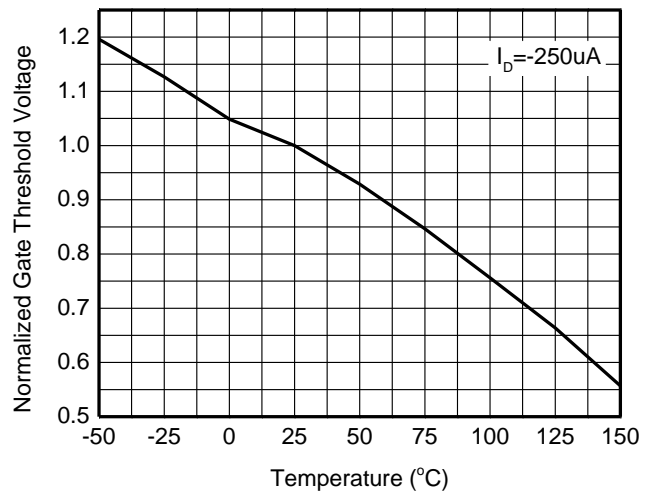
d Maximum junction temperature T_J=150°C.

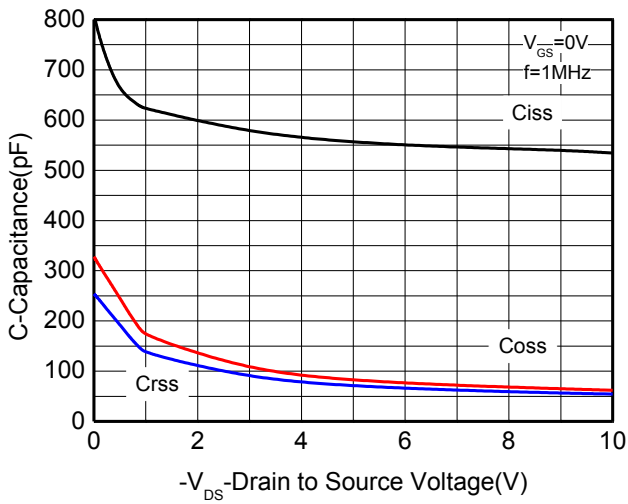
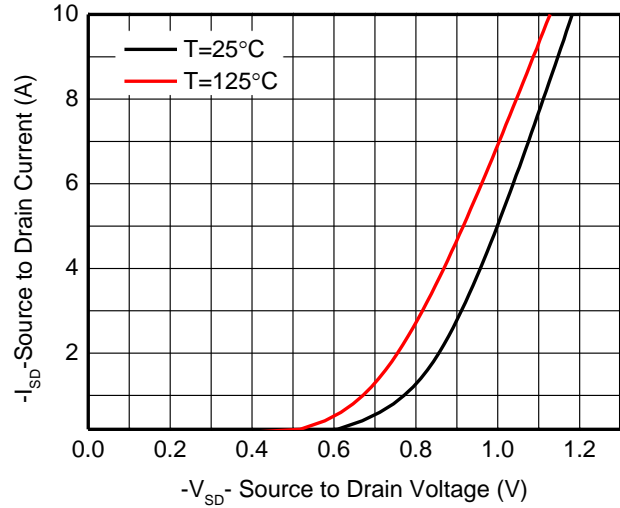
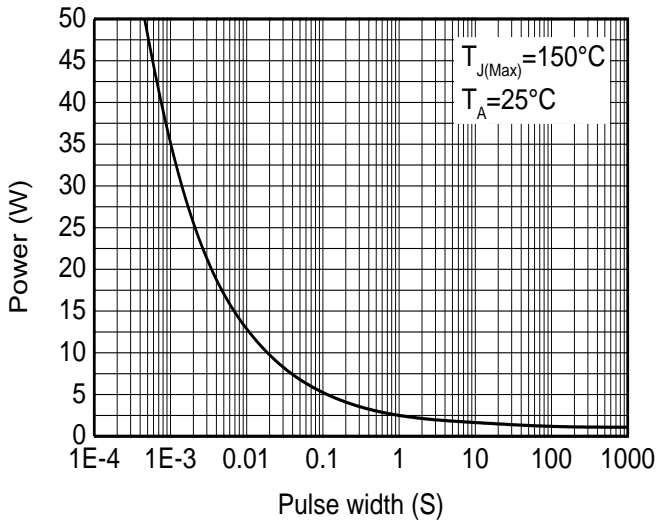
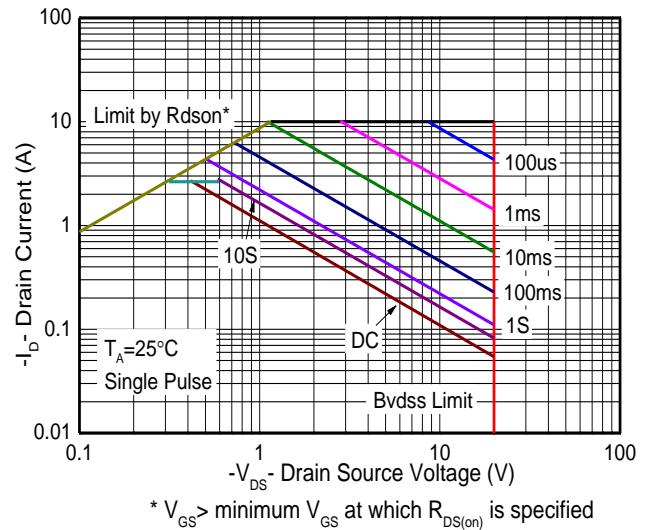
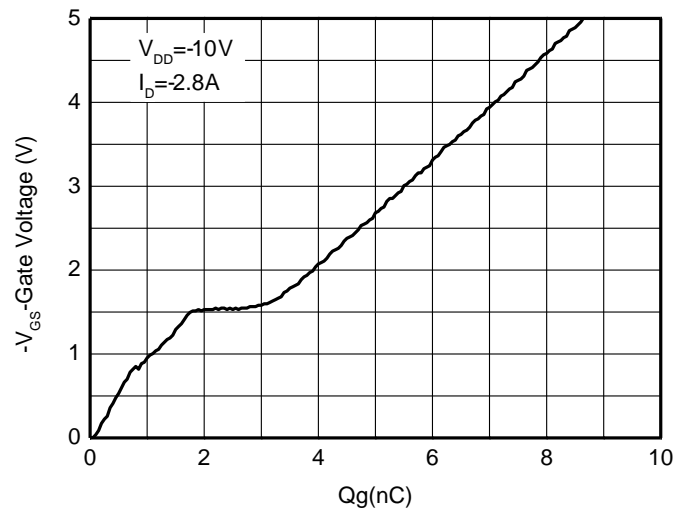
e Current rating is limited by wire-bonding.

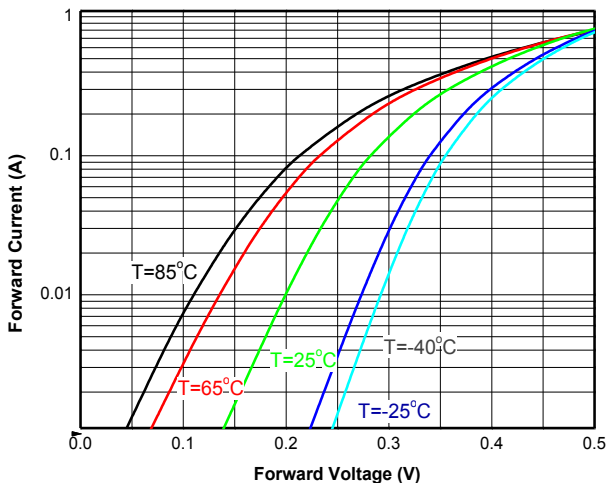
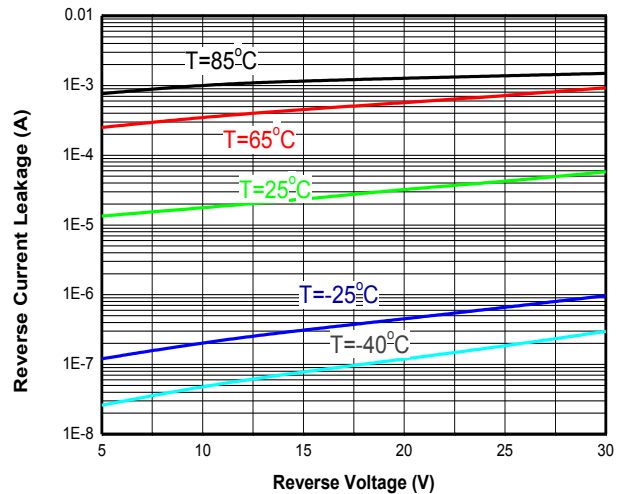
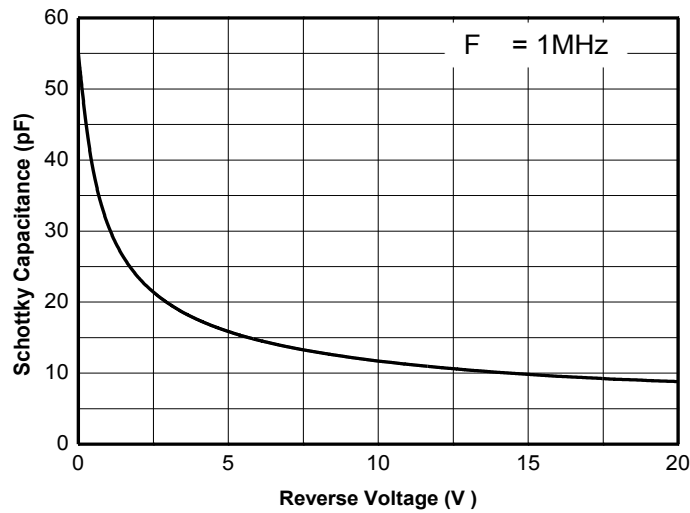
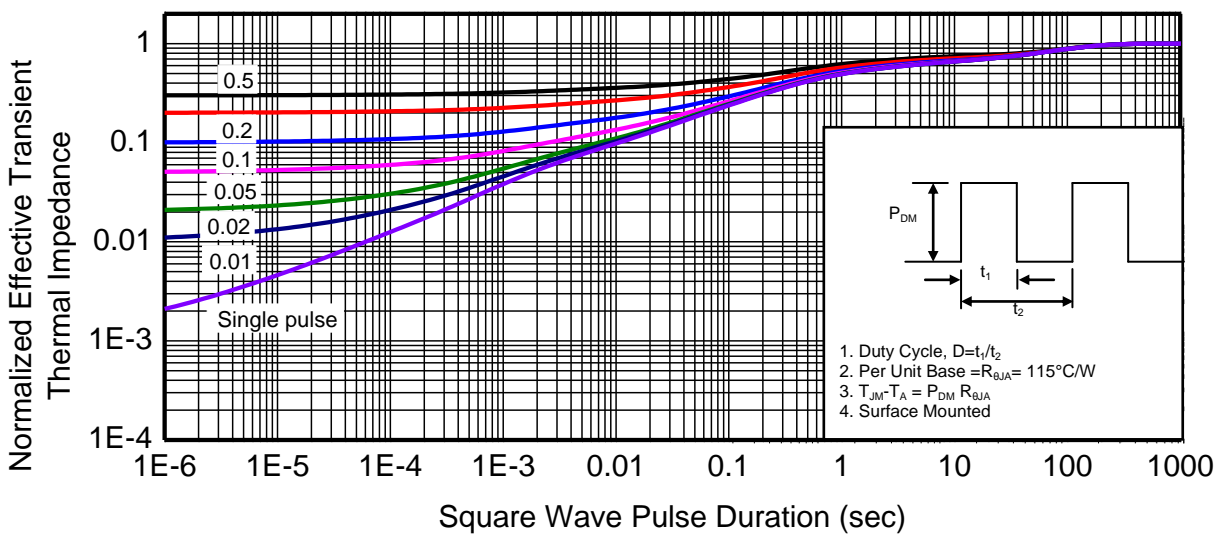
f 8.3ms single half sine-wave superimposed on rated load

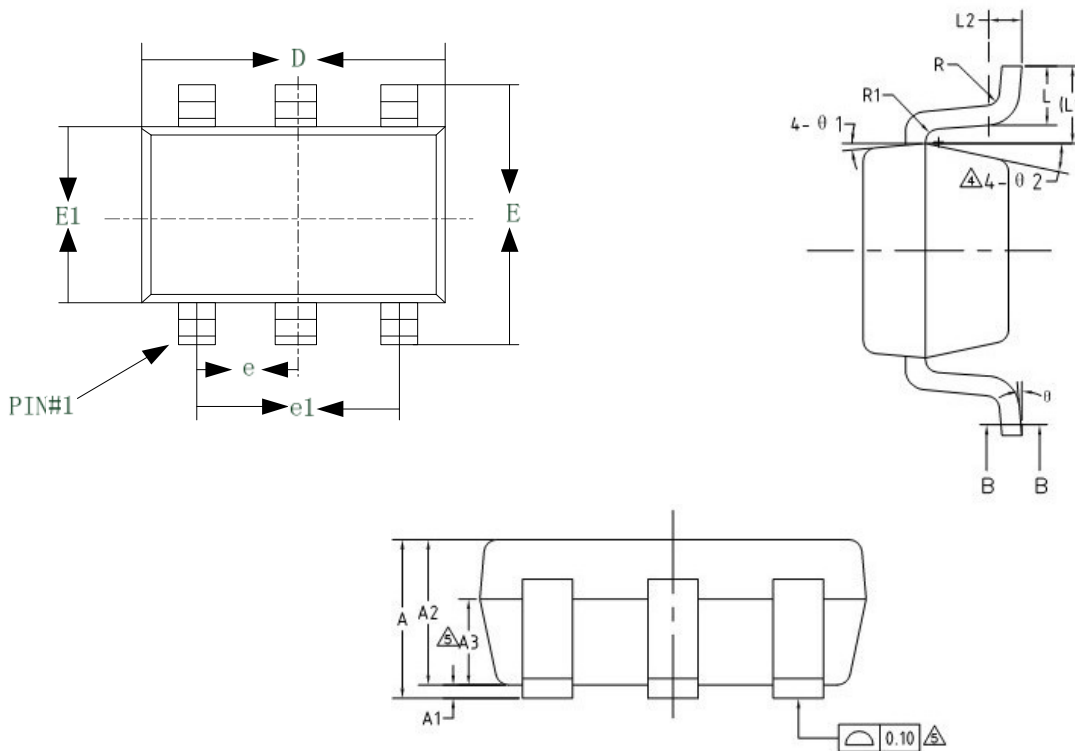
Electronics Characteristics (Ta=25°C, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0\text{ V}, I_D = -250\mu\text{A}$	-20			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -16\text{ V}, V_{GS} = 0\text{ V}$			-1	μA
Gate-to-source Leakage Current	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 8\text{ V}$			± 1	μA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS} = V_{DS}, I_D = -250\mu\text{A}$		-0.7	-1.0	V
Drain-to-source On-resistance	$R_{DS(on)}$	$V_{GS} = -4.5\text{ V}, I_D = -2.8\text{ A}$		93	115	m Ω
		$V_{GS} = -2.5\text{ V}, I_D = -2.0\text{ A}$		122	154	
		$V_{GS} = -1.8\text{ V}, I_D = -1.0\text{ A}$		160	198	
CHARGES, CAPACITANCES AND GATE RESISTANCE						
Input Capacitance	C_{ISS}	$V_{DS} = -10\text{ V}, V_{GS} = 0\text{ V}, F = 1\text{ MHz}$		531		pF
Output Capacitance	C_{OSS}			61		
Reverse Transfer Capacitance	C_{RSS}			54		
Total Gate Charge	$Q_{G(TOT)}$	$V_{DD} = -10\text{ V}, V_{GS} = -4.5\text{ V}, I_D = -2.8\text{ A}$		8.1		nC
Threshold Gate Charge	$Q_{G(TH)}$			0.82		
Gate-to-Source Charge	Q_{GS}			1.8		
Gate-to-Drain Charge	Q_{GD}			1.1		
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	$t_d(ON)$	$V_{DD} = -10\text{ V}, I_D = -1.2\text{ A},$ $V_{GS} = -4.5\text{ V}, R_G = 6\Omega$		21.6		ns
Rise Time	t_r			8.6		
Turn-Off Delay Time	$t_d(OFF)$			58		
Fall Time	t_f			8.4		
BODY DIODE CHARACTERISTICS						
Forward Voltage	V_{SD}	$V_{GS} = 0\text{ V}, I_S = -1.0\text{ A}$		-0.8	-1.5	V
SCHOTTKY DIODE CHARACTERISTICS						
Reverse breakdown voltage	V_R	$I_R = 100\mu\text{A}$	20			V
Forward voltage	V_{F1}	$I_F = 100\text{ mA}$		0.28	0.36	V
	V_{F2}	$I_F = 500\text{ mA}$		0.41	0.47	
Reverse current	I_R	$V_R = 20\text{ V}$			150	μA
Diode capacitance	C_D	$V_R = 5\text{ V}, F = 1\text{ MHz}$		15.8		pF

Typical Characteristics (Ta=25°C, unless otherwise noted)

Output characteristics

Transfer characteristics

On-Resistance vs. Drain current

On-Resistance vs. Gate-to-Source voltage

On-Resistance vs. Junction temperature

Threshold voltage vs. Temperature


Capacitor

Body diode forward voltage

Single pulse power (Junction-to-ambient)

Safe operating power

Gate charge Characteristics


Schottky Diode Forward Current

Schottky Diode Reverse Current

Schottky Capacitance

Transient thermal response (Junction-to-Ambient)

Package outline dimensions
SOT-23-6L


Symbol	Dimensions in millimeter		
	Min.	Typ.	Max.
A	-	-	1.25
A1	0	-	0.15
A2	1.00	1.10	1.20
A3	0.60	0.65	0.70
b	0.36	-	0.50
b1	0.36	0.38	0.45
c	0.14	-	0.20
c1	0.14	0.15	0.16
D	2.826	2.926	3.026
E	2.60	2.80	3.00
E1	1.526	1.626	1.726
e	0.90	0.95	1.00
e1	1.80	1.90	2.00
L	0.35	0.45	0.60
L1	0.59REF		
L2	0.25BSC		
R	0.10	-	-
R1	0.10	-	0.20
θ	0°		8°
θ1	3°	5°	7°
θ2	6°		14°