

General Description

The WSD30L90DN56 is the highest performance trench P-Channel MOSFET with extreme high cell density, which provide excellent $R_{DS(ON)}$ and gate charge for most of the synchronous buck converter applications.

The WSD30L90DN56 meet the RoHS and Green Product requirement 100% E_{AS} guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% E_{AS} Guaranteed
- Green Device Available

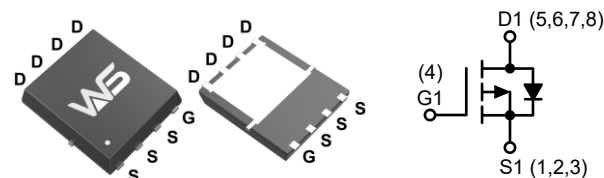
Product Summary

BV_{DSS}	$R_{DS(ON)}$	I_D
-30V	5.2m Ω	-90A

Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

DFN5X6-8L Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating		Units
		10s	Steady State	
V _{DS}	Drain-Source Voltage	-30		V
V _{GS}	Gate-Source Voltage	±25		
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ -10V ¹	-90		A
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ -10V ¹	-57		
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ -10V ¹	-27	-22	
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ -10V ¹	-24	-19	
I _{DM}	Pulsed Drain Current ²	-360		
E _{AS}	Single Pulse Avalanche Energy ³	88		mJ
I _{AS}	Avalanche Current	-42		A
P _D @T _C =25°C	Power Dissipation ⁴	40		W
P _D @T _A =25°C	Power Dissipation ⁴	6.3	6.15	
T _{STG}	Storage Temperature Range	-55 to 150		°C
T _J	Operating Junction Temperature Range	-55 to 150		

Thermal Data

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient ¹	---	50	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient ¹ ($t \leq 10s$)	---	20	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case ¹	---	1.6	

Electrical Characteristics ($T_J=25^{\circ}\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V$, $I_D=-250\mu A$	-30	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_D=-1mA$	---	-0.0332	---	V/ $^{\circ}\text{C}$
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=-10V$, $I_D=-25A$	---	5.2	6.4	m Ω
		$V_{GS}=-4.5V$, $I_D=-10A$	---	8.6	12	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=-250\mu A$	-1.3	-1.8	-2.3	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	4.4	---	mV/ $^{\circ}\text{C}$
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=-24V$, $V_{GS}=0V$, $T_J=25^{\circ}\text{C}$	---	---	1.0	μA
		$V_{DS}=-24V$, $V_{GS}=0V$, $T_J=55^{\circ}\text{C}$	---	---	5.0	
I_{GSS}	Gate-Body Leakage Current	$V_{GS}=\pm 20V$, $V_{DS}=0V$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=-5V$, $I_D=-30A$	---	28	---	S
R_g	Gate Resistance	$V_{DS}=0V$, $V_{GS}=0V$, $f=1.0MHz$	---	2.0	5.0	Ω
Q_g	Total Gate Charge(-4.5)	$V_{DS}=-15V$, $V_{GS}=-10V$, $I_D=-25A$	---	70	---	nC
Q_{gs}	Gate-Source Charge		---	10	---	
Q_{gd}	Gate-Drain Charge		---	18	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=-15V$, $V_{GEN}=-10V$, $R_G=6\Omega$, $I_D=-1A$, $R_L=15\Omega$	---	15	---	ns
T_r	Rise Time		---	19	---	
$T_{d(off)}$	Turn-Off Delay Time		---	88	---	
T_f	Fall Time		---	62	---	
C_{iss}	Input Capacitance	$V_{DS}=-15V$, $V_{GS}=0V$, $f=1.0MHz$	---	3200	---	pF
C_{oss}	Output Capacitance		---	640	---	
C_{rss}	Reverse Transfer Capacitance		---	600	---	

Guaranteed Avalanche Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
E_{AS}	Single Pulse Avalanche Energy ⁵	$V_{DD}=-25V$, $L=0.5mH$, $I_{AS}=-36A$	88	---	---	mJ

Diode Characteristics

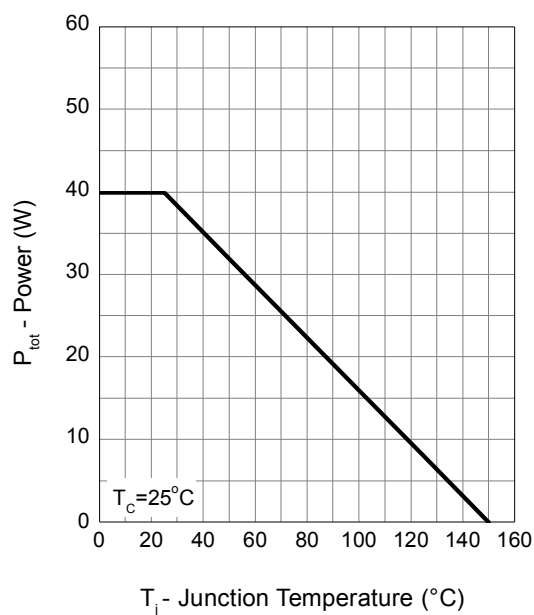
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
I_S	Continuous Source Current ^{1,6}	$V_G=V_D=0V$, Force Current	---	---	-45	A
I_{SM}	Pulsed Source Current ^{2,6}		---	---	-300	
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0V$, $I_S=-1A$, $T_J=25^{\circ}\text{C}$	---	---	-1.0	V
t_{rr}	Reverse Recovery Time	$I_F=-15A$, $dI/dt=100A/\mu s$, $T_J=25^{\circ}\text{C}$	---	30	---	ns
Q_{rr}	Reverse Recovery Charge		---	14	---	nC

Note:

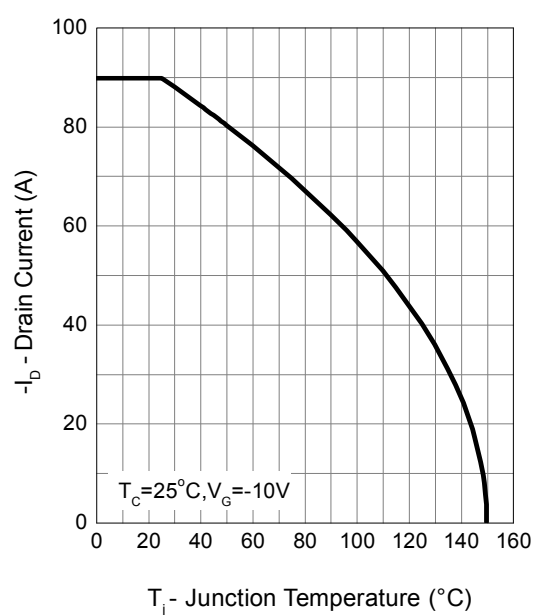
1. The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper, $t_s \leq 10\text{sec}$.
2. The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
3. The E_{AS} data shows Max. rating. The test condition is $V_{DD}=-25V$, $V_{GS}=-10V$, $L=0.5mH$, $I_{AS}=-36A$
4. The power dissipation is limited by 150°C junction temperature.
5. The Min. value is 100% E_{AS} tested guarantee.
6. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.

Typical Characteristics

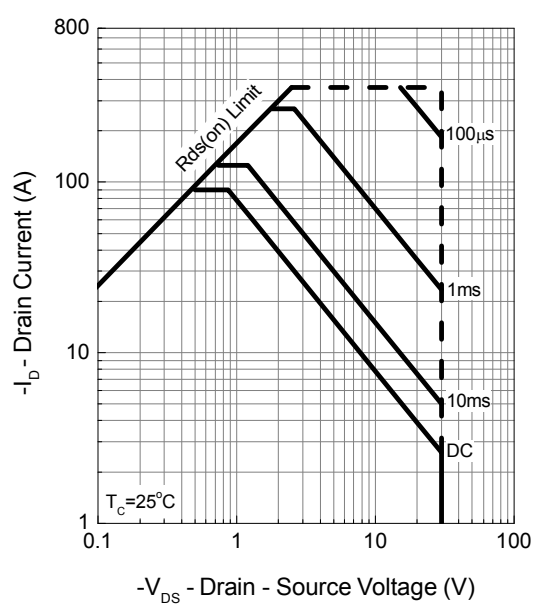
Power Dissipation



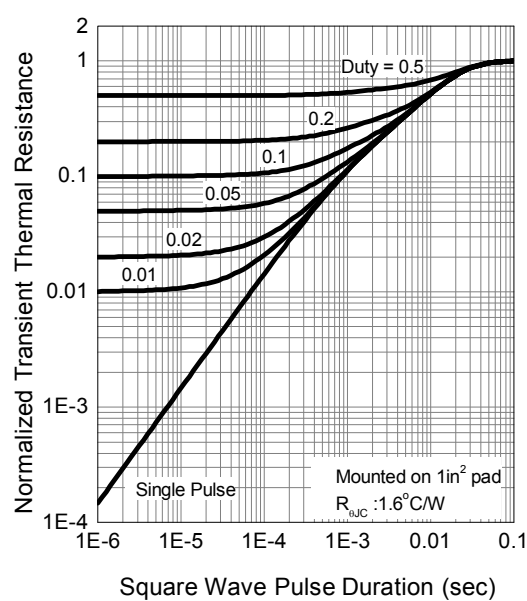
Drain Current



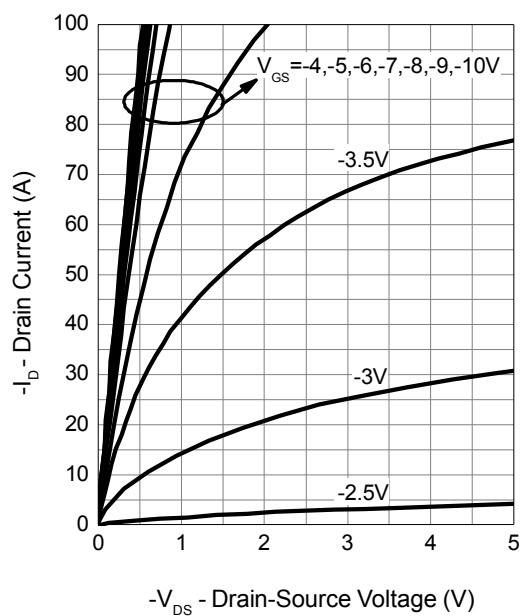
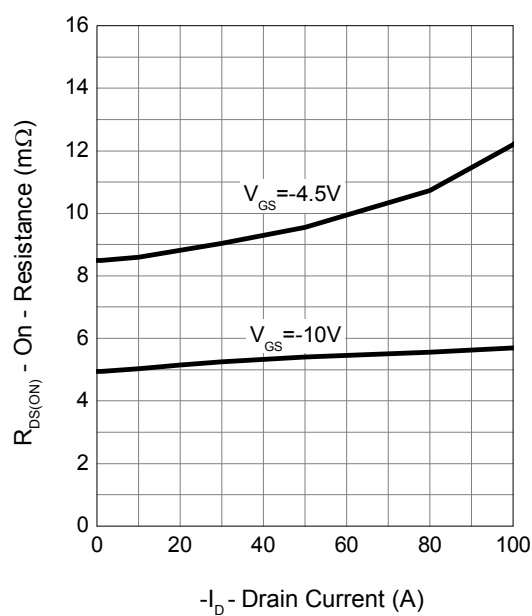
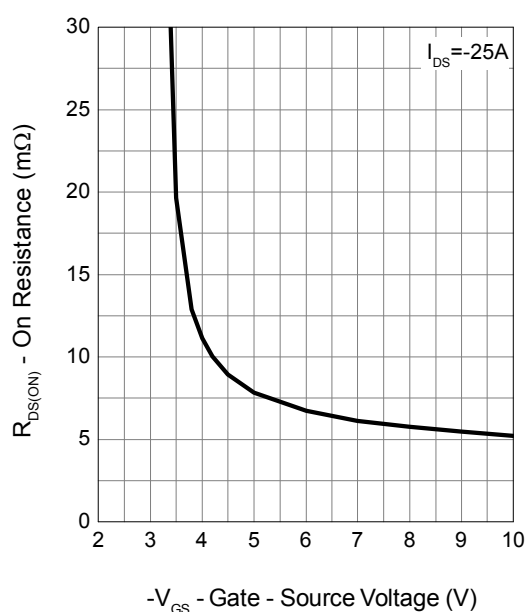
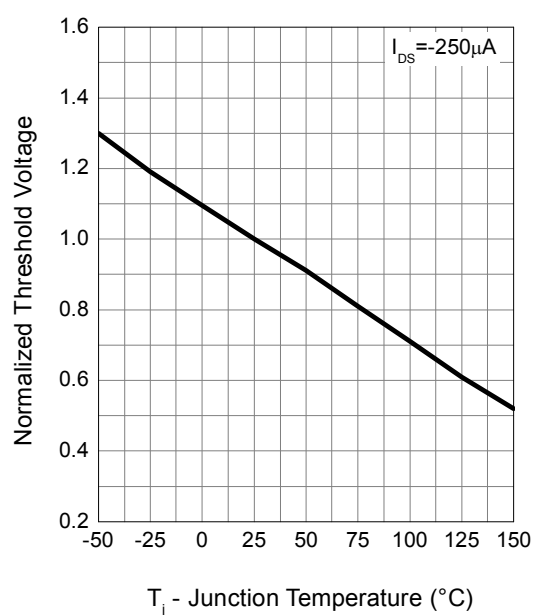
Safe Operation Area



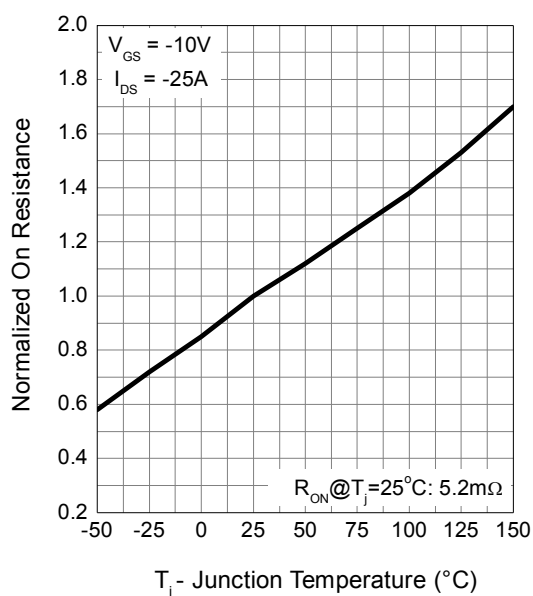
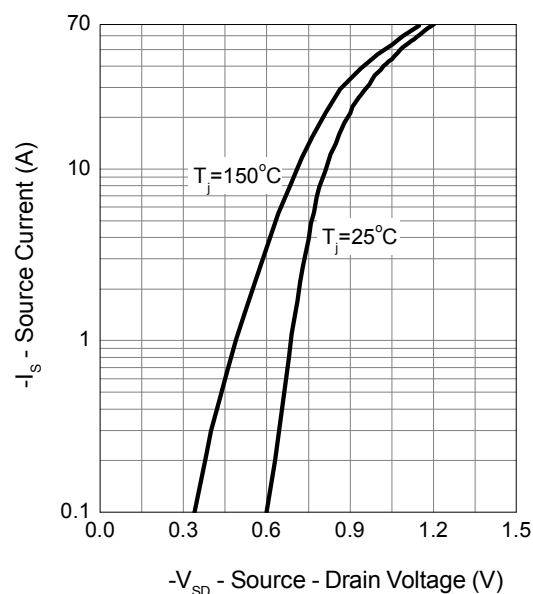
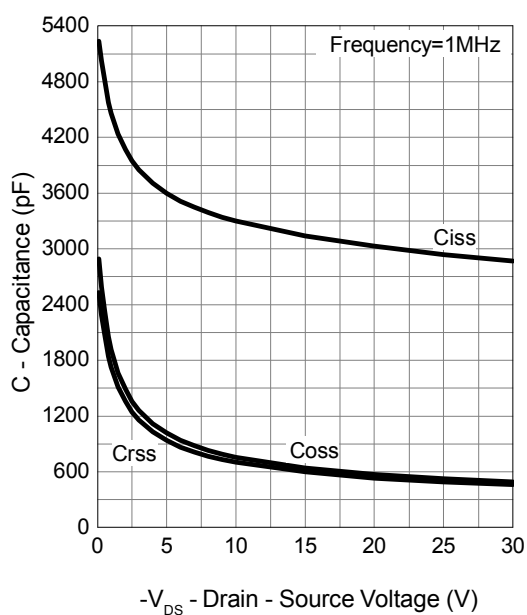
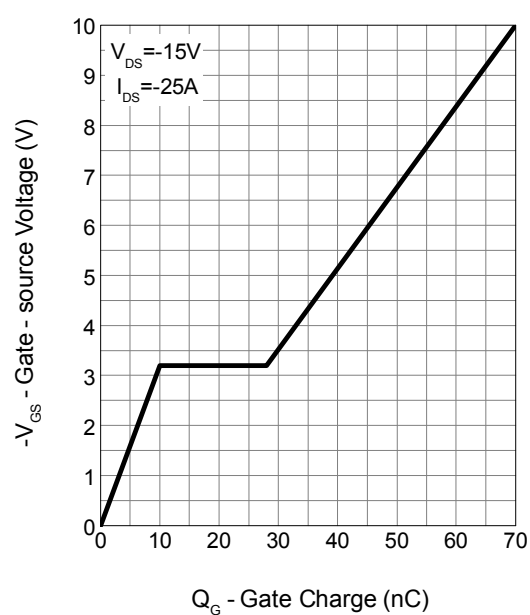
Thermal Transient Impedance



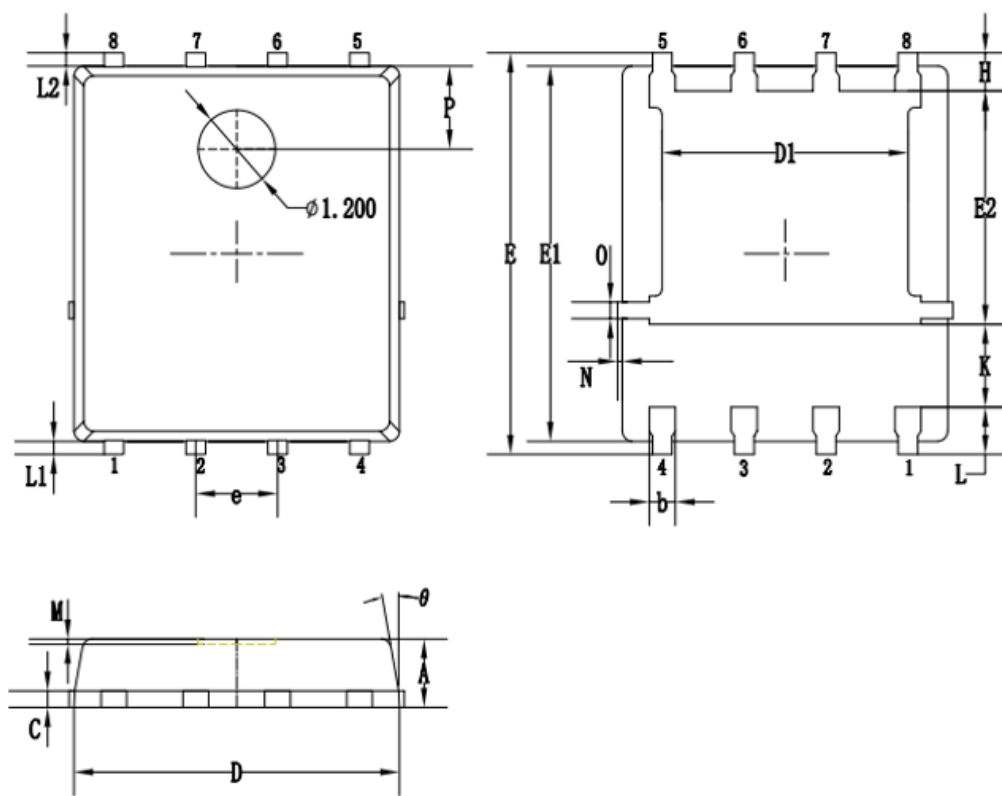
Typical Characteristics (Cont.)

Output Characteristics

Drain-Source On Resistance

Gate-Source On Resistance

Gate Threshold Voltage


Typical Characteristics (Cont.)

Drain-Source On Resistance

Source-Drain Diode Forward

Capacitance

Gate Charge


Packaging information



SYMBOLS	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.90	1.05	1.20
b	0.35	0.40	0.50
C	0.20	0.25	0.35
D	4.90	5.05	5.20
D1	3.72	3.82	3.92
E	6.00	6.15	6.30
E1	5.60	5.75	5.90
E2	3.47	3.57	3.67
e	1.27 BSC.		
H	0.48	0.58	0.68
K	1.17	1.27	1.37
L	0.64	0.74	0.84
L1/L2	0.20 REF.		
θ	8°	10°	12°
M	0.08 REF.		
N	0	-	0.15
O	0.25 REF.		
P	1.28 REF.		

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