

#### **N-Channel MOSFET**

### **General Description**

The WSD75N12GDN56 uses Super Trench technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{DS(ON)}$  and  $Q_G$ . This device is ideal for high frequency switching and synchronous rectification.

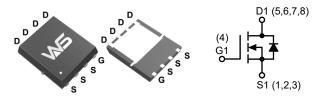
### **Product Summery**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	Ι <sub>D</sub>
120V	6.0mΩ	75A

#### Applications

- DC/DC Converter
- Load switch.

#### **DFN5X6-8L Pin Configuration**



### Features

- Excellent gate charge x R<sub>DS(ON)</sub> product(FOM)
- Very low on-resistance R<sub>DS(ON)</sub>
- 150°C operating temperature
- Pb-free lead plating
- 100% UIS tested.

### **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	120	V
V <sub>GS</sub>	V <sub>GS</sub> Gate-Source Voltage		v
-	Continuous Drain Current <sup>1</sup> ( T <sub>C</sub> =25°C)	75	
Ι <sub>D</sub>	Continuous Drain Current <sup>1</sup> ( T <sub>C</sub> =70°C)	70	
I <sub>DM</sub>	Pulsed Drain Current	320	— A
I <sub>AR</sub>	Single pulse avalanche current	40	
E <sub>AS</sub>	Single pulse avalanche energy	240	mJ
P <sub>D</sub>	Power Dissipation	125	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	
TJ	Operating Junction Temperature Range	Operating Junction Temperature Range -55 to 150	
TL	Maximum Temperature for Soldering	260	

### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Units
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient		50	°C/W
R <sub>θJC</sub>	Thermal Resistance, Junction-to-Case		1.0	C/W



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### **Electrical Characteristics** (T<sub>J</sub>=25°C, Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250µA	120			V
R <sub>DS(ON)</sub> <sup>1</sup>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =20A		6.0	6.8	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_{D}=250\mu A$	2.5	3.0	3.5	V
I <sub>DSS</sub>	Drain-Source Leakage Current	$V_{DS}$ =120V, $V_{GS}$ =0V			1.0	μA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ =±20V, $V_{DS}$ =0V			±100	nA
9 <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =50A		130		S
R <sub>g</sub>	Gate resistance	V <sub>DS</sub> =50V , V <sub>GS</sub> =0V , <i>f</i> =1.0MHz		2.5		Ω
Qg	Total Gate Charge			61.4		
Q <sub>gs</sub>	Gate-Source Charge	$V_{DS}$ =50V , $V_{GS}$ =10V , $I_{D}$ =20A		17.4		nC
Q <sub>gd</sub>	Gate-Drain Charge			14.1		
T <sub>d(on)</sub>	Turn-On Delay Time			20		
T <sub>r</sub>	Rise Time	$V_{DS}$ =50V, $V_{GS}$ =10V,		11		<b>n</b> 0
T <sub>d(off)</sub>	Turn-Off Delay Time	R <sub>G</sub> =5Ω , I <sub>D</sub> =20A		55		ns
T <sub>f</sub>	Fall Time			28		
C <sub>iss</sub>	Input Capacitance			4282		
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =50V , V <sub>GS</sub> =0V , <i>f</i> =1.0MHz		429		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			17		

#### **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
ا <sub>S</sub>	Continuous Source Current	T <sub>C</sub> =25°C			100	۸
I <sub>SM</sub>	Pulsed Source Current				320	A
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V,I <sub>S</sub> =6A			1.2	V
t <sub>rr</sub>	Reverse Recovery Time	· I <sub>S</sub> =20A, V <sub>DD</sub> =50V, di <sub>F</sub> /dt=100A/µs		100		ns
Q <sub>rr</sub>	Reverse Recovery Charge			250		nC

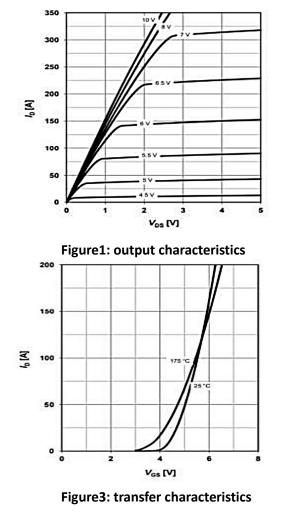
Note:

- 1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2. The data tested by pulsed, pulse width≤300µs, duty cycle≤2%.
- 3. The  $E_{AS}$  data shows Max. rating . The test condition is  $V_{DD}$ =50V, L=0.3mH, R<sub>G</sub>=25 $\Omega$ , Starting T<sub>J</sub>=25°C
- 4. The power dissipation is limited by 150  $^{\circ}\text{C}$  junction temperature.



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### **Typical Characteristics**



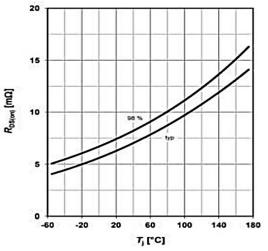


Figure5: Drain-source on-state resistance

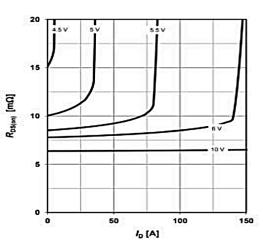


Figure2: Typcal drain-source on resistance

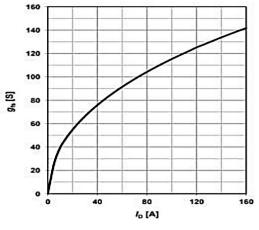


Figure4: forward transconductance

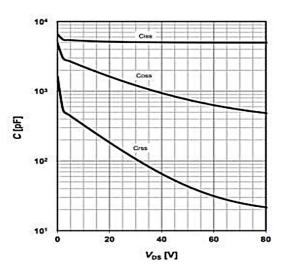


Figure6: Typ. capacitances



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### **Typical Characteristics (Cont.)**

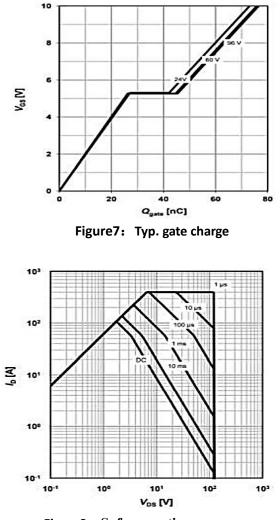


Figure9: Safe operating area

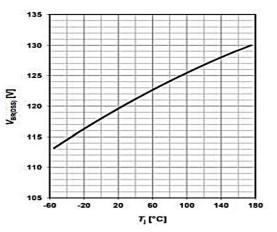


Figure8: Drain-source breakdown voltage

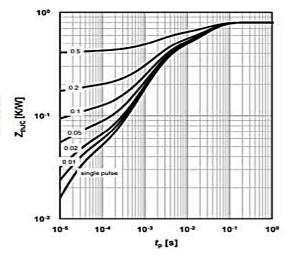
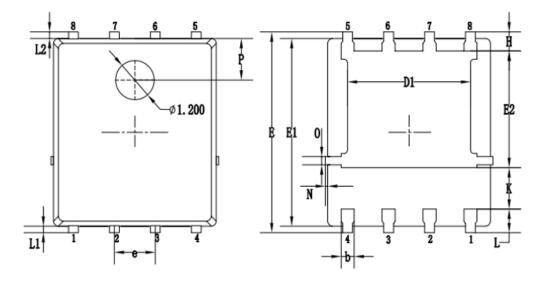


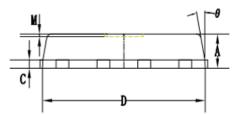
Figure 10: Max. transient thermal impedance



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### **Packaging information**





	MILLIMETERS				
SYMBOLS	MIN.	NOM.	MAX.		
A	0.90	1.05	1.20		
b	0.35	0.40	0.50		
С	0.20	0.25	0.35		
D	4.90	5.05	5.20		
D1	3.72	3.82	3.92		
E	6.00	6.15	6.30		
E1	5.60	5.75	5.90		
E2	3.47	3.57	3.67		
е		1.27 BSC.			
Н	0.48	0.58	0.68		
К	1.17	1.27	1.37		
L	0.64	0.74	0.84		
L1/L2		0.20 REF.			
θ	<b>8</b> °	10°	12°		
М		0.08 REF.			
N	0	-	0.15		
0		0.25 REF.			
Р		1.28 REF.			



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