

## General Description

The WSD86P10DN56 is the highest performance trench P-Channel MOSFET with extreme high cell density, which provide excellent  $R_{DS(ON)}$  and gate charge for most of the synchronous buck converter applications.

The WSD86P10DN56 meet the RoHS and Green Product requirement, 100%  $E_{AS}$  guaranteed with full function reliability approved.

## Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent  $CdV/dt$  effect decline
- 100%  $E_{AS}$  Guaranteed
- Green Device Available

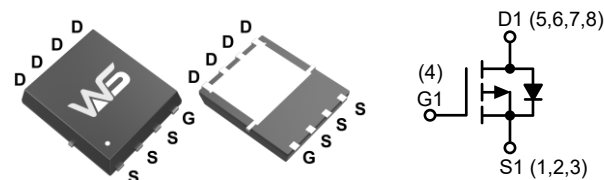
## Product Summary

$BV_{DSS}$	$R_{DS(ON)}$	$I_D$
-100V	17mΩ	-86A

## Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

## DFN5X6-8L Pin Configuration



## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-100	V
$V_{GS}$	Gate-Source Voltage	±20	
$I_D@T_C=25^{\circ}C$	Continuous Drain Current, $V_{GS} @ -10V$ <sup>1</sup>	-86	A
$I_D@T_C=100^{\circ}C$	Continuous Drain Current, $V_{GS} @ -10V$ <sup>1</sup>	-55	
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	-280	
$E_{AS}$	Single Pulse Avalanche Energy <sup>3</sup>	729	mJ
$I_{AS}$	Avalanche Current	-54	A
$P_D@T_C=25^{\circ}C$	Total Power Dissipation <sup>4</sup>	250	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^{\circ}C$
$T_J$	Operating Junction Temperature Range	-55 to 150	

## Thermal Data

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	---	62	$^{\circ}C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	---	0.5	

**Electrical Characteristics ( $T_J=25^{\circ}\text{C}$ , Unless Otherwise Noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V$ , $I_D=-250\mu A$	-100	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25^{\circ}\text{C}$ , $I_D=-1mA$	---	-0.021	---	$V/^{\circ}\text{C}$
$R_{DS(ON)}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=-10V$ , $I_D=-20A$	---	17	28	$m\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=-250\mu A$	-1.4	-2.1	-2.8	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-4.08	---	$mV/^{\circ}\text{C}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=-80V$ , $V_{GS}=0V$ , $T_J=25^{\circ}\text{C}$	---	---	-1.0	$\mu A$
		$V_{DS}=-80V$ , $V_{GS}=0V$ , $T_J=55^{\circ}\text{C}$			-5.0	
$I_{GSS}$	Gate-Body Leakage Current	$V_{GS}=\pm 20V$ , $V_{DS}=0V$	---	---	$\pm 100$	nA
$g_{fs}$	Forward Transconductance	$V_{DS}=-10V$ , $I_D=-20A$	30	---	---	S
$Q_g$	Total Gate Charge (-4.5V)	$V_{DS}=-30V$ , $V_{GS}=-10V$ , $I_D=-20A$	---	110	---	nC
$Q_{gs}$	Gate-Source Charge		---	15	---	
$Q_{gd}$	Gate-Drain Charge		---	18	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=-30V$ , $V_{GS}=-10V$ , $R_G=6\Omega$ , $I_D=-10A$ , $R_L=30\Omega$	---	27	---	ns
$T_r$	Rise Time		---	15	---	
$T_{d(off)}$	Turn-Off Delay Time		---	104	---	
$T_f$	Fall Time		---	57	---	
$C_{iss}$	Input Capacitance	$V_{DS}=-30V$ , $V_{GS}=0V$ , $f=1.0MHz$	---	6105	---	pF
$C_{oss}$	Output Capacitance		---	728	---	
$C_{rss}$	Reverse Transfer Capacitance		---	258	---	

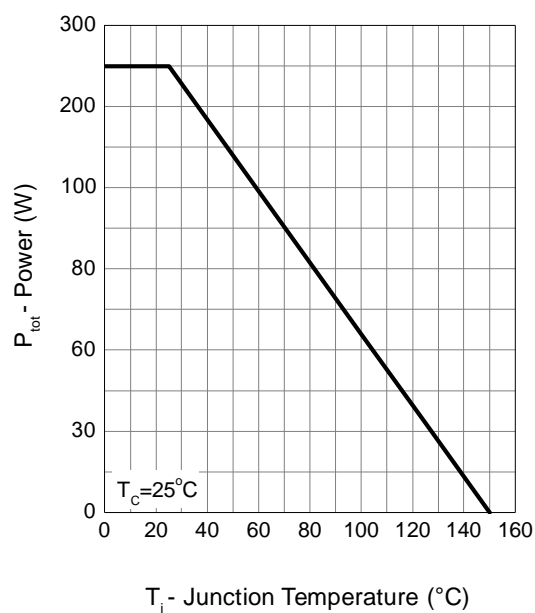
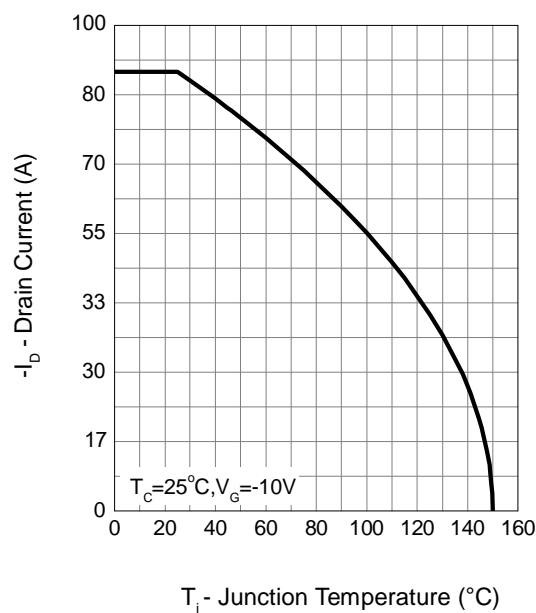
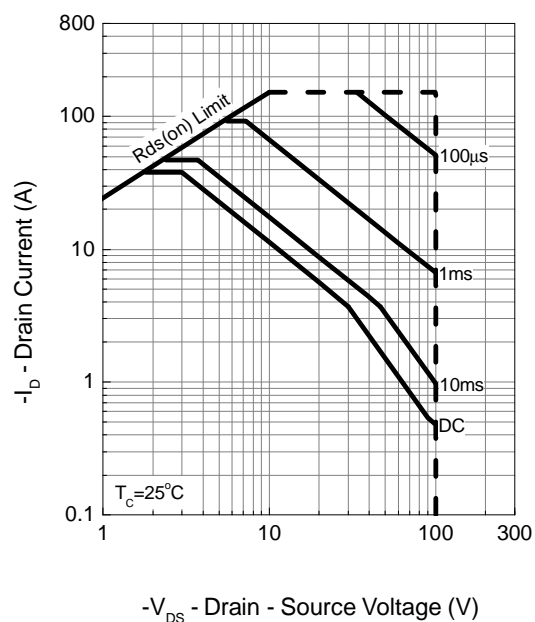
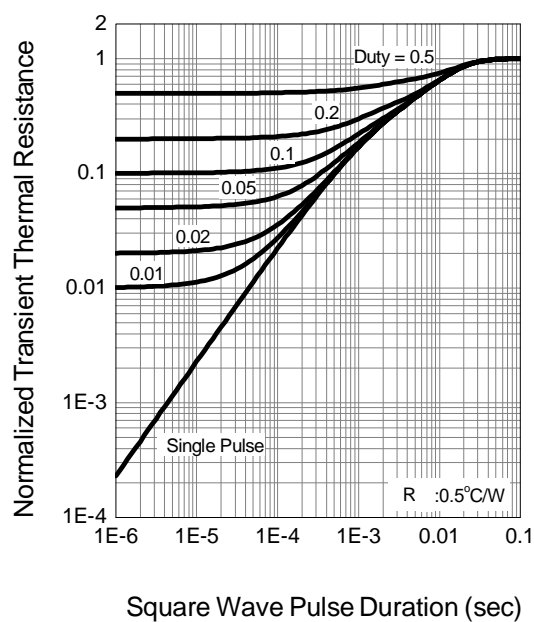
**Diode Characteristics**

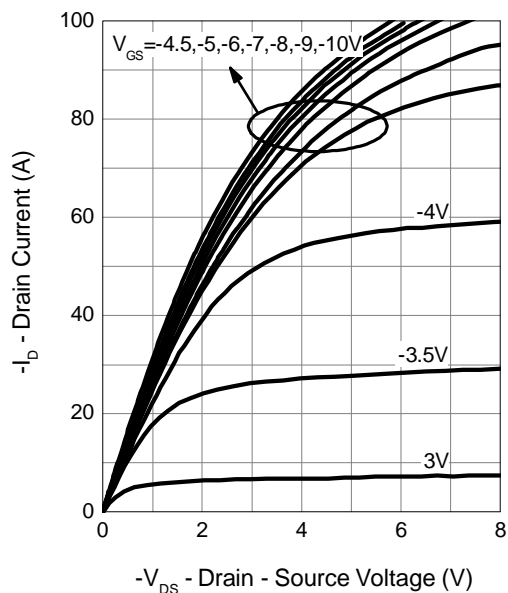
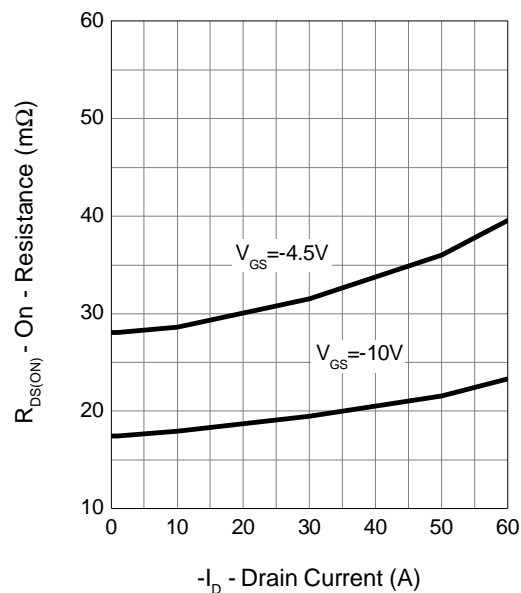
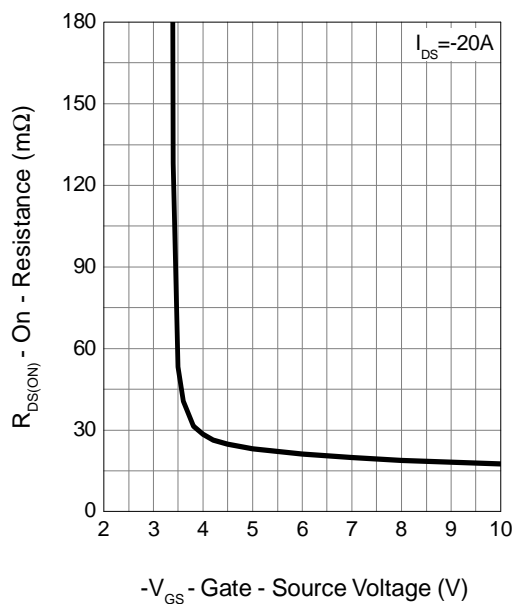
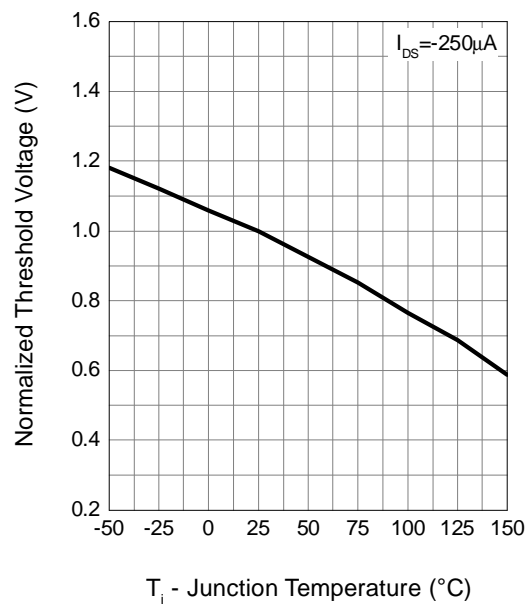
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
$I_S$	Continuous Source Current <sup>1,6</sup>	$V_G=V_D=0V$ , Force Current	---	---	-86	A
$V_{SD}$	Diode Forward Voltage <sup>2</sup>	$V_{GS}=0V$ , $I_S=-10$ , $T_J=25^{\circ}\text{C}$	---	---	-1.2	V

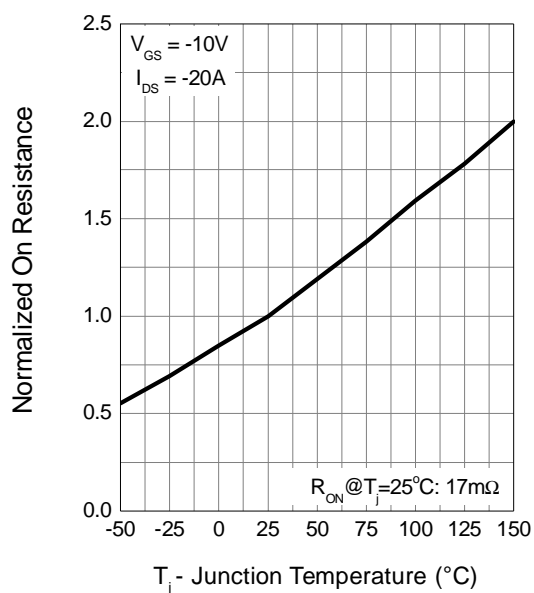
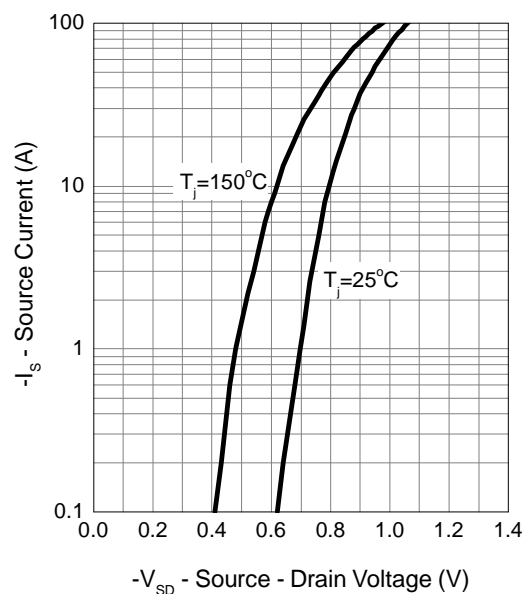
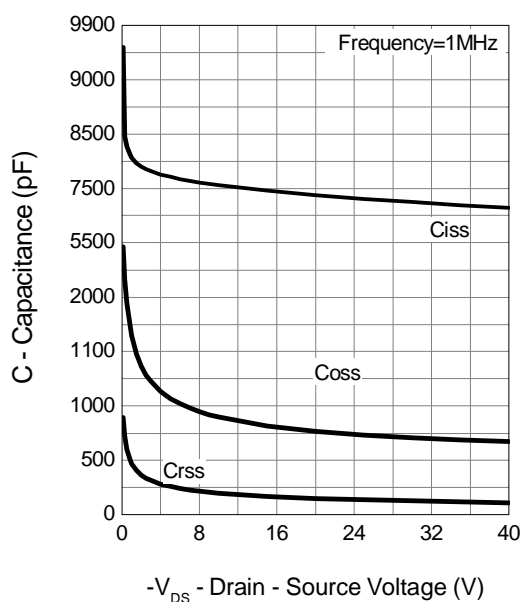
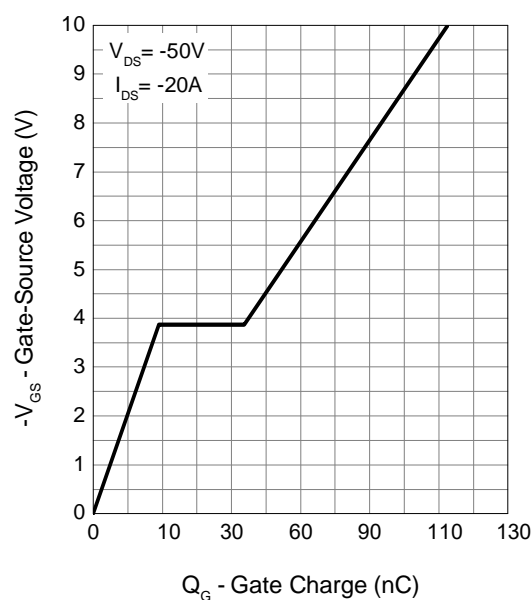
**Note:**

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper,  $t \leq 10\text{sec}$ .
2. The data tested by pulsed, pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$ .
3. The  $E_{AS}$  data shows Max. rating. The test condition is  $V_{DD}=-30V$ ,  $V_{GS}=-10V$ ,  $L=0.5mH$ ,  $I_{AS}=-54A$ .
4. The power dissipation is limited by  $150^{\circ}\text{C}$  junction temperature.
5. The Min. value is 100%  $E_{AS}$  tested guarantee.
6. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.

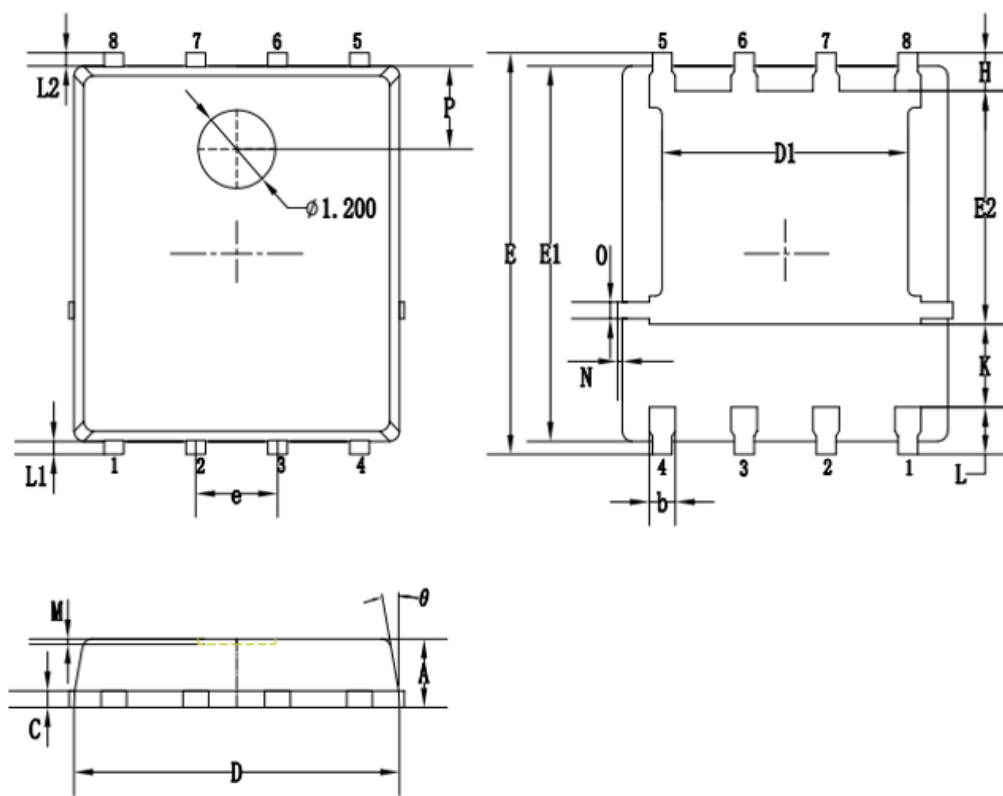
## Typical Characteristics

**Power Dissipation**

**Drain Current**

**Safe Operation Area**

**Thermal Transient Impedance**


**Typical Characteristics (Cont.)**
**Output Characteristics**

**Drain-Source On Resistance**

**Gate-Source On Resistance**

**Gate Threshold Voltage**


**Typical Characteristics (Cont.)**
**Drain-Source On Resistance**

**Source-Drain Diode Forward**

**Capacitance**

**Gate Charge**


## Packaging information



SYMBOLS	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.90	1.05	1.20
b	0.35	0.40	0.50
C	0.20	0.25	0.35
D	4.90	5.05	5.20
D1	3.72	3.82	3.92
E	6.00	6.15	6.30
E1	5.60	5.75	5.90
E2	3.47	3.57	3.67
e	1.27 BSC.		
H	0.48	0.58	0.68
K	1.17	1.27	1.37
L	0.64	0.74	0.84
L1/L2	0.20 REF.		
$\theta$	8°	10°	12°
M	0.08 REF.		
N	0	-	0.15
O	0.25 REF.		
P	1.28 REF.		

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