

Surface Mount P-Channel Enhancement Mode MOSFET

(Pb) Lead(Pb)-Free

Features:

- * Super high dense
- * Cell design for low $R_{DS(ON)}$
- * $R_{DS(ON)} < 10\text{m}\Omega @ V_{GS} = -10\text{V}$
- * $R_{DS(ON)} < 13\text{m}\Omega @ V_{GS} = -4.5\text{V}$
- * Simple Drive Requirement
- * Lower On-resistance
- * Fast Switching

Description:

The WTK6679 provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

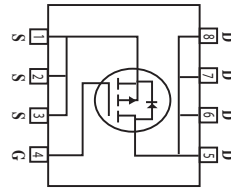
The SOP-8 package is universally preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

Maximum Ratings ($T_A = 25^\circ\text{C}$ Unless Otherwise Specified)

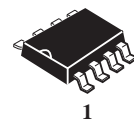
Rating	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 25	V
Continuous Drain Current ($T_A = 25^\circ\text{C}$) ($T_A = 70^\circ\text{C}$)	I_D	-14 -8.9	A
Pulsed Drain Current ⁽¹⁾	I_{DM}	-50	A
Power Dissipation ($T_A = 25^\circ\text{C}$)	P_D	2.5	W
Maximax Junction-to-Ambient	$R_{\theta JA}$	50	$^\circ\text{C}/\text{W}$
Operating Junction Temperature Range	T_J	+150	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	-55 to +150	$^\circ\text{C}$

Device Marking

WTK6679=6679SC



DRAIN CURRENT
-14 AMPERES
DRAIN SOURCE VOLTAGE
-30 VOLTAGE



SOP-8

WTK6679**WEITRON****Electrical Characteristics** ($T_A=25^{\circ}\text{C}$ Unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage $V_{GS}=0V, I_D=-250\ \mu\text{A}$	$V_{(BR)DSS}$	-30	-	-	V
Gate-Source Threshold Voltage $V_{DS}=V_{GS}, I_D=-250\ \mu\text{A}$	$V_{GS(th)}$	-1.0	-	-3.0	V
Gate-Source Leakage Current $V_{DS}=0V, V_{GS}=\pm 25V$	I_{GSS}	-	-	± 100	nA
Zero Gate Voltage Drain Current $V_{DS}=-30V, V_{GS}=0V$ $V_{DS}=-24V, V_{GS}=0V$	I_{DSS}	-	-	-1 -25	μA
Drain-Source On-Resistance $V_{GS}=-10V, I_D=-14A$ $V_{GS}=-4.5V, I_D=-11A$	$R_{DS(on)}$	- -	- -	10 13	m Ω
Forward Transconductance $V_{DS}=-10V, I_D=-14A$	g_{fs}	-	26	-	S

Dynamic

Input Capacitance $V_{DS}=-25V, V_{GS}=0V, f=1\text{MHz}$	C_{iss}	-	2860	4580	pF
Output Capacitance $V_{DS}=-25V, V_{GS}=0V, f=1\text{MHz}$	C_{oss}	-	950	-	
Reverse Transfer Capacitance $V_{DS}=-25V, V_{GS}=0V, f=1\text{MHz}$	C_{rss}	-	640	-	

Switching

Turn-On Delay Time ⁽²⁾ $V_{DS} = -15V, I_D = -1A, V_{GS} = -10V, R_G = 3.3\Omega, R_D = 15\Omega$	$t_{d(on)}$	-	13	-	nS
Rise Time $V_{DS} = -15V, I_D = -1A, V_{GS} = -10V, R_G = 3.3\Omega, R_D = 15\Omega$	t_r	-	11	-	nS
Turn-Off Time $V_{DS} = -15V, I_D = -1A, V_{GS} = -10V, R_G = 3.3\Omega, R_D = 15\Omega$	$t_{d(off)}$	-	58	-	nS
Fall Time $V_{DS} = -15V, I_D = -1A, V_{GS} = -10V, R_G = 3.3\Omega, R_D = 15\Omega$	t_f	-	43	-	nS
Total Gate Charge ⁽²⁾ $V_{DS}=-24V, I_D=-14A, V_{GS}=-4.5V$	Q_g	-	37	60	nC
Gate-Source Charge $V_{DS}=-24V, I_D=-14A, V_{GS}=-4.5V$	Q_{gs}	-	3	-	nC
Gate-Drain Charge $V_{DS}=-24V, I_D=-14A, V_{GS}=-4.5V$	Q_{gd}	-	25	-	nC
Forward On Voltage ⁽²⁾ $V_{GS}=0V, I_S=-2A$	V_{SD}	-	-	-1.2	V
Reverse Recovery Time ⁽²⁾ $V_{GS}=0V, I_S=-14A, dl/dt=100A/\mu\text{s}$	T_{rr}	-	48	-	nS
Reverse Recovery Charge $V_{GS}=0V, I_S=-14A, dl/dt=100A/\mu\text{s}$	Q_{rr}	-	46	-	nC

Notes: 1. Pulse width limited by Max. junction temperature.

2. Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.3. Surface mounted on 1 in² copper pad of FR4 board; 125°C/W when mounted on Min. copper pad.

Characteristics Curve

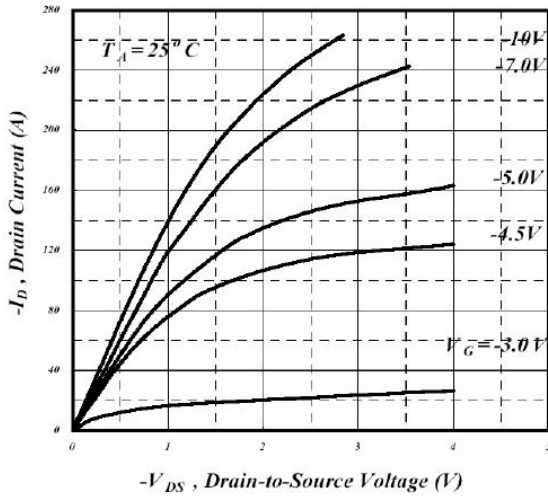


Fig 1. Typical Output Characteristics

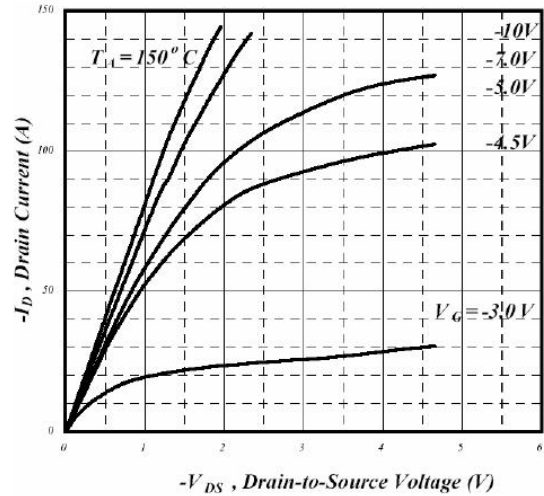


Fig 2. Typical Output Characteristics

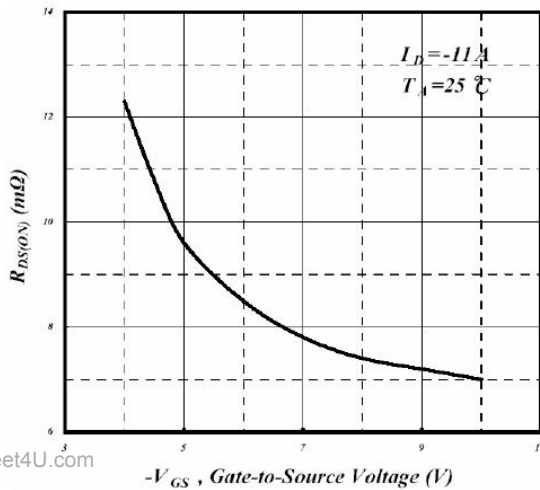


Fig 3. On-Resistance v.s. Gate Voltage

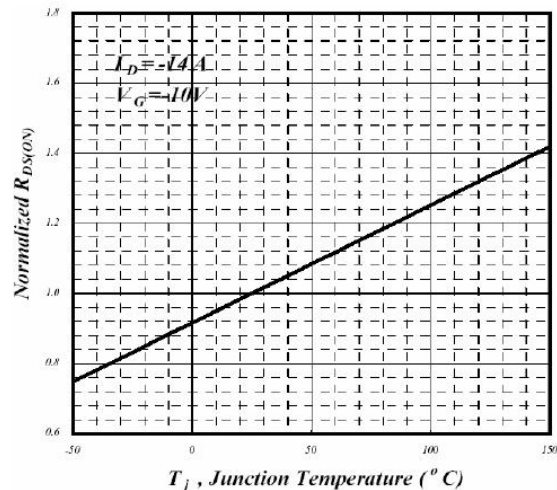


Fig 4. Normalized On-Resistance v.s. Junction Temperature

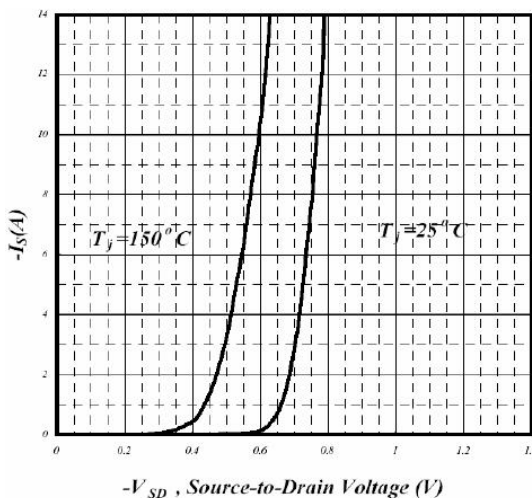


Fig 5. Forward Characteristics of Reverse Diode

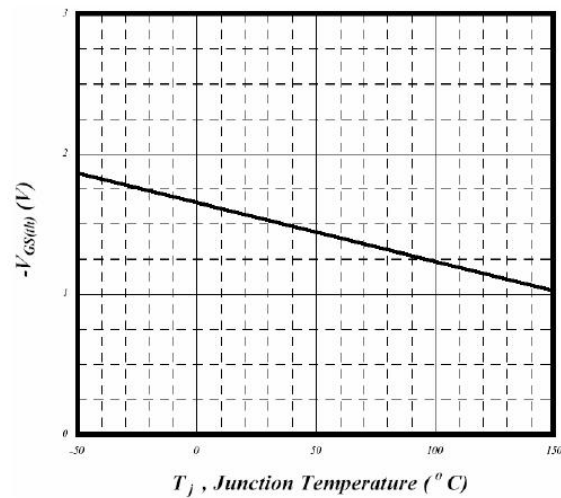


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

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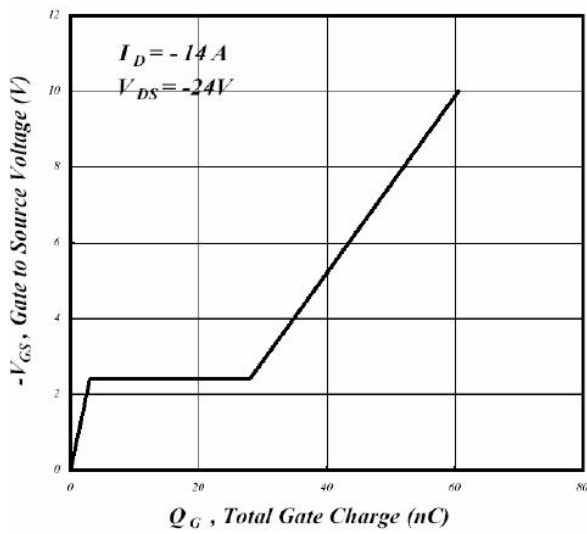


Fig 7. Gate Charge Characteristics

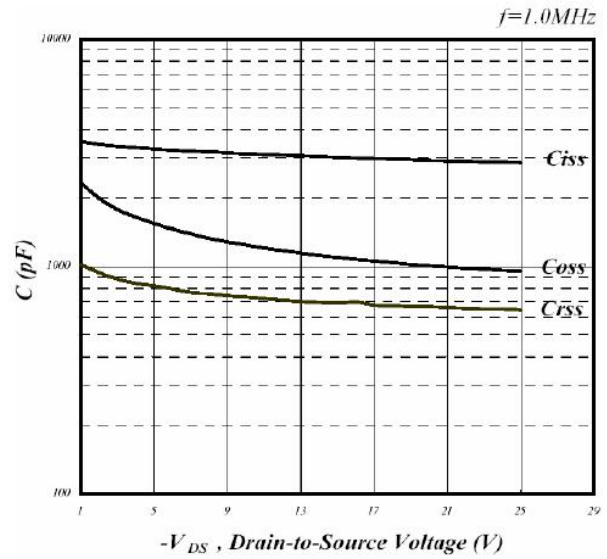


Fig 8. Typical Capacitance Characteristics

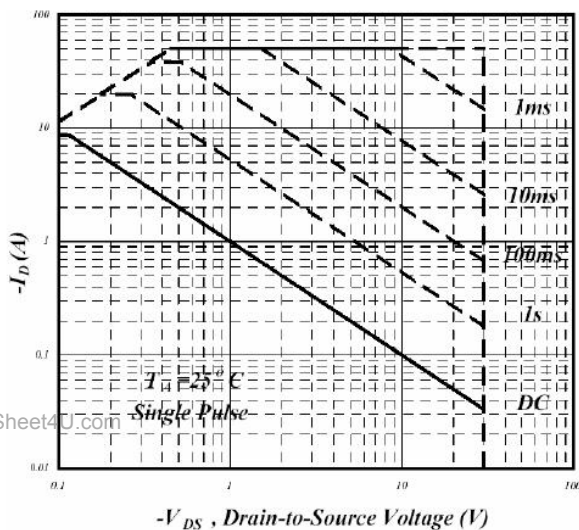


Fig 9. Maximum Safe Operating Area

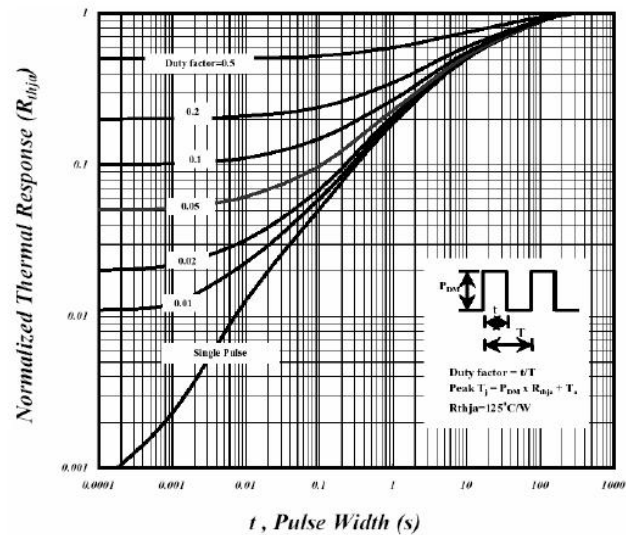


Fig 10. Effective Transient Thermal Impedance

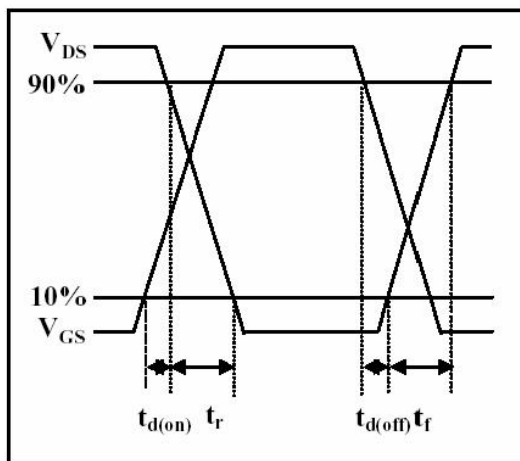


Fig 11. Switching Time Waveform

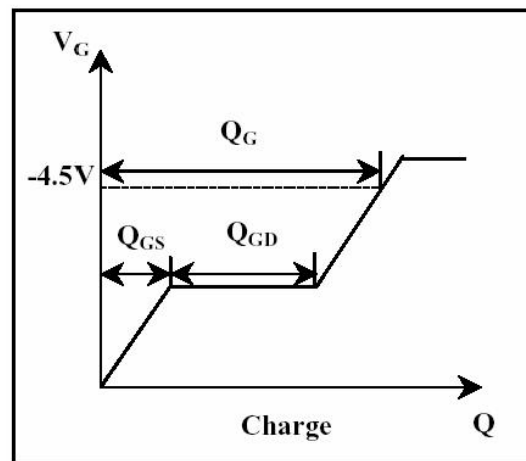
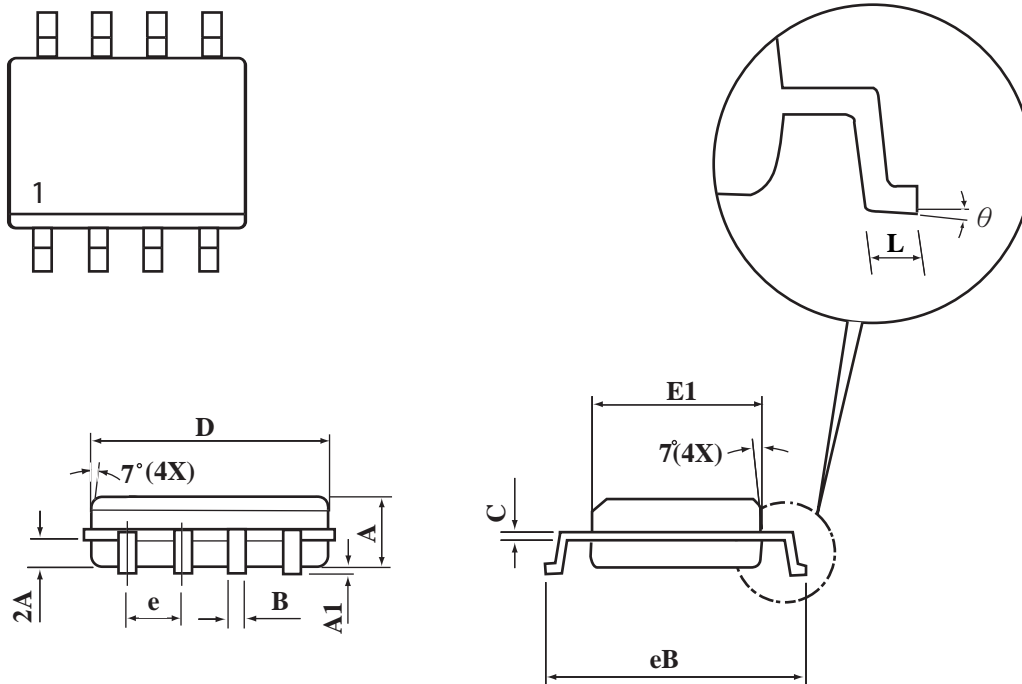


Fig 12. Gate Charge Waveform

WTK6679**WEITRON****SOP-8 Package Outline Dimensions**

Unit:mm



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SYMBOLS	MILLIMETERS	
	MIN	MAX
A	1.35	1.75
A1	0.10	0.20
B	0.35	0.45
C	0.18	0.23
D	4.69	4.98
E1	3.56	4.06
eB	5.70	6.30
e	1.27 BSC	
L	0.60	0.80
θ	0°	8°