XN01504 (XN1504)

Silicon NPN epitaxial planer transistor

For amplification of low frequency output

Features

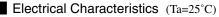
- Two elements incorporated into one package. (Emitter-coupled transistors)
- Reduction of the mounting area and assembly cost by one half.

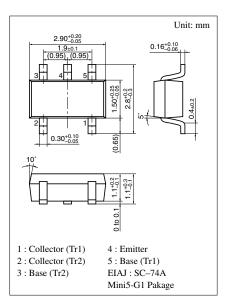
Basic Part Number of Element

• 2SD1915F \times 2 elements

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Parameter		Symbol	Ratings	Unit				
Rating of element	Collector to base voltage	V _{CBO}	50	V				
	Collector to emitter voltage	V _{CEO}	20	V				
	Emitter to base voltage	V _{EBO}	25	V				
	Collector current	I _C	300	mA				
	Peak collector current	I _{CP}	500	mA				
Overall	Total power dissipation	P _T	300	mW				
	Junction temperature	Tj	150	°C				
	Storage temperature	T _{stg}	-55 to +150	°C				

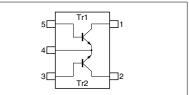
Absolute Maximum Ratings (Ta=25°C)





Marking Symbol: 5S

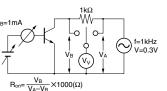
Internal Connection



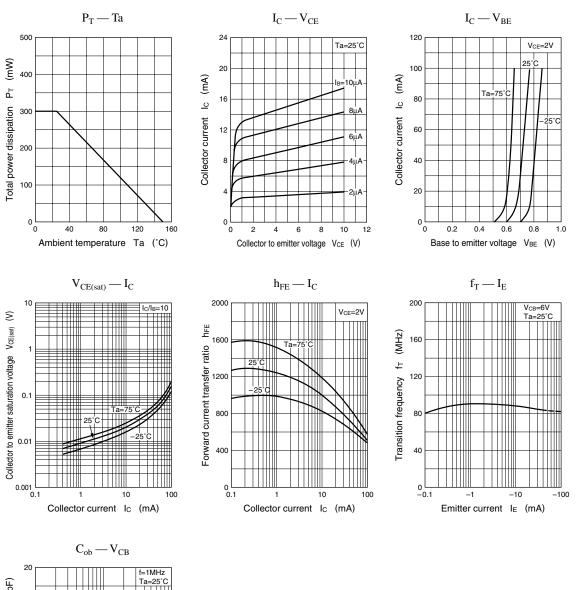
Parameter	Symbol	Conditions	min	typ	max	Unit
Collector to emitter voltage	V _{CEO}	$I_{\rm C} = 1 {\rm mA}, I_{\rm B} = 0$	20			V
Collector cutoff current	I _{CBO}	$V_{CB} = 50V, I_E = 0$			0.1	μΑ
Emitter cutoff current	I _{EBO}	$V_{EB} = 25V, I_C = 0$			0.1	μΑ
Forward current transfer ratio	h _{FE}	$V_{CE} = 2V, I_C = 4mA$	500		2500	
Forward current transfer h_{FE} ratio	h _{FE} (small/large)*1	$V_{CE} = 2V, I_C = 4mA$	0.5	0.99		
Collector to emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = 30 {\rm mA}, I_{\rm B} = 3 {\rm mA}$			0.1	V
Base to emitter voltage	V _{BE}	$V_{CE} = 2V, I_C = 4mA$		0.6		V
Transition frequency	f _T	$V_{CB} = 6V, I_E = -4mA, f = 200MHz$		80		MHz
Collector output capacitance	C _{ob}	$V_{CB} = 10V, I_E = 0, f = 1MHz$			7	pF
ON Resistance	R _{on} ^{*2}			1.0		Ω

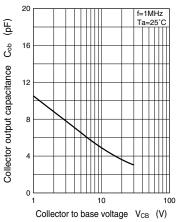
*1 Ratio between 2 elements

*2 Ron test circuit



Note) The Part number in the Parenthesis shows conventional part number.





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