XN04502 (XN4502)

Silicon NPN epitaxial planar type

For general amplification

■ Features

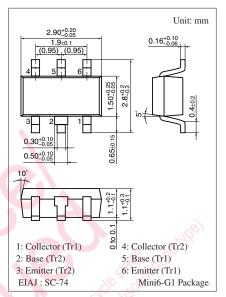
- Two elements incorporated into one package
- Reduction of the mounting area and assembly cost by one half

■ Basic Part Number

• 2SD0602A (2SD602A) × 2

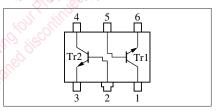
■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V _{CBO}	60	V	
Collector-emitter voltage (Base open)	V _{CEO}	50	V	
Emitter-base voltage (Collector open)	V _{EBO}	5	V	
Collector current	I_C	0.5	A	
Peak collector current	I _{CP}	1	A	
Total power dissipation	P _T	300	mW	
Junction temperature	T_{j}	150	°C	
Storage temperature	T_{stg}	-55 to +150	°C	



Marking Symbol: 5Q

Internal Connection



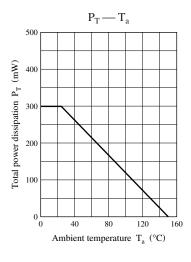
■ Electrical Characteristics $T_a = 25$ °C ± 3 °C

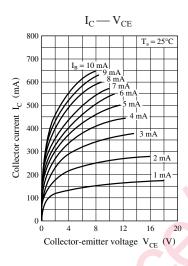
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_C = 10 \mu\text{A}, I_E = 0$	60			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_C = 2 \text{ mA}, I_B = 0$	50			V
Emitter-base voltage (Collector open)	V_{EBO}	$I_E = 10 \mu A, I_C = 0$	5			V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{CB} = 20 \text{ V}, I_E = 0$			0.1	μΑ
Forward current transfer ratio	$h_{\rm FE1}$	$V_{CE} = 10 \text{ V}, I_{C} = 150 \text{ mA}$	85		340	_
6.	h _{FE2} *	$V_{CE} = 10 \text{ V}, I_{C} = 500 \text{ mA}$	40			
Collector-emitter saturation voltage	V _{CE(sat)}	$I_C = 300 \text{ mA}, I_B = 30 \text{ mA}$		0.35	0.60	V
Transition frequency	f_T	$V_{CB} = 10 \text{ V}, I_{E} = -50 \text{ mA}, f = 200 \text{ MHz}$		200		MHz
Collector output capacitance	C _{ob}	$V_{CB} = 10 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$		6	15	pF
(Common base, input open circuited)						

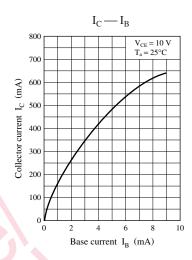
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

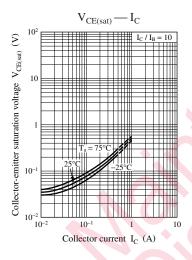
2. *: Pulse measurement

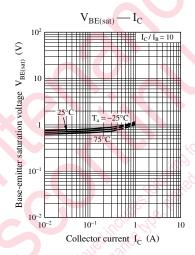
Note) The part number in the parenthesis shows conventional part number.

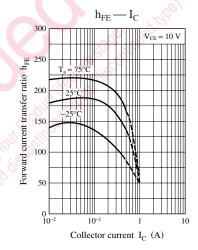


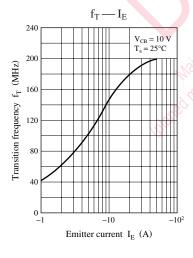


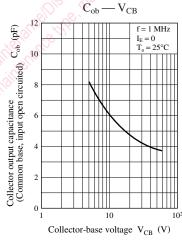


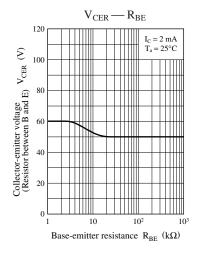












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