XN04505G

Silicon NPN epitaxial planar type

For general amplification (Tr1)
For amplification of low-frequency output (Tr2)

■ Features

- Two elements incorporated into one package
- Reduction of the mounting area and assembly cost by one half

■ Basic Part Number

- 2SD0601A + 2SD1328

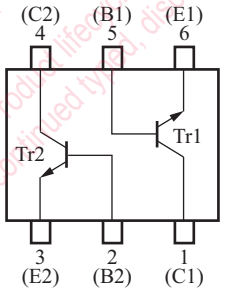
■ Package

- Code
  Mini6-G3
- Pin Name

1: Collector (Tr1)
2: Base (Tr2)
3: Emitter (Tr2)
4: Collector (Tr2)
5: Base (Tr1)
6: Emitter (Tr1)

■ Marking Symbol: DZ

■ Internal Connection



■ Absolute Maximum Ratings $T_a = 25°C$

Parameter		Symbol	Rating	Unit
Tr1	Collector-base voltage (Emitter open)	V_{CBO}	60	V
	Collector-emitter voltage (Base open)	V_{CEO}	50	V
	Emitter-base voltage (Collector open)	V_{EBO}	7	V
	Collector current	I_C	100	mA
	Peak collector current	I_{CP}	200	mA
Tr2	Collector-base voltage (Emitter open)	V_{CBO}	25	V
	Collector-emitter voltage (Base open)	V_{CEO}	20	V
	Emitter-base voltage (Collector open)	V_{EBO}	12	V
	Collector current	I_C	0.5	A
	Peak collector current	I_{CP}	1	A
Overall	Total power dissipation	P_T	300	mW
	Junction temperature	T_j	150	°C
	Storage temperature	T_{stg}	−55 to +150	°C

■ Electrical Characteristics $T_a = 25°C \pm 3°C$

• Tr1

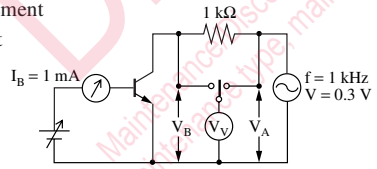
Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Collector-base voltage (Emitter open)	V_{CBO}	$I_C = 10\ \mu A$, $I_E = 0$	60			V
Collector-emitter voltage (Base open)	V_{CEO}	$I_C = 2$ mA, $I_B = 0$	50			V
Emitter-base voltage (Collector open)	V_{EBO}	$I_E = 10\ \mu A$, $I_C = 0$	7			V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{CB} = 20$ V, $I_E = 0$			0.1	μA
Collector-emitter cutoff current (Base open)	I_{CEO}	$V_{CE} = 10$ V, $I_B = 0$			100	μA
Forward current transfer ratio	h_{FE}	$V_{CE} = 10$ V, $I_C = 2$ mA	160		460	—
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C = 100$ mA, $I_B = 10$ mA		0.3	0.5	V
Transition frequency	f_T	$V_{CB} = 10$ V, $I_E = -2$ mA, f = 200 MHz		150		MHz
Collector output capacitance (Common base, input open circuited)	C_{ob}	$V_{CB} = 10$ V, $I_E = 0$, f = 1 MHz		3.5		pF

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

• Tr2

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Collector-base voltage (Emitter open)	V_{CBO}	$I_C = 10\ \mu A$, $I_E = 0$	25			V
Collector-emitter voltage (Base open)	V_{CEO}	$I_C = 1$ mA, $I_B = 0$	20			V
Emitter-base voltage (Collector open)	V_{EBO}	$I_E = 10\ \mu A$, $I_C = 0$	12			V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{CB} = 25$ V, $I_E = 0$			0.1	μA
Forward current transfer ratio *1	h_{FE1}	$V_{CE} = 2$ V, $I_C = 0.5$ A	200		800	—
	h_{FE2}	$V_{CE} = 2$ V, $I_C = 1$ A	60			
Collector-emitter saturation voltage *1	$V_{CE(sat)}$	$I_C = 0.5$ A, $I_B = 20$ mA		0.13	0.40	V
Base-emitter saturation voltage *1	$V_{BE(sat)}$	$I_C = 0.5$ A, $I_B = 20$ mA			1.2	V
Transition frequency	f_T	$V_{CB} = 10$ V, $I_E = -50$ mA, f = 200 MHz		200		MHz
Collector output capacitance (Common base, input open circuited)	C_{ob}	$V_{CB} = 10$ V, $I_E = 0$, f = 1 MHz		10		pF
ON resistance *2	R_{on}			1.0		Ω

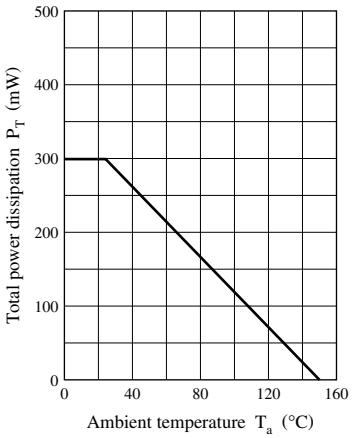
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.
2. *1: Pulse measurement
*2: R_{on} test circuit



$$R_{on} = \frac{V_B \times 1000}{V_A - V_B}\ (\Omega)$$

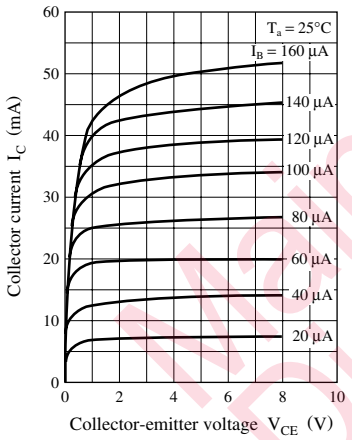
Common characteristics chart

P_T — T_a

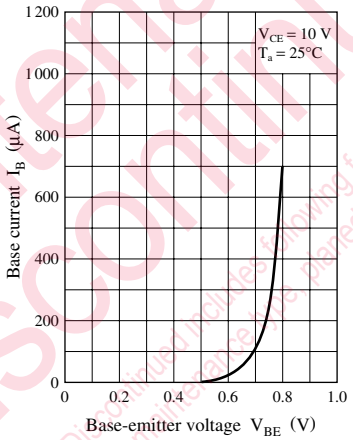


Characteristics charts of Tr1

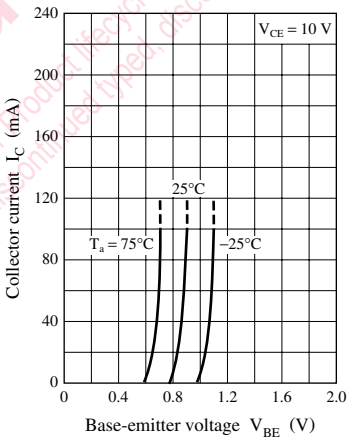
I_C — V_{CE}



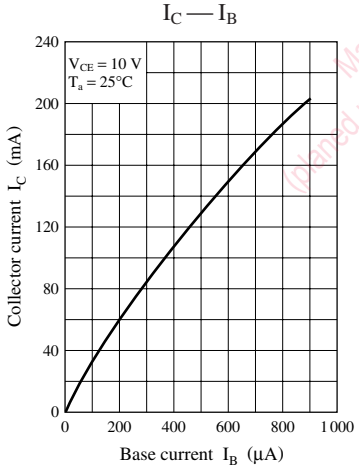
I_B — V_{BE}



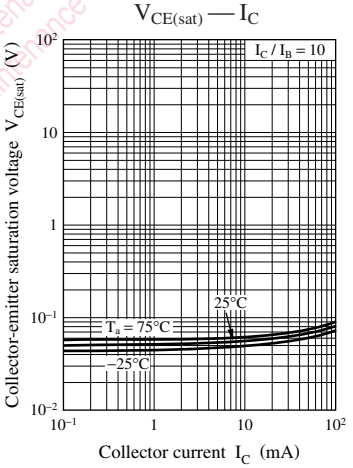
I_C — V_{BE}



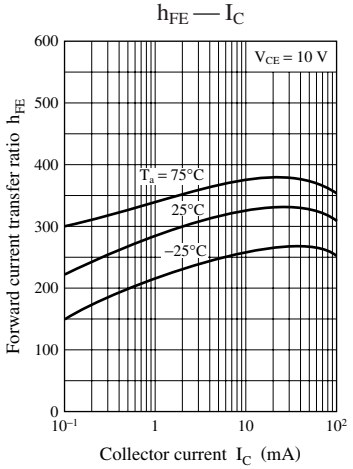
I_C — I_B



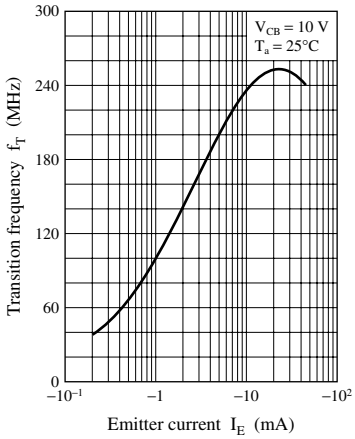
$V_{CE(sat)}$ — I_C



h_{FE} — I_C

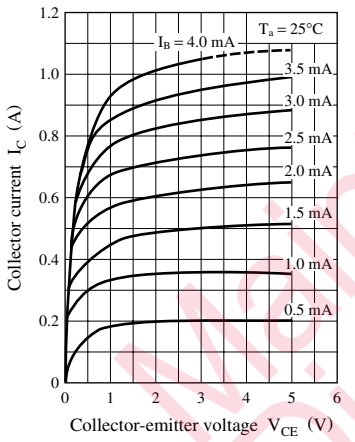


f_T — I_E

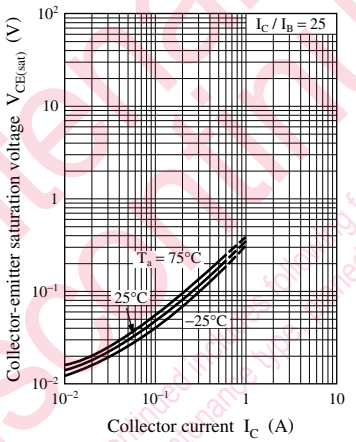


Characteristics charts of Tr2

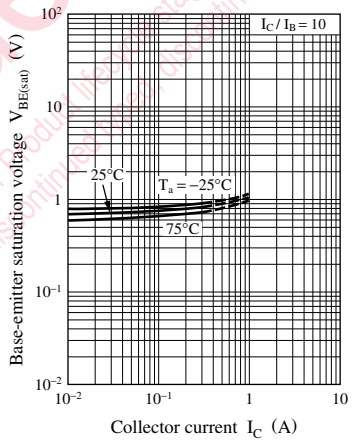
I_C — V_{CE}



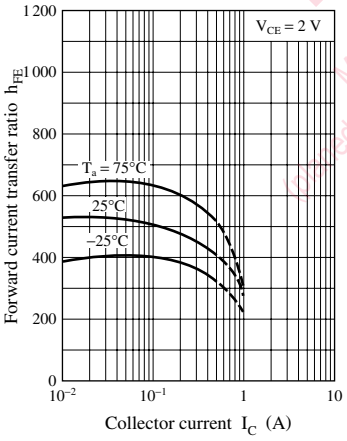
$V_{CE(sat)}$ — I_C



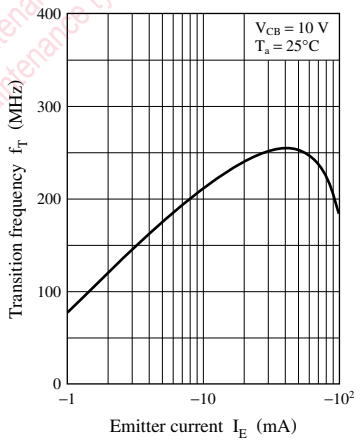
$V_{BE(sat)}$ — I_C



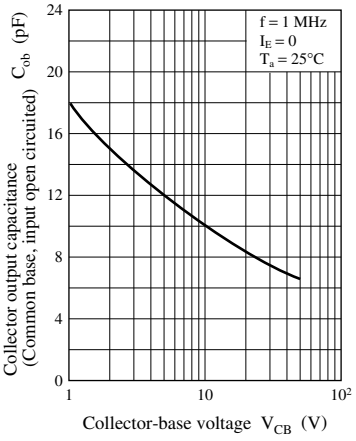
h_{FE} — I_C



f_T — I_E

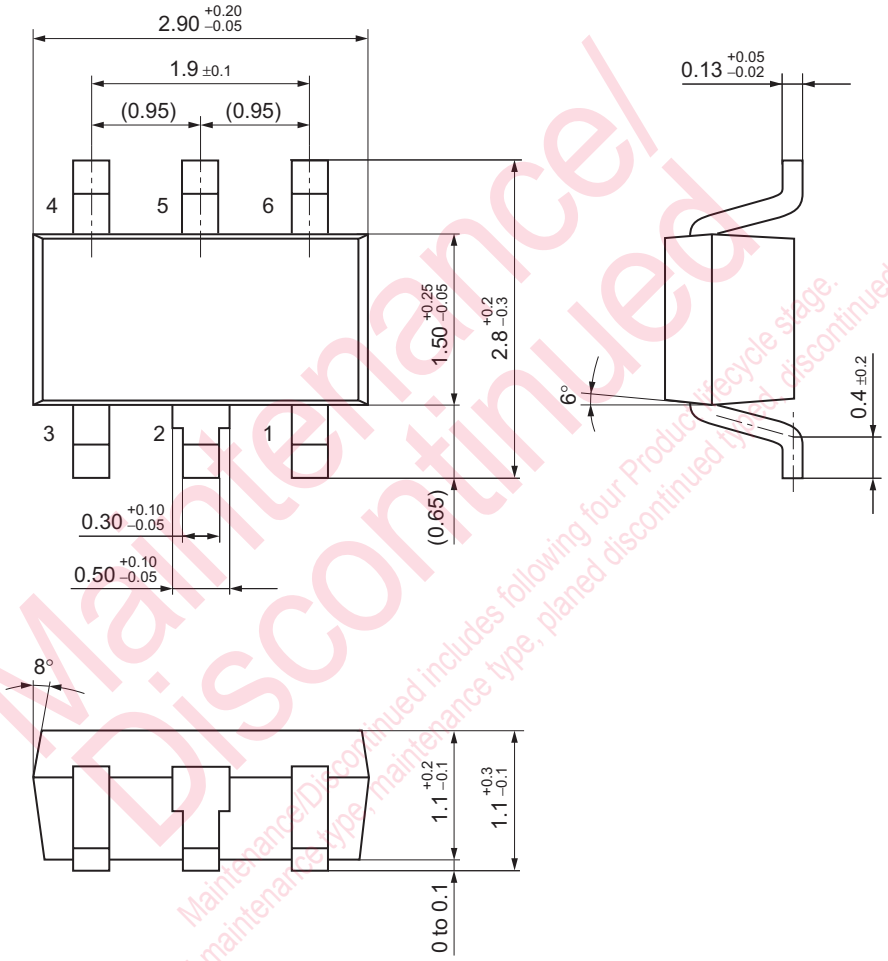


C_{ob} — V_{CB}



Mini6-G3

Unit: mm



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