

## Dual-Tracking Voltage Regulator

### GENERAL DESCRIPTION

The XR-4194 is a dual-polarity tracking regulator designed to provide balanced or unbalanced positive and negative output voltages at currents of up to 200 mA. A single resistor can be used to adjust both outputs between the limits of  $\pm 50\text{mV}$  and  $\pm 42\text{ V}$ . The device is ideal for local on-card regulation, which eliminates the distribution problems associated with single-point regulation. The XR-4194 is available in a 14-pin ceramic dual-in-line package, which has a 900 mW rating.

### FEATURES

- Direct Replacement for RM/RC 4194
- Both Outputs Adjust with Single Resistor
- Load Current to  $\pm 200\text{ mA}$  with 0.2% Load Regulation
- Low External Parts Count
- Internal Thermal Shutdown at  $T_J = 175^\circ\text{C}$
- External Adjustment for  $\pm V_O$  Unbalancing

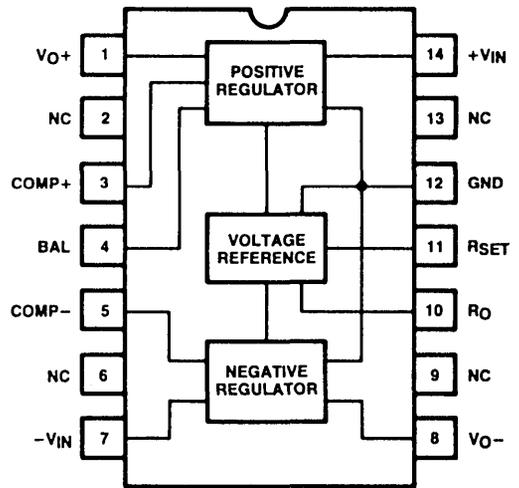
### APPLICATIONS

- On-Card Regulation
- Adjustable Regulator

### ABSOLUTE MAXIMUM RATINGS

Input Voltage $\pm V$ to Ground	
XR-4194M	$\pm 45\text{ V}$
XR-4194CN	$\pm 35\text{ V}$
Input/Output Voltage Differential	$\pm 45\text{ V}$
Power Dissipation at $T_A = 25^\circ\text{C}$	900 mW
Load Current	30 mA
Operating Junction Temperature Range	
XR-4194M	$-55^\circ\text{C}$ to $+150^\circ\text{C}$
XR-4194CN	$0^\circ\text{C}$ to $+125^\circ\text{C}$
Storage Temperature Range	$-65^\circ\text{C}$ to $+150^\circ\text{C}$

### FUNCTIONAL BLOCK DIAGRAM



### ORDERING INFORMATION

Part Number	Package	Operating Temperature
XR-4194CN	Ceramic DIP	$0^\circ\text{C}$ to $+70^\circ\text{C}$
XR-4194M	Ceramic DIP	$-55^\circ\text{C}$ to $+125^\circ\text{C}$

### SYSTEM DESCRIPTION

The XR-4194 is a dual polarity tracking voltage regulator. An on board reference, set by a single resistor, determines both output voltages. Tracking accuracy is better than 1%. Non-symmetrical output voltages are obtained by connecting a resistor to the balance adjust (Pin 4). Internal protection circuits include thermal shutdown and active current limiting.

# XR-4194

## ELECTRICAL CHARACTERISTICS

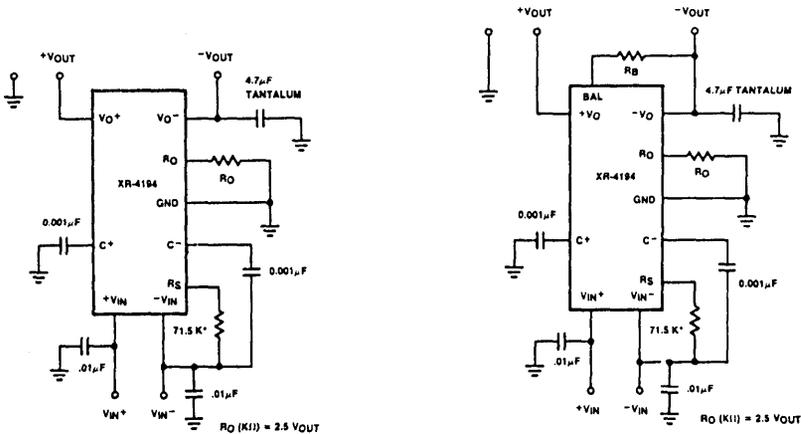
Test Conditions:  $\pm 5 \leq V_{OUT} \leq V_{MAX}$ ; XR-4194M  $-55^{\circ}\text{C} \leq +125^{\circ}\text{C}$ ; XR-4194CN  $0^{\circ}\text{C} \leq T_J \leq +70^{\circ}\text{C}$

PARAMETERS	XR-4194M			XR-4194CN			UNIT	CONDITIONS
	MIN	TYP	MAX	MIN	TYP	MAX		
Line Regulation		0.02	0.1		0.02	0.1	% $V_{OUT}$	$\Delta V_{IN} = 0.1 V_{IN}$
Load Regulation		0.001	0.0025		0.001	0.004	% $V_{O}/\text{mA}$	XR-4194CN, M: $I_L = 5$ to $100$ mA
TC of Output Voltage		0.002	0.020		0.003	0.015	%/ $^{\circ}\text{C}$	
*Stand-by Current Drain from		+0.3	+1.0		+0.3	+1.5	mA	$V_{IN} = V_{MAX}$ , $V_O = 0V$
to		-1.2	-2.0		-1.2	-2.0		$V_{IN} = V_{MAX}$ , $V_O = 0V$
Input Voltage Range	$\pm 9.5$		$\pm 45$	$\pm 9.5$		$\pm 35$	V	
Output Voltage Scale Factor	2.45	2.5	2.55	2.38	2.5	2.62	K $\Omega/V$	$R_{SET} = 71.5$ K $T_J = 25^{\circ}\text{C}$
Output Voltage Range	0.05		+42	0.05		$\pm 32$	V	$R_{SET} = 71.5$ K
Output Voltage Tracking			1.0			2.0	%	
Ripple Rejection		70			70		dB	$f = 120$ Hz, $T_J = 25^{\circ}\text{C}$
Input-Output Voltage Differential	3.0			3.0			V	$I_L = 50$ mA
Output Short Circuit Current		300			300		mA	$V_{IN} = \pm 30$ V Max
Output Noise Voltage		250			250		$\mu\text{V RMS}$	$C_L = 4.7$ $\mu\text{F}$ , $V_O = \pm 15$ V $f = 10$ Hz to $100$ KHz
Internal Thermal Shutdown		175			175		$^{\circ}\text{C}$	

\*  $\pm I_{Quiescent}$  will increase by  $50 \mu\text{A}/V_{OUT}$  on positive side and  $100 \mu\text{A}/V_{OUT}$  on negative side.

## THERMAL CHARACTERISTICS

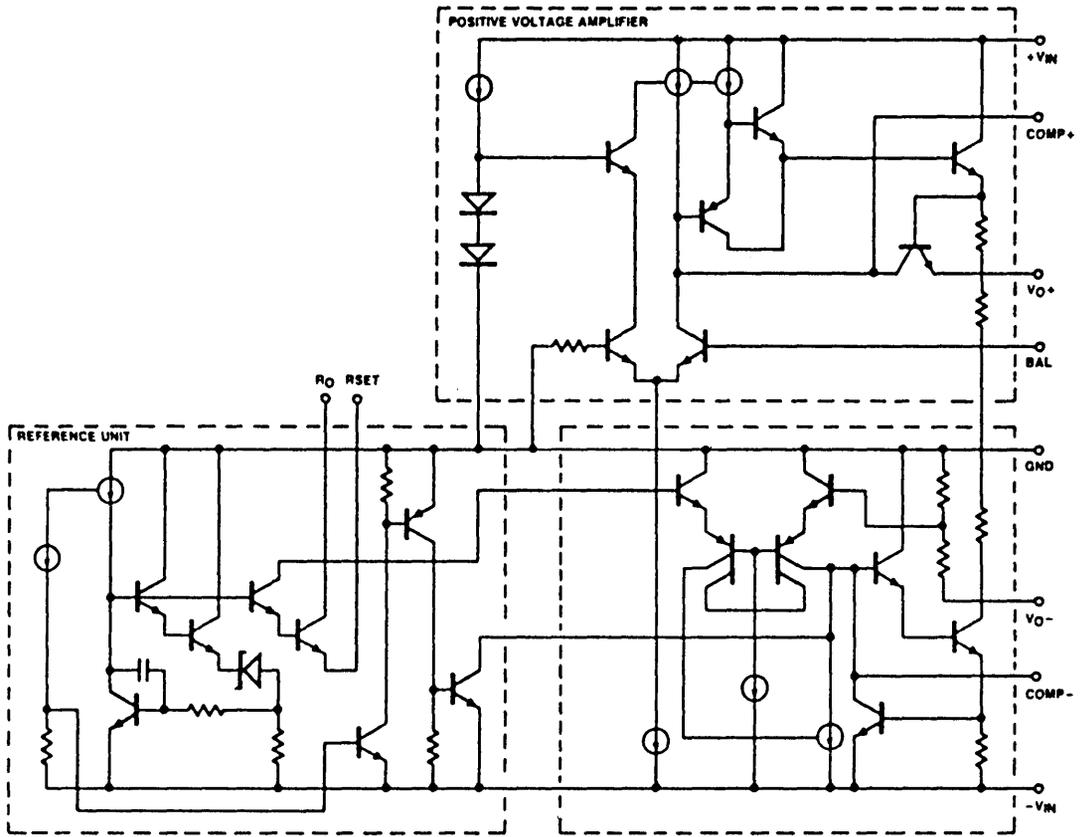
PARAMETERS	XR-4194M			XR-4194CN			CONDITIONS
	MIN	TYP	MAX	MIN	TYP	MAX	
Power Dissipation			900 mW 2.2 W			900 mW 2.2 W	$T_A = 25^{\circ}\text{C}$ $T_C = 25^{\circ}\text{C}$
Thermal Resistance Junction to Ambient Junction to Case		128 $^{\circ}\text{C}/\text{W}$ 55 $^{\circ}\text{C}/\text{W}$			128 $^{\circ}\text{C}/\text{W}$ 55 $^{\circ}\text{C}/\text{W}$		



\* For Best Tracking Temperature Coefficient of  $R_O$  Should Be Same As For  $R_G$   
 Adjust  $R_O$  for  $-V_S = 6$  V (15 K $\Omega$ ) then  
 Adjust  $R_B$  for  $+V_S = 12$  V (20 K $\Omega$ )

Figure 2. Typical Applications

# XR-4194



EQUIVALENT SCHEMATIC DIAGRAM