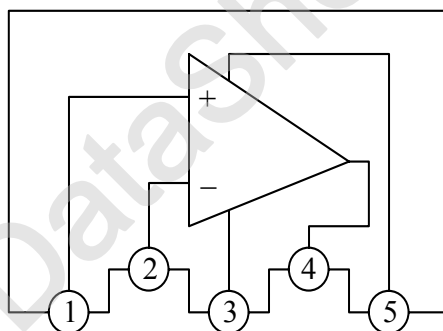


18W HI-FI AUDIO AMPLIFIER—YD1006**DESCRIPTION**

The YD1006 is a monolithic audio power amplifier integrated circuit.

FEATURES

- *Very low external component required.
- *High Current output and high operating voltage.
- *Low harmonic and crossover distortion.
- *Built-in Over temperature protection.
- *Short circuit protection between all pins.
- *Safety Operating Area for output transistors.

BLOCK DIAGRAM**WuXi YouDa Electronics Co., Ltd**

Add: No.5 Xijin Road, National Hi-Tech Industrial Development Zone, Wuxi Jiangsu China
Tel: 86-510-5205117 86-510-5205108 Fax: 86-510-5205110 Website: www.e-youda.com
SHENZHEN OFFICE Tel : 86-755-83740369 13823533350 Fax : 86-755-83741418

ABSOLUTE MAXIMUM RATINGS ($T_{amb}=25$)

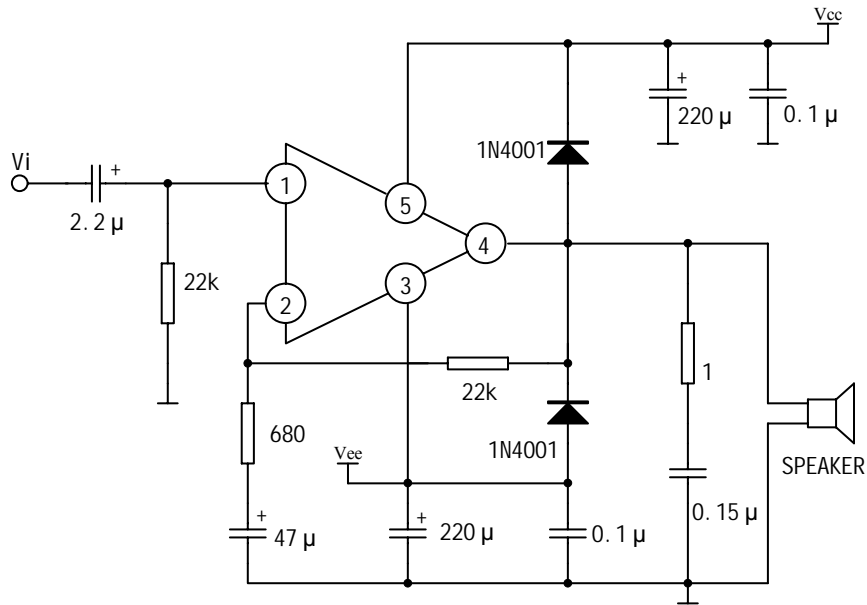
PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage	V_{CC}/V_{EE}	± 22	V
Input Voltage	V_i	V_{CC}	
Differential Input Voltage	V_{di}	± 15	V
Peak Output Current (internally limited)	I_o	3.5	A
Total Power Dissipation at $T_{case}=90$	R_{th}	3	/W
Storage Temperature	P_D	20	W
Junction Temperature	T_{stg}	-40 ~ +150	

ELECTRICAL CHARACTERISTICS(Refer to the test circuit , $V_{CC}/V_{EE} = \pm 16V$, $T_{amb}=25$)

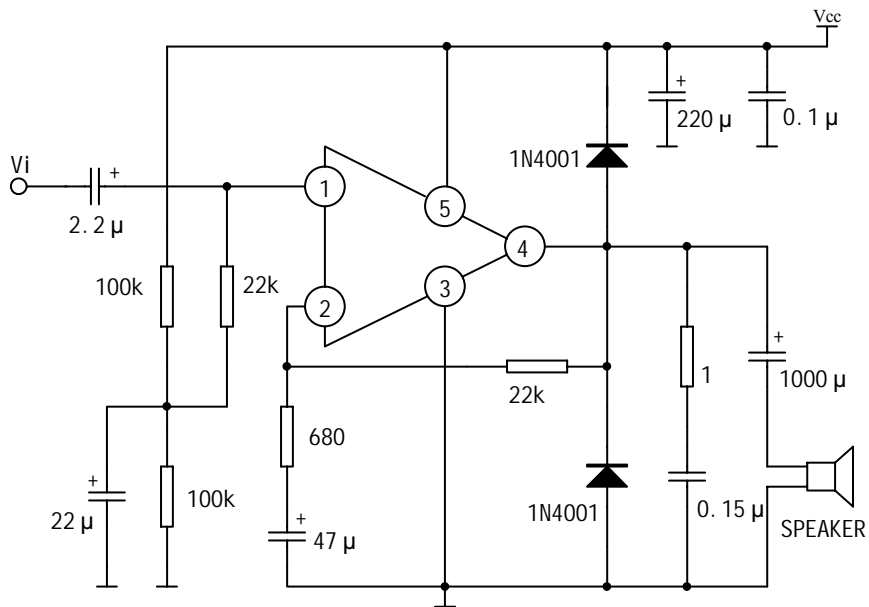
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	V_{CC}/V_{EE}		± 6		± 22	V
Quiescent Drain Current	I_{ccq}			40	60	mA
Input Bias Current	I_{ib}	$V_{CC}/V_{EE} = \pm 22V$		0.2	2	μA
Input Offset Voltage	V_{is}			± 2	± 20	mV
Input Offset Current	I_{is}			± 20	± 200	nA
Output Power	P_o		THD=0.5%, $G_V=26dB$, $f=40$ to 15kHz			
		$R_L=4\Omega$	12	14		
		$R_L=8\Omega$	8	9		
		THD=10%, $G_V=26dB$, $f=40$ to 15kHz				W
		$R_L=4\Omega$		18		
$R_L=8\Omega$		11				
Power Bandwidth	BW	$P_o=15W$, $R_L=4\Omega$	10		140k	Hz
Open Loop Voltage Gain	G_{VO}	$f=1kHz$		90		dB
Close Loop Voltage Gain	G_V	$f=1kHz$	29.5	30	30.5	dB
Total Harmonic Distortion	THD	$P_o=0.1\sim 12W$, $R_L=4\Omega$ $f=40Hz\sim 15kHz$		0.2	0.5	%
		$P_o=0.1\sim 8W$, $R_L=8\Omega$ $f=40Hz\sim 15kHz$		0.1	0.5	
Input Noise Voltage	V_{NI}	$B=22Hz\sim 22kHz$		3	10	μV
Input Noise Current	I_{NI}	$B=22Hz\sim 22kHz$		80	200	pA
Input Resistance(pin1)	Z_i	Open Loop, $f=1kHz$	0.5	5		M Ω
Supply Voltage Rejection	RR	$R_L=4\Omega$, $R_g=22k\Omega$ $G_V=30dB$, $f=1kHz$	40	50		dB
Thermal Shut-Down Junction Temperature	T_j			145		

APPLICATION CIRCUIT

(1) YD1006 DUAL SUPPLY POWER APPLICATION



(2) YD1006 SINGLE SUPPLY POWER APPLICATION



OUTLINE DRAWING

