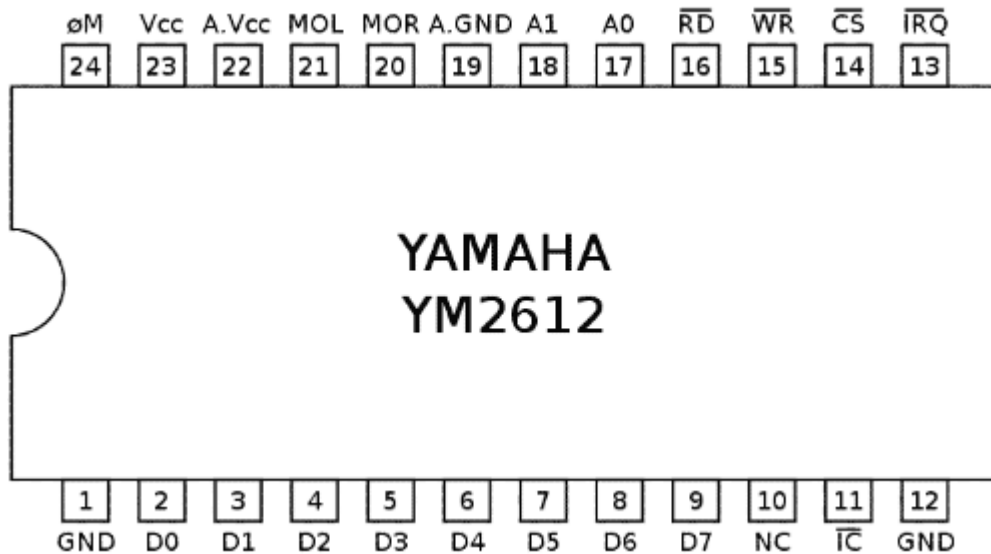


Yamaha YM2612 (OPN) Sound Chip



No.	Pin Name	I/O	Function																																																						
1	GND	-	Ground pin.																																																						
2	D ₀	I/O	8-bit bidirectional data bus. Communicates data with the processor.																																																						
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7	D ₅																																																								
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9	D ₇																																																								
10	$\overline{\text{TEST}}$	I/O	Pin to test this LSI. Do not connect.																																																						
11	$\overline{\text{IC}}$	I	Initializes the internal register.																																																						
12	GND	-	Ground pin.																																																						
13	$\overline{\text{IRQ}}$	O	Interrupt signal issued from the two timers. When the time programmed into the timer has elapsed, this goes low. Output with open drain.																																																						
14	$\overline{\text{CS}}$	I	Control the D0 – D7 data bus.																																																						
			<table border="1"> <thead> <tr> <th>CS</th> <th>RD</th> <th>WR</th> <th>A1</th> <th>A0</th> <th>Details</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>1</td> <td>0</td> <td>0</td> <td>0</td> <td>Writes register addresses of timers, etc.</td> </tr> <tr> <td>0</td> <td>1</td> <td>0</td> <td>0</td> <td>1</td> <td>Writes register addresses of channels 1-3.</td> </tr> <tr> <td>0</td> <td>1</td> <td>0</td> <td>1</td> <td>0</td> <td>Writes register data of timers, etc.</td> </tr> <tr> <td>0</td> <td>1</td> <td>0</td> <td>1</td> <td>1</td> <td>Writes register data of channels 1-3.</td> </tr> <tr> <td>0</td> <td>1</td> <td>0</td> <td>1</td> <td>0</td> <td>Writes register addresses of channels 4-6.</td> </tr> <tr> <td>0</td> <td>1</td> <td>0</td> <td>1</td> <td>1</td> <td>Writes register data of channels 4-6.</td> </tr> <tr> <td>0</td> <td>0</td> <td>1</td> <td>0</td> <td>0</td> <td>Reads statuses.</td> </tr> <tr> <td>1</td> <td>X</td> <td>X</td> <td>X</td> <td>X</td> <td>D0 – D7 are set to high-impedance.</td> </tr> </tbody> </table>	CS	RD	WR	A1	A0	Details	0	1	0	0	0	Writes register addresses of timers, etc.	0	1	0	0	1	Writes register addresses of channels 1-3.	0	1	0	1	0	Writes register data of timers, etc.	0	1	0	1	1	Writes register data of channels 1-3.	0	1	0	1	0	Writes register addresses of channels 4-6.	0	1	0	1	1	Writes register data of channels 4-6.	0	0	1	0	0	Reads statuses.	1	X	X	X	X	D0 – D7 are set to high-impedance.
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15	WR	I	Control the D0 – D7 data bus.
16	RD		
17	A₀		
18	A₁		
19	A GND	-	Ground pin.
20	MOR	O	Two-channel analog outputs. These are output with a source follower.
21	MOL		
22	A V_{CC}	-	+5V power supply pins.
23			
24	ϕ M V_{CC}	I	Master clock input.