

YS-150MS01

TFT-LCD Module

SHARP IN SIDE

Spec. Issue Date: Feb. 14, 2006

No: LD-150N2A

RECORDS OF REVISION

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2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel, driver ICs, control circuit, power supply circuit and a back light unit. Graphics and texts can be displayed on a 1024 × RGB × 768 dots panel with about 16 million colors by using LVDS (Low Voltage Differential Signaling) and supplying +3.3V DC supply voltages for TFT-LCD panel driving and supply voltage for backlight.

3. Mechanical Specifications

Parameter	Specifications	Unit
Display size	38 (Diagonal)	cm
	15.0 (Diagonal)	Inch
Active area	304.1 (H) 228.1 (V)	mm
Pixel format	1024 (H) × 768 (V)	Pixel
	(1 pixel R+G+B dots)	-
Pixel pitch	0.297 (H) 0.297 (V)	mm
Pixel configuration	R, G, B vertical stripe	-
Display mode	Normally white	-
Brightness	350 (Typ)	cd/m ²
Response time	Tr=5 ,Td=20	ms
Contrast ration	350 : 1	-
View angle	55,80,80	Deg.
Unit outline dimensions *1	326.0(W) 252.0(H) 10.55(D)	mm
Mass	1400 (MAX)	g
Surface treatment	Anti-glare and hard-coating 2H (Haze value = 28)	

*1. Note: excluding back light cables, cover and pet sheet.

The thickness of module (D) doesn't contain the projection.

Outline dimensions are shown in Fig.1.

4. Input Terminals

4-1. TFT-LCD panel driving

CN1 (Interface signals and +3.3V DC power supply)

Using connectors : DF14H-20P-1.25H (Hirose Electric Co., Ltd.) or compatible

Corresponding connectors : DF14-20S-1.25C(Connector)

DF14-2628SCFA(Terminal)

Corresponding LVDS Transmitter : THC63LVDM83R(Thine) or compatible

Pin No.	Symbol	Function	Remark
1	Vcc	+3.3V Power supply	
2	Vcc	+3.3V Power supply	
3	GND		
4	GND		
5	RXIN0-	Receiver signal (-)	LVDS
6	RXIN0+	Receiver signal (+)	LVDS
7	GND		
8	RXIN1-	Receiver signal (-)	LVDS
9	RXIN1+	Receiver signal (+)	LVDS
10	GND		
11	RXIN2-	Receiver signal (-)	LVDS
12	RXIN2+	Receiver signal (+)	LVDS
13	GND		
14	RXCKIN-	Clock signal (-)	LVDS
15	RXCKIN+	Clock signal (+)	LVDS
16	GND		
17	RXIN3-	Receiver signal (-)	LVDS
18	RXIN3+	Receiver signal (+)	LVDS
19	GND		
20	LVDS_SET	LVDS_SET	note1

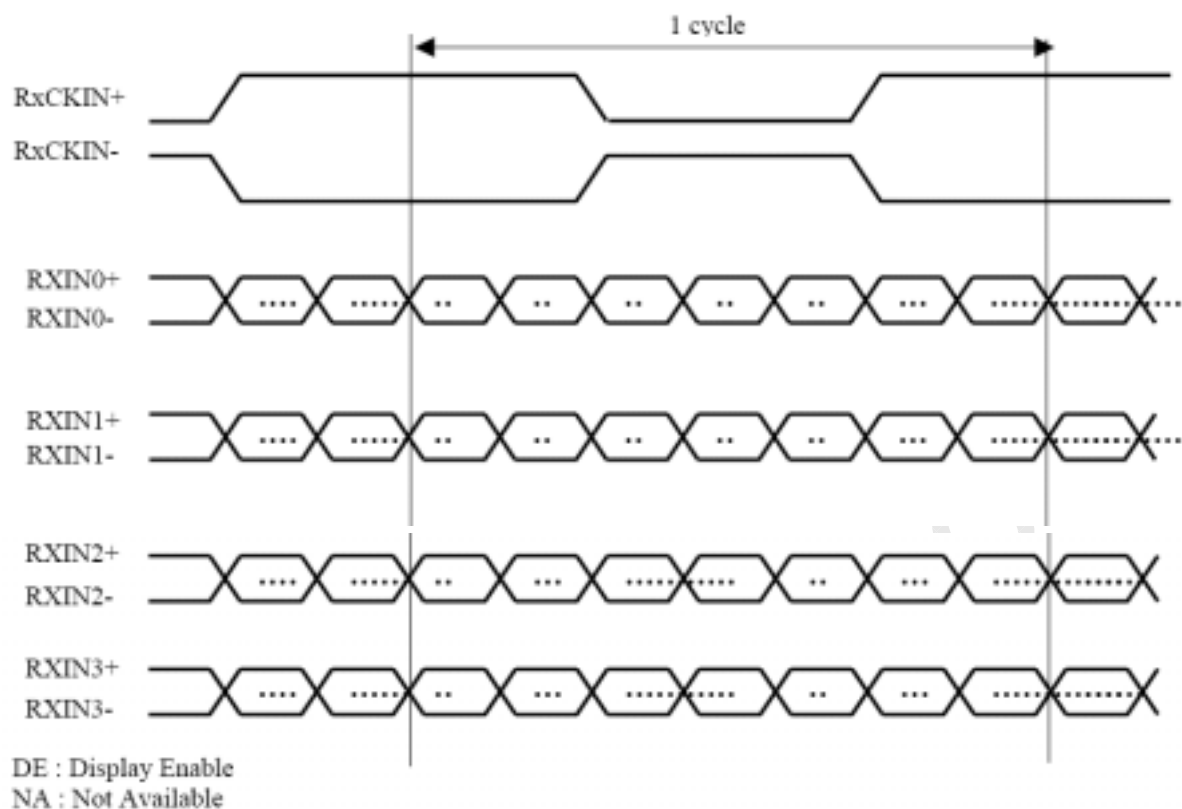
4-2 Data Mapping

1) 8 bit input

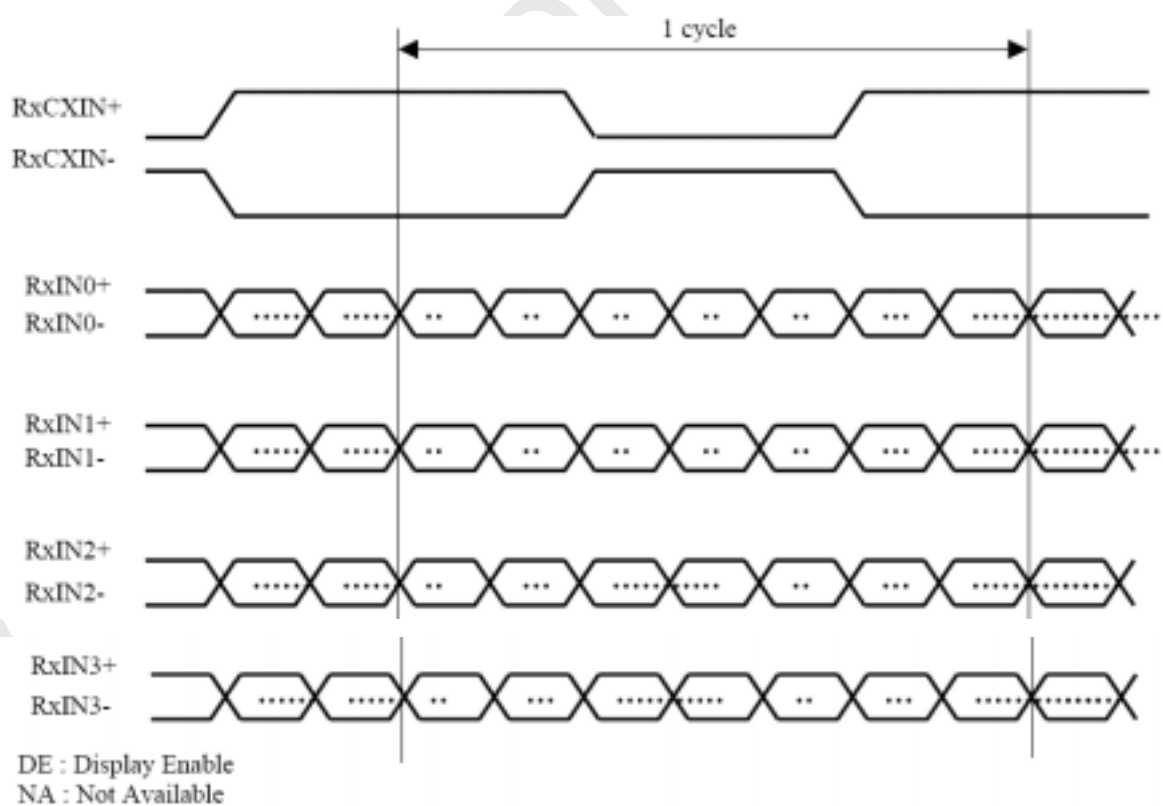
【note1】 pin assignment with LVDS_SET pin (Thine:THC63LVDM83R)

Transmitter		20pin LVDS_SET	
Pin No	Data	=L (GND) or Open	=H (3.3V)
51	TA0	R2	R0 (LSB)
52	TA1	R3	R1
54	TA2	R4	R2
55	TA3	R5	R3
56	TA4	R6	R4
3	TA5	R7 (MSB)	R5
4	TA6	G2	G0 (LSB)
6	TB0	G3	G1
7	TB1	G4	G2
11	TB2	G5	G3
12	TB3	G6	G4
14	TB4	G7 (MSB)	G5
15	TB5	B2	B0 (LSB)
19	TB6	B3	B1
20	TC0	B4	B2
22	TC1	B5	B3
23	TC2	B6	B4
24	TC3	B7(MSB)	B5
27	TC4	(NA)	(NA)
28	TC5	(NA)	(NA)
30	TC6	DE	DE
50	TD0	R0 (LSB)	R6
2	TD1	R1	R7 (MSB)
8	TD2	G0(LSB)	G6
10	TD3	G1	G7 (MSB)
16	TD4	B0(LSB)	B6
18	TD5	B1	B7 (MSB)
25	TD6	(NA)	(NA)

<LVDS_SET=L or Open>



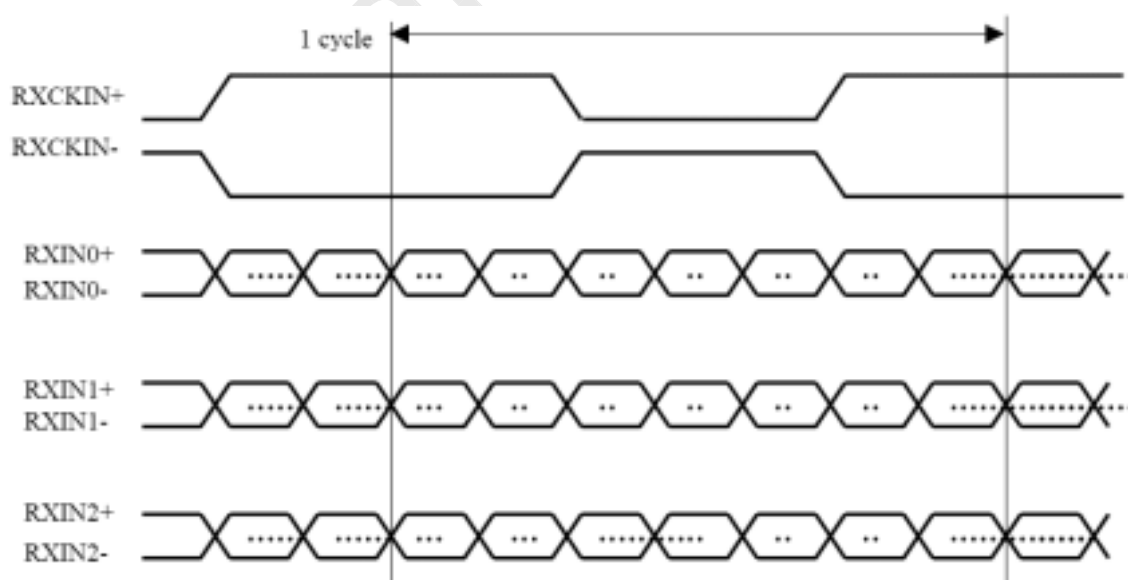
<LVDS_SET =H>



2) 6 bit input

【note1】 pin assignment with LVDS_SET pin (Thine:THC63LVDM83R)

Transmitter		20pin	LVDS_SET
Pin No	Data	=L (GND) or Open	=H (3.3V)
51	TA0	R0 (LSB)	-
52	TA1	R1	-
54	TA2	R2	-
55	TA3	R3	-
56	TA4	R4	-
3	TA5	R5 (MSB)	-
4	TA6	G0(LSB)	-
6	TB0	G1	-
7	TB1	G2	-
11	TB2	G3	-
12	TB3	G4	-
14	TB4	G5 (MSB)	-
15	TB5	B0(LSB)	-
19	TB6	B1	-
20	TC0	B2	-
22	TC1	B3	-
23	TC2	B4	-
24	TC3	B5(MSB)	-
27	TC4	(NA)	-
28	TC5	(NA)	-
30	TC6	DE	-
50	TD0	GND	-
2	TD1	GND	-
8	TD2	GND	-
10	TD3	GND	-
16	TD4	GND	-
18	TD5	GND	-
25	TD6	(NA)	-



DE : Display Enable
NA : Not Available

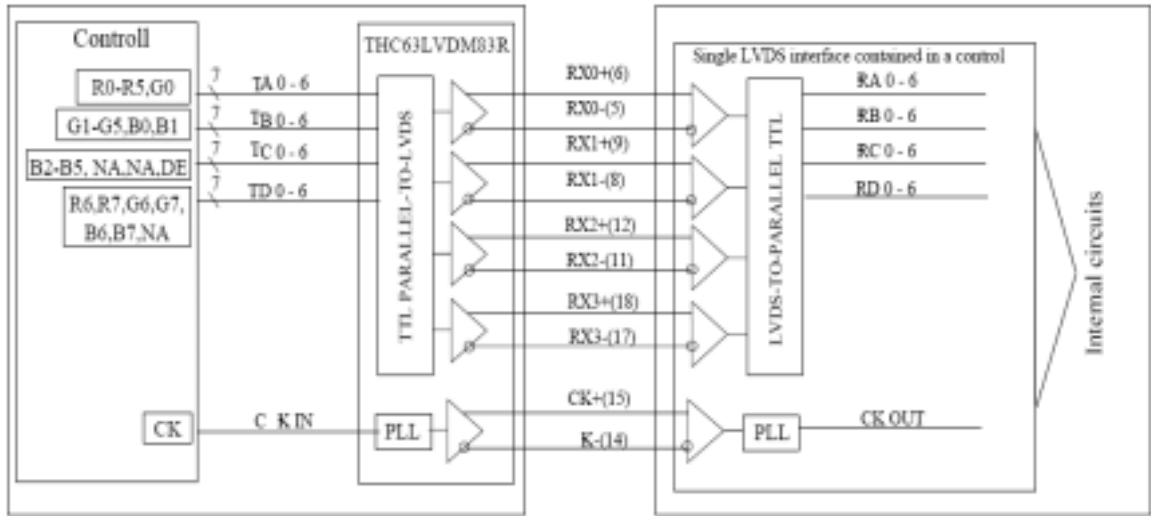
In case of supplying 6 bit signal, it is recommended to connect pin No.17(Rx3-) with VCC(3.3V), and No.18(Rx3+) with GND(0V).

4-3 Interface block diagram

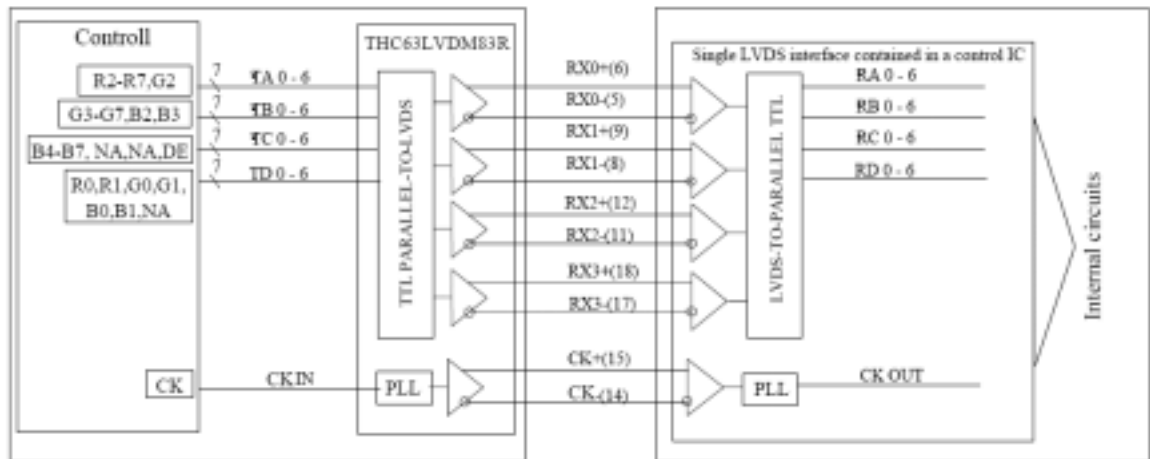
(Computer side)

(TFT-LCD side)

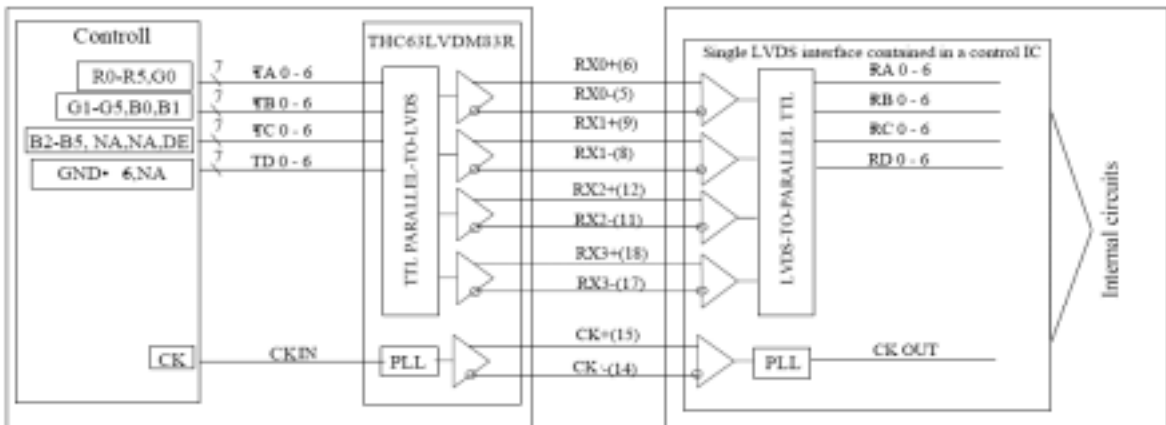
(1) 8bit Mode LVDS_SET=H (20 pin=3.3[v])



(2) 8bit Mode LVDS_SET=L (20 pin=GND or OPEN)



(3) 6bit Mode LVDS_SET=L (20 pin=GND or OPEN)



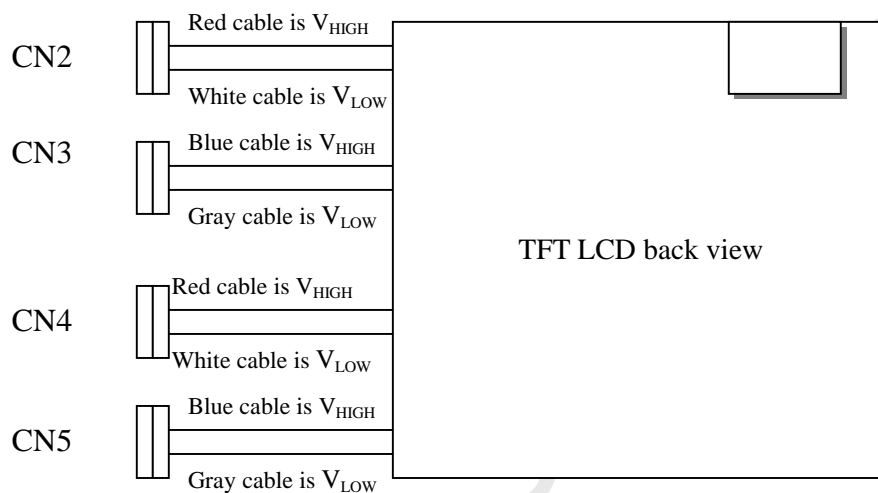
4-4 Backlight

CN 2, 3, 4,5

The module-side connector : BHSR-02VS-1 (JST)

The user-side connector : SM02B-BHSS-1-TB (JST)

Pin no.	symbol	I/O	Function
1	V_{HIGH}	I	Power supply for lamp (High voltage side)
2	V_{LOW}	I	Power supply for lamp (Low voltage side)



5. Absolute Maximum Ratings

Parameter	Symbol	Condition	Ratings	Unit	Remark
Supply voltage	V_{CC}	$T_a=25$	0 ~ + 4.0	V	
Lamp Input voltage	V_{HIGH}	-	0 ~ + 2700	V_{rms}	
Storage temperature	T_{STG}	-	25 ~ + 60		Note1
Operating temperature	T_{OPA}	-	0 ~ + 50		Note1

【Note 1】 Humidity : 95%RH Max. ($T_a=40$)

Maximum wet-bulb temperature at 39 or less. ($T_a>40$)

No condensation.

6. Electrical Characteristics

6-1. TFT-LCD panel driving $T_a=25$

Parameter		Symbol	Min.	Typ.	Max.	Unit	Remark
Vcc	Supply voltage	Vcc	+3.0	+3.3	+3.6	V	Note2
	Current dissipation	Icc		425	700	mA	Note3
Permissive input ripple voltage		V _{RF}			100	mVp-p	V _{CC} =+3.3V
Differential input	Hight	V _{TH}			+100	mV	V _{CM} =+1.2V
hreshold voltage	Low	V _{TL}	-100			mV	Note1
Input current (High)		I _{OH}			10	A	V _I =2.4V, V _{CC} =3.6V
Input current (Low)		I _{OL}			10	A	V _I =0V, V _{CC} =3.6V
Terminal resistor		R _T	100				Differential input

Note1 V_{CM}: Common mode voltage of LVDS driver.

Note2

1) On-off sequences of Vcc

and data

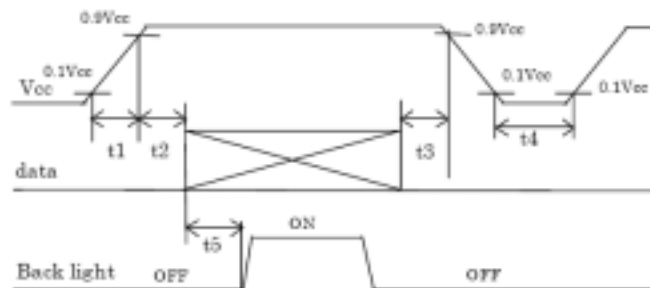
$$0 < t_1 \leq 10\text{ms}$$

$$0 < t_2 \leq 10\text{ms}$$

$$0 < t_3 \leq 1\text{s}$$

$$1\text{s} \leq t_4$$

$$200\text{ms} \leq t_5$$



Power sequence for Backlight is not especially specified, however it is recommended to consider some timing difference between LVDS input and Backlight input as shown above.

If the Backlight lights on before LCD starting, or if the Backlight is kept on after LCD stopping, the screen may look white for a moment or abnormal image may be displayed.

This is caused by variation in output signal from timing generator at LVDS input on or off. It does not cause the damage to the LCD module.

2) Dip conditions for supply voltage

$$1) V_2 \leq V_{CC} < V_1$$

$$t_d \leq 10\text{ms}$$

$$2) V_{CC} < V_2$$

V_{CC}-dip conditions should also follow the on-off conditions.

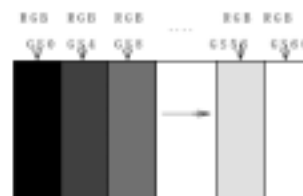
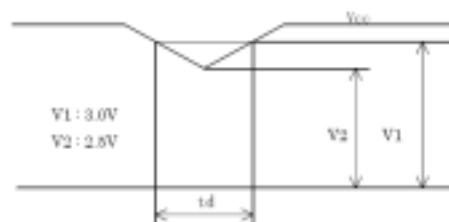
【Note3】 Typical current situation : 16-gray-bar pattern

V_{CC}=+3.3V, CK=65MHz

Horizontal period =20.7 us

Gray scale : GS(4n)

$$n=0\sim 15$$



The explanation of each gray scale, GS(4n), is described below section 8-2.

6-2. Backlight

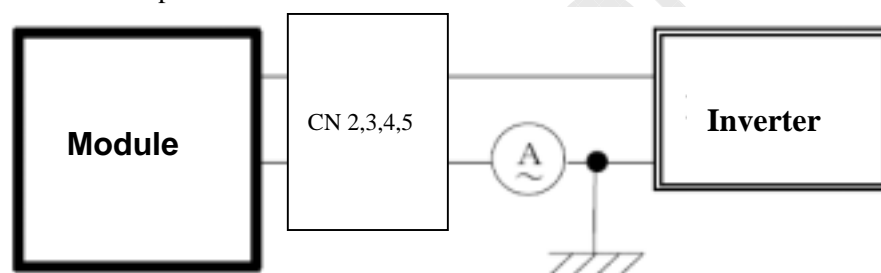
The back light system is an edge-lighting type with 4 CCFTs (Cold Cathode Fluorescent Tube).

The characteristics of the lamp are shown in the following table.

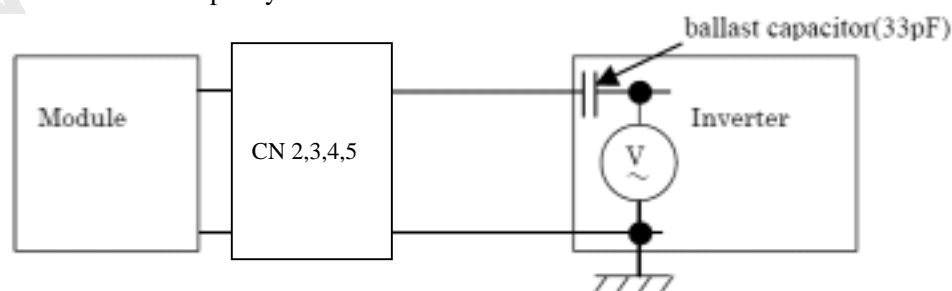
The value mentioned below is at the case of one CCFT.

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
Lamp current range	I_L	5.0	6.0	-	mArms	Note1
Lamp voltage	V_L	558	572	-	Vrms	$I_L=4.5$ mArms $T_a=25$ 60kHz
Lamp power consumption	P_L	-	3.4	4.0	W	Note2 $I_L=4.5$ mArms $T_a=25$ 60kHz
Lamp frequency	F_L	49	50	-	kHz	Note3
Kick-off voltage	V_s	562	601	-	Vrms	$T_a=0$ 60kHz [Note4]
Lamp life time	T_L	-	40,000	-	hour	Note5

- 【Note1】 A lamp can be light in the range of lamp current shown above.
Maximum rating for current is measured by high frequency current measurement equipment connected to V_{Low} at circuit showed below.
(Note : To keep enough kick-off voltage and necessary steady voltage for CCFT.)
Lamp frequency : 40~70kHz
Ambient temperature : 0~50



- 【Note2】 Referential data per one CCFT by calculation ($I_L \times V_L$).
The data don't include loss at inverter.
- 【Note3】 Lamp frequency of inverter may produce interference with horizontal synchronous frequency, and this may cause horizontal beat on the display. Therefore, adjust lamp frequency, and keep inverter as far as from module or use electronic shielding between inverter and module to avoid interference.
- 【Note4】 This is transformer output voltage at 33pF for the ballast capacitor of a DC-AC inverter.
The kick-off voltage may rise up in the user set, please decide the open output voltage by checking not to occur lighting failure under operating state.
The open output voltage should be applied to the lamp for more than 1 second to startup.
Otherwise the lamp may not be turned on.



- 【Note5】 Lamp life time is defined as the time when either (1) or (2) occurs in the continuous operation under the condition of $T_a=25$ and $I_L=6.0$ mA rms.
- (1) Brightness becomes 50% of the original value under standard condition.
- (2) Kick-off voltage at $T_a=0$ exceeds $2200 V_{rms}$ value.

<<Note>>

The performance of the backlight, for example lifetime or brightness, is much influenced by the characteristics of the DC-AC inverter for the lamp. When you design or order the inverter, please make sure that a poor lighting caused by the mismatch of the backlight and the inverter (miss-lighting, flicker, etc.) never occurs. When you confirm it, the module should be operated in the same condition as it is installed in your instrument.

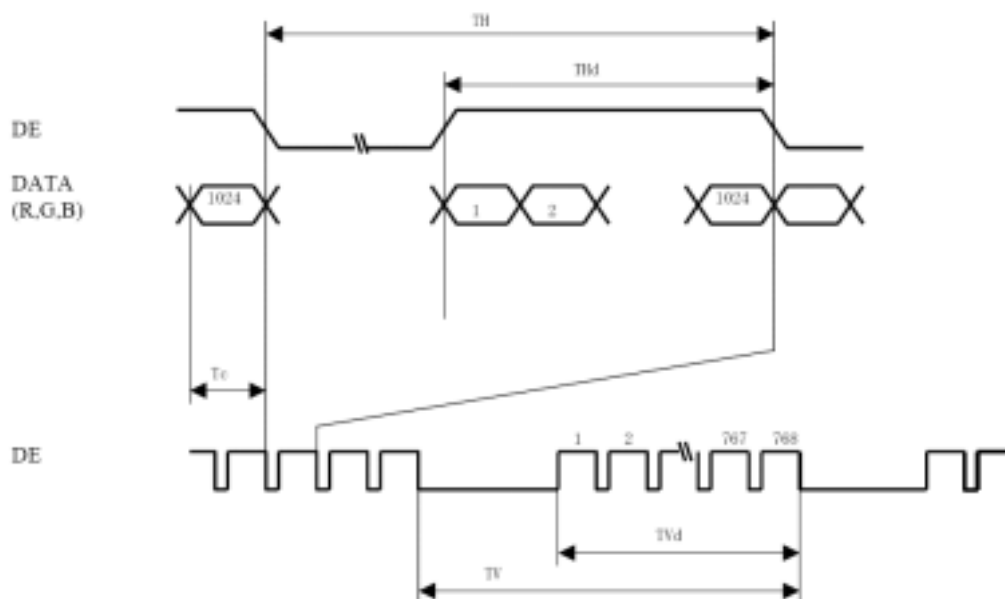
Use the lamp inverter power source incorporating such safeguard as overvoltage / overcurrent protective circuit or lamp voltage waveform detection circuit, which should have individual control of each lamp. In case one circuit without such individual control is connected to more than two lamps, excessive current may flow into one lamp when the other one is not in operation.

7. Timing characteristics of input signals

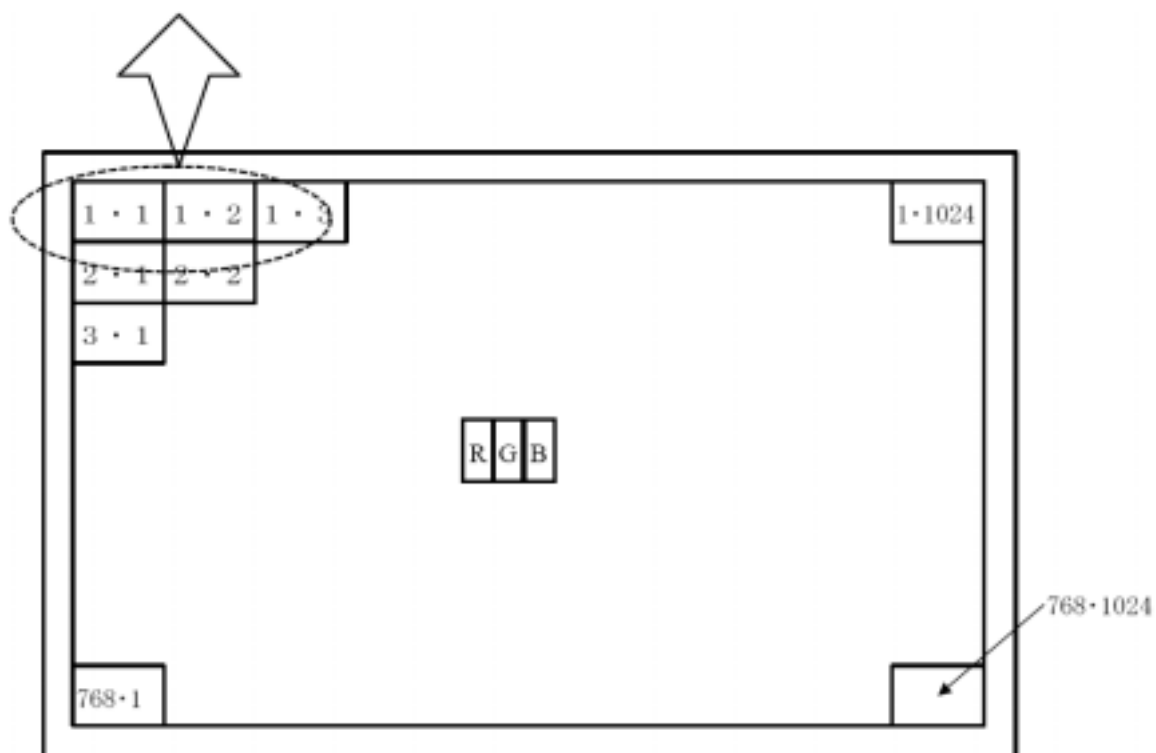
7-1. Timing characteristics

Parameter		Symbol	Min.	Typ.	Max.	Unit
Clock signal	Frequency	1/Tc	50.0	65.0	80.0	MHz
ENAB signal	Horizontal period	TH	1056	1344	1720	clock
			16.0	20.7	23.4	μ s
	Horizontal period(High)	THd	1024	1024	1024	clock
	Vertical period	TV	773	806	990	line
	Vertical period (High)	TVd	768	768	768	line

【Note】 In case of using the long vertical period, the deterioration of display quality, flicker etc. may occur.



7-2 Input Data Signals and Display Position on the screen



8. Input Signals, Basic Display Colors and Gray Scale of Each Color

8-1 8bit input

	Colors& Gray scale	Data signal																									
		Gray Scale	R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	B0	B1	B2	B3	B4	B5	B6	B7	
Basic Color	Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Blue		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	X	X	1	1	1	1	1	1	
	Green		0	0	0	0	0	0	0	0	X	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
	Cyan		0	0	0	0	0	0	0	0	X	X	1	1	1	1	1	1	X	X	1	1	1	1	1	1	
	Red		X	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Magenta		X	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0	X	X	1	1	1	1	1	1	
	Yellow		X	X	1	1	1	1	1	1	X	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
	White		X	X	1	1	1	1	1	1	X	X	1	1	1	1	1	1	X	X	1	1	1	1	1	1	
Gray Scale of Red	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Darker	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Brighter	GS250	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		GS251	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS252	X	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Gray Scale of Green	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Darker	GS1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		GS2	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Brighter	GS250	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0
		GS251	0	0	0	0	0	0	0	0	0	1	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0
	Green	GS252	0	0	0	0	0	0	0	0	0	X	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Gray Scale of Blue	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Darker	GS1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
		GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Brighter	GS250	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1
		GS251	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	1	1	1
	Blue	GS252	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	X	X	1	1	1	1	1	1

0 : Low level voltage, 1 : High level voltage. X :Don't care.

Each basic color can be displayed in 253 gray scales from 8 bit data signals. According to the combination of total 24 bit data signals, the 16-million-color display can be achieved on the screen.
Basic Color Gray Scale of Red Gray Scale of Green Gray Scale of Blue

8-2 6bit input

	Colors& Gray scale	Date signal																		
		Gray Scale	R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	B0	B1	B2	B3	B4	B5
Basic Color	Black	—	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	—	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Green	—	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Cyan	—	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Red	—	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	—	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	—	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	—	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale of Red	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	↓						↓												
	↓	↓						↓												
	Brighter	GS61	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	↓	GS62	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of Green	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	GS1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
	↑	↓						↓												
	↓	↓						↓												
	Brighter	GS61	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0
	↓	GS62	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0
	Green	GS63	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Gray Scale of Blue	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
	↑	↓						↓												
	↓	↓						↓												
	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	1
	↓	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	Blue	GS63	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

0 : Low level voltage, 1 : High level voltage.

Each basic color can be displayed in 64 gray scales from 6 bit data signals. According to the combination of total 18 bit data signals, the 262,144-color display can be achieved on the screen.

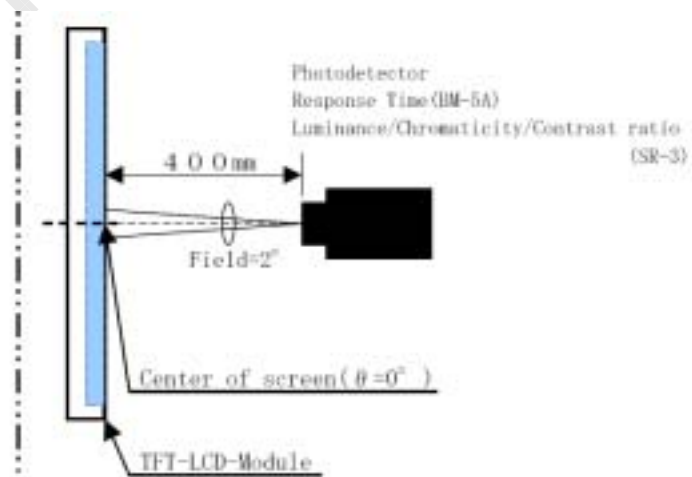
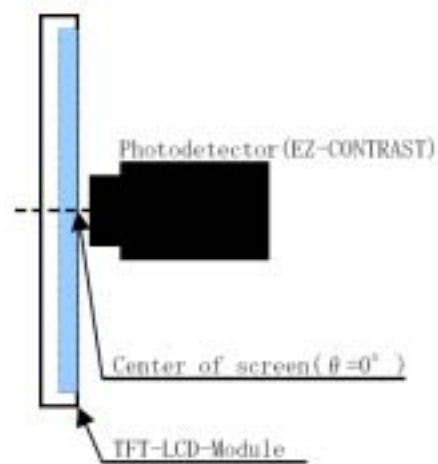
9. Optical Characteristics

Ta=25 , Vcc =+3.3V

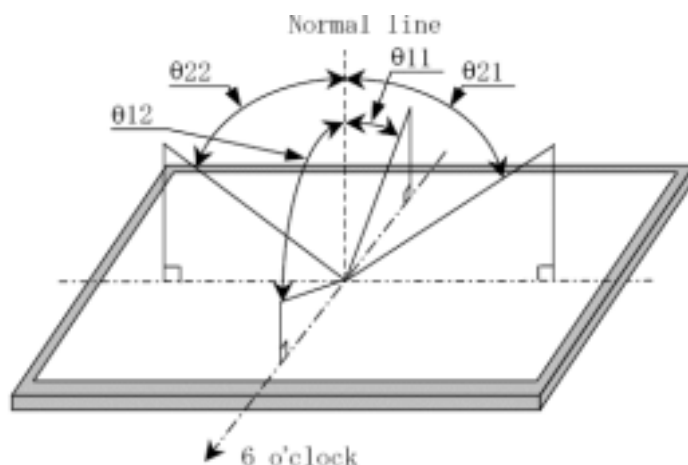
Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing angle range	Vertical	θ_{11}	CR>=5	40	55	-	Deg.	【Note1,4】
		θ_{12}		70	80	-	Deg.	
	Horizontal	θ_{21}, θ_{22}		70	80	-	Deg.	
	Vertical	θ_{11}	CR>=10	30	45	-	Deg.	
		θ_{12}		45	55	-	Deg.	
	Horizontal	θ_{21}, θ_{22}		50	60	-	Deg.	
Contrast ration		CR	$\theta=0^{\circ}$	300	350	-		【Note2,4】
Response Time	Rise	γ_r	$\theta=0^{\circ}$	-	5	20	ms	【Note3,4】
	Fall	γ_d		-	20	40	ms	
Chromaticity of White		x		0.283	0.313	0.343		【Note 4】
		y		0.299	0.329	0.359		
Chromaticity of Red		x		0.551	0.581	0.611		
		y		0.292	0.322	0.352		
Chromaticity of Green		x		0.277	0.307	0.337		
		y		0.516	0.546	0.576		
Chromaticity of Blue		x		0.121	0.151	0.181		
		y		0.097	0.127	0.157		
Luminance of white		Y_L	200	260	-	cd/m ²	IL=4.5mArms Fl=60kHz 【Note 4】	
White Uniformity		δ_w	-	-	1.25	-	【Note 5】	

The measurement shall be executed 30 minutes after lighting at rating.

The optical characteristics shall be measured in a dark room or equivalent state with the method shown in Fig.2 below.



【Note1】 Definitions of viewing angle range:



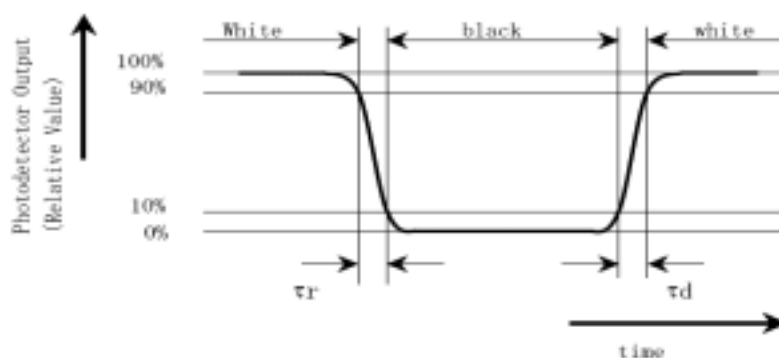
【Note2】 Definition of contrast ratio:

The contrast ratio is defined as the following.

$$\text{Contrast Ratio (CR)} = \frac{\text{Luminance (brightness) with all pixels white}}{\text{Luminance (brightness) with all pixels black}}$$

【Note3】 Definition of response time:

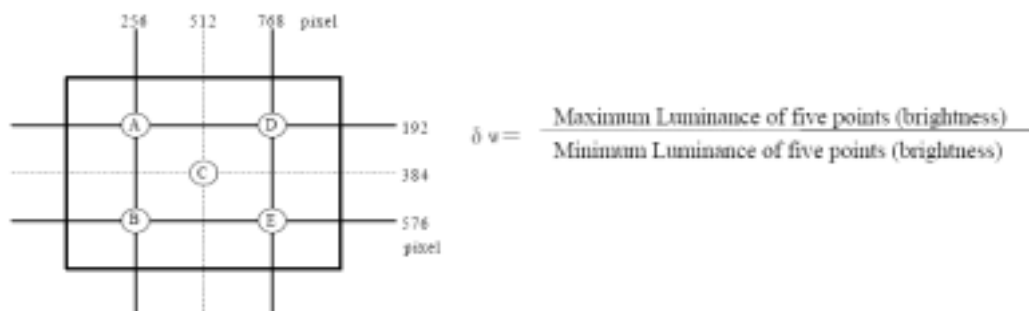
The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



【Note4】 This shall be measured at center of the screen.

【Note5】 Definition of white uniformity:

White uniformity is defined as the following with five measurements (A~E).



10. Handling Precautions

- a) Be sure to turn off the power supply when inserting or disconnecting the cable.
- b) Be sure to design the cabinet so that the module can be installed without any extra stress such as warp or twist.
- c) Since the front polarize is easily damaged, pay attention not to scratch it.
- d) Since long contact with water may cause discoloration or spots, wipe off water drop immediately.
- e) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- f) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.
Handle with care.
- g) Since CMOS LSI is used in this module, take care of static electricity and take the human earth into consideration when handling.
- h) Make sure the four mounting holes of the module are grounded sufficiently. Take electro-magnetic interference (EMI) into consideration.
- i) The module has some printed circuit boards (PCBs) on the back side. Take care to keep them from any stress or pressure when handling or installing the module; otherwise some of electronic parts on the PCBs may be damaged.
- j) Observe all other precautionary requirements in handling components.
- k) When some pressure is added onto the module from rear side constantly, it causes display non-uniformity issue, functional defect, etc. So, please avoid such design.
- l) When handling LCD modules and assembling them into cabinets, please be noted that long-term storage in the environment of oxidization or deoxidization gas and the use of such materials as reagent, solvent, adhesive, resin, etc. which generate these gasses, may cause corrosion and discoloration of the LCD modules.

11. Packing form

- a) Production country : JAPAN , TAIWAN, CHINA
- b) Piling number of cartons : maximum 6 cartons
- c) Packing quantity in one carton : 10 modules
- d) Carton size : 408mm(W) × 340mm(H) × 398mm(D)
- e) total mass of one carton filled with full modules : 12.0kg(typ.)

12 Reliability test items

No.	Test item	Conditions
1	High temperature storage test	Ta = 60 240h
2	Low temperature storage test	Ta = -25 240h
3	High temperature & high humidity operation test	Ta = 40 ; 95%RH 240h (No condensation)
4	High temperature operation test	Ta = 60 240h (The temperature of panel surface)
5	Low temperature operation test	Ta = 0 240H
6	Vibration test(non- operating)	Waveform : Sine wave Frequency : 10□ 57Hz/Vibration width (one side) : 0.075mm : 58~500Hz/Gravity : 9.8m/s ² Sweep time : 11minutes Test period : 3 hours (1 hour for each direction of X,Y,Z)
7	Shock test(non- operating)	Max. gravity : 490m/s ² Pulse width : 11ms, sine wave Direction : ±X, ±Y, ±Z, once for each direction.
8	Thermal shock test(Storage)	Ta=-25 ~60 ; 5 cycles Test period : 10 hours (1 hour for each temperature)
9	Altitude	Ta=50 ,70kPa,3,048m(10,000ft), t=24h (Operating) Ta=70 ,12kPa,15,240m(50,000ft), t=24h (Storage)

[Result Evaluation Criteria]

Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function.

Fig.1. Outline dimensions

