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Direct Conversion 4FSK Data Receiver Target Specification

DS4760 - 1.2 August 1997

The ZIF100 IC is a direct conversion receiver. The device provides the functions required for the reproduction of I and Q from either 2FSK or 4FSK transmission.

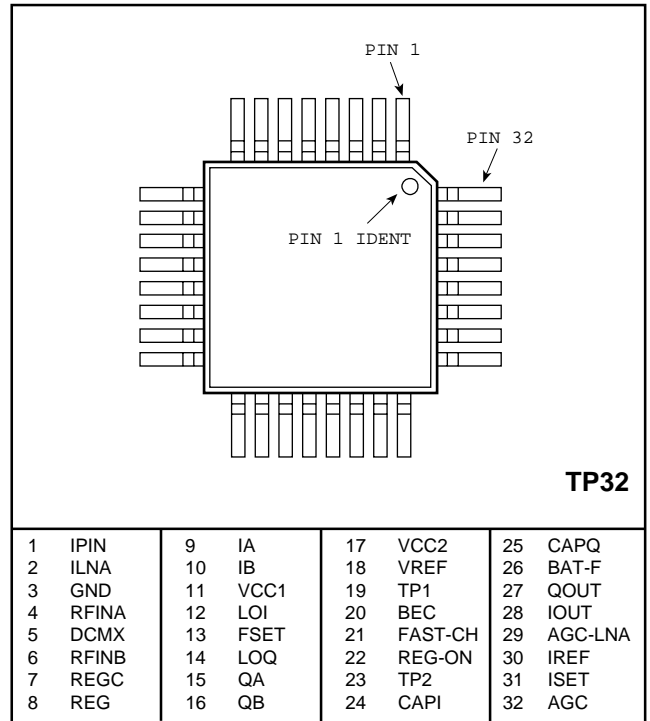
Adjacent channel rejection is provided using programmable channel filters. RF and Audio AGC functions assist operation when large interfering signals are present. The ZIF100 also includes an on chip 1 volt regulator function capable of sourcing up to 3mA and a battery flag function. Both the I and Q channel outputs and battery flag have open collector outputs to ease their interface with other devices.

FEATURES

- Low Power Consumption - most of the device is powered directly from a single cell to minimise the power consumed
- Very Competitive Sensitivity Performance
- Programmable Channel Filtering
- 1 Volt Regulator and 1.25 Volt Reference Voltage with Pre-wakeup Mode
- Wide Range Automatic Gain Control (AGC) including Mixer Overload Protection
- Battery Economy Function
- 1mm Thick TQFP Package
- Programmable LNA Current Source

APPLICATIONS

- Pagers - including small form factor designs such as credit card pagers, watch pagers and PCMCIA applications
- Low data rate receivers - security/remote control



ABSOLUTE MAXIMUM RATINGS

Supply Voltage	4V
Storage temperature	-55°C to +150°C
Operating temperature	-20°C to +70°C

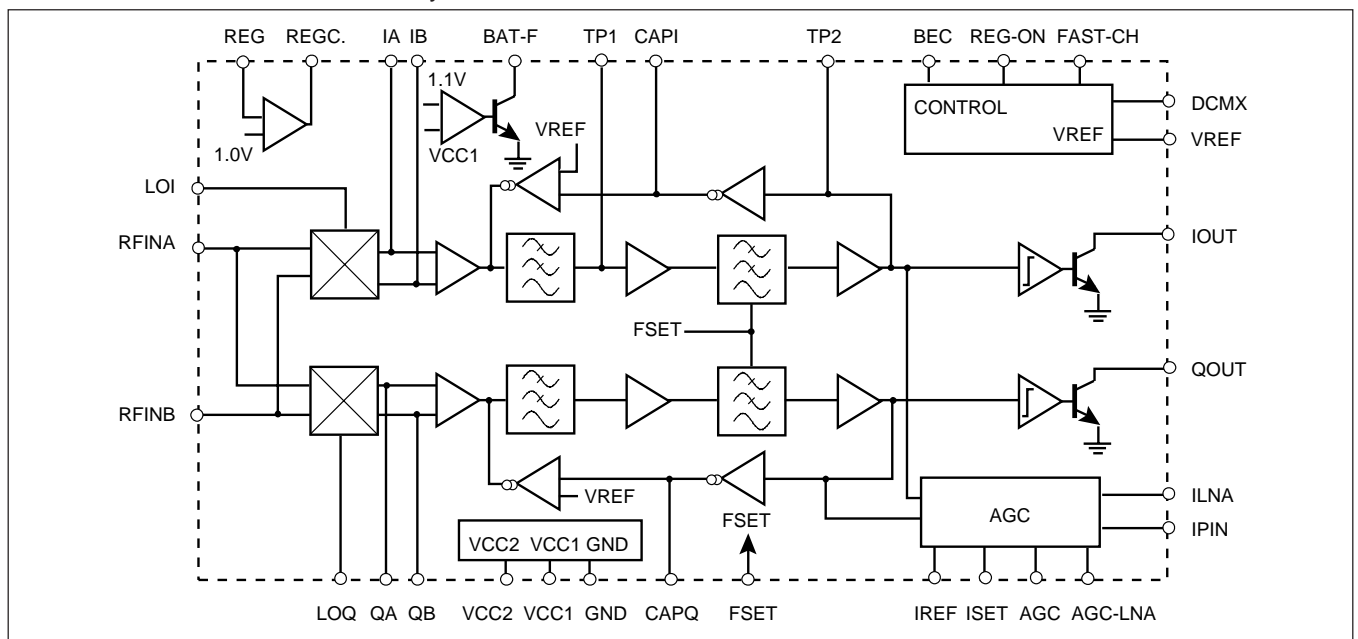
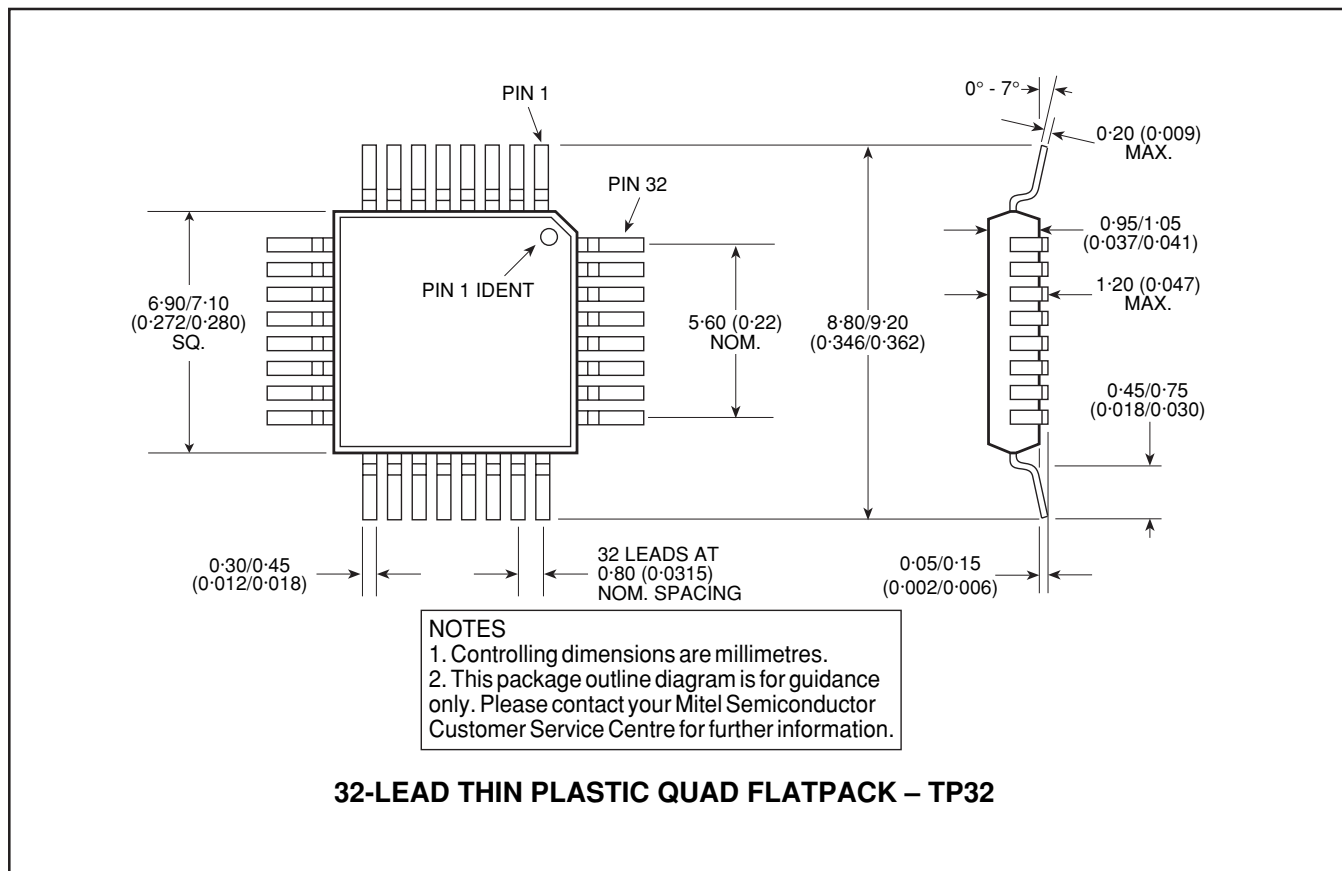


Fig.2 ZIF100 block diagram

ZIF100

PACKAGE DETAILS

Dimensions are shown thus: mm (in). For further package information, please contact your local Customer Service Centre.



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