

**• General Description**

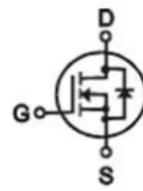
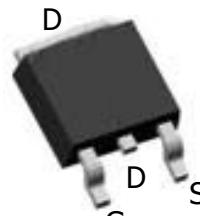
The ZM027N03D combines advanced trench MOSFET technology with a low resistance package to provide extremely low  $R_{DS(ON)}$ . This device is ideal for load switch and battery protection applications.

**• Features**

- Advance high cell density Trench technology
- Low  $R_{DS(ON)}$  to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

**• Application**

- MB/VGA Vcore
- SMPS 2<sup>nd</sup> Synchronous Rectifier
- POL application
- BLDC Motor driver

**• Product Summary** $V_{DS} = 30V$  $R_{DS(ON)} = 2.7m\Omega$  $I_D = 95A$ 

TO-252

**• Ordering Information:**

Part NO.	ZM027N03D
Marking	ZM027N03
Packing Information	REEL TAPE
Basic ordering unit (pcs)	2500

**• Absolute Maximum Ratings ( $T_c = 25^\circ C$ )**

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D @ T_c = 25^\circ C$	95	A
	$I_D @ T_c = 75^\circ C$	72	A
	$I_D @ T_c = 100^\circ C$	60	A
Pulsed Drain Current <sup>①</sup>	$I_{DM}$	220	A
Total Power Dissipation	$P_D @ T_c = 25^\circ C$	70	W
Total Power Dissipation	$P_D @ T_A = 25^\circ C$	2.8	W
Operating Junction Temperature	$T_J$	-55 to 175	$^\circ C$
Storage Temperature	$T_{STG}$	-55 to 175	$^\circ C$
Single Pulse Avalanche Energy	$E_{AS}$	350	mJ
Avalanche Current	$I_{AS} I_{AR}$	60	A



## ● Thermal resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub>	-	-	1.8	° C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub>	-	-	45	° C/W
Soldering temperature, wave soldering for 10s	T <sub>sold</sub>	-	-	265	° C

## ● Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	30			V
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.2		2.5	V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V			1.0	uA
Gate- Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V			±100	nA
Static Drain-source On Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =24A		2.7	3.6	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =12A		4.6	5.5	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =25V, I <sub>D</sub> =10A		30		s
Source-drain voltage	V <sub>SD</sub>	I <sub>S</sub> =24A			1.28	V

## ● Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Input capacitance	C <sub>iss</sub>	f = 1MHz, V <sub>DS</sub> =25V	-	2800	-	pF
Output capacitance	C <sub>oss</sub>		-	420	-	
Reverse transfer capacitance	C <sub>rss</sub>		-	280	-	

● Gate Charge characteristics(T<sub>a</sub> = 25°C)

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Gate Resistance	R <sub>g</sub>	f = 1MHz V <sub>DD</sub> = 25V I <sub>D</sub> = 8A V <sub>GS</sub> = 10V		2.5		Ω
Total gate charge	Q <sub>g</sub>		-	27	-	nC
Gate - Source charge	Q <sub>gs</sub>		-	8.6	-	
Gate - Drain charge	Q <sub>gd</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =15V R <sub>G</sub> = 3.3Ω, I <sub>D</sub> = 15A	-	13.8	-	
Turn-ON Delay time	t <sub>D(on)</sub>			12		ns
Turn-ON Rise time	t <sub>r</sub>	R <sub>G</sub> = 3.3Ω, I <sub>D</sub> = 15A		44		ns



Turn-Off Delay time	$t_{D(\text{off})}$	$V_{DD} = 20 \text{ V}$ , $dI/dt = 100 \text{ A/s}$ , $I_S = 30 \text{ A}$		50		ns
Turn-Off Fall time	$t_f$			15		ns
Reverse Recovery Time	$t_{RR}$			5.8		ns
Charge Time	$t_a$			3.4		ns
Discharge Time	$t_b$			2.4		ns
Reverse Recovery Charge	$Q_{RR}$			1.6		nC

Note: ① Pulse Test : Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$  ;

Fig.1 Power Dissipation

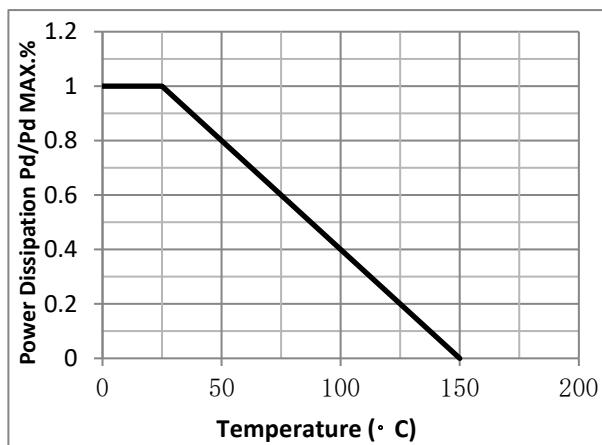


Fig.2 Typical output Characteristics

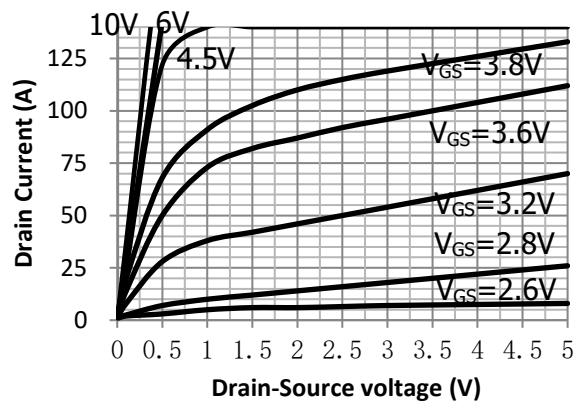


Fig.3 Threshold Voltage V.S Junction Temperature

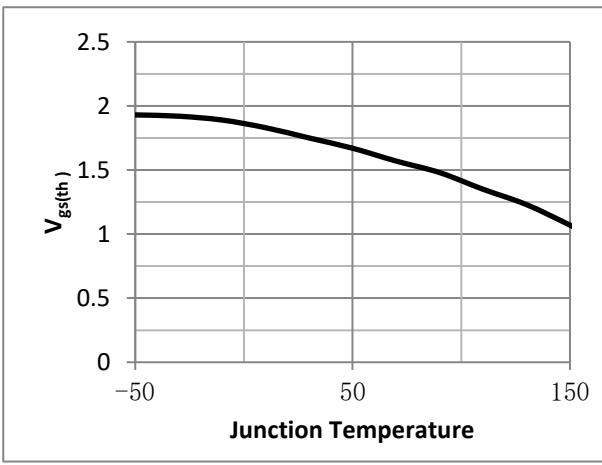


Fig.4 Resistance V.S Drain Current

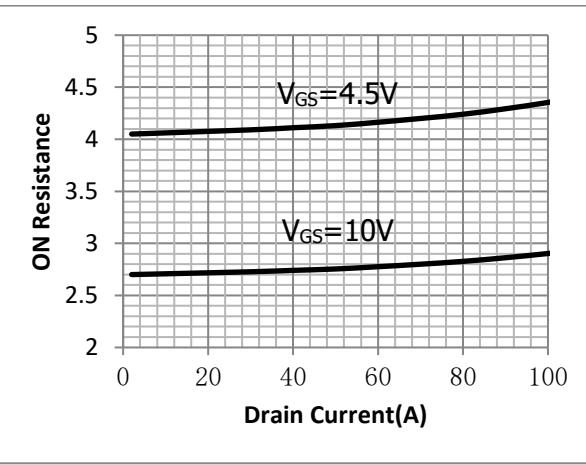




Fig.5 On-Resistance VS Gate Source Voltage

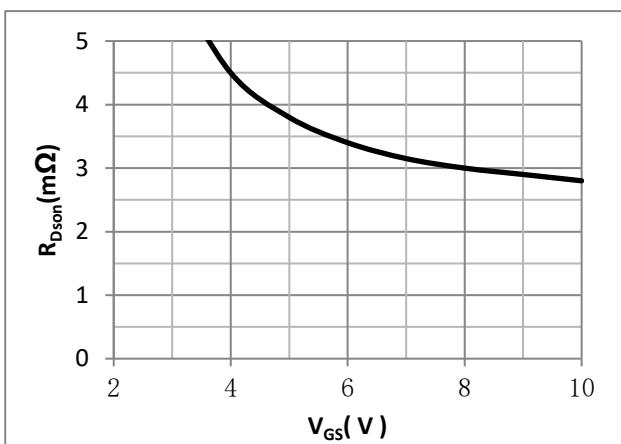


Fig.6 On-Resistance V.S Junction Temperature

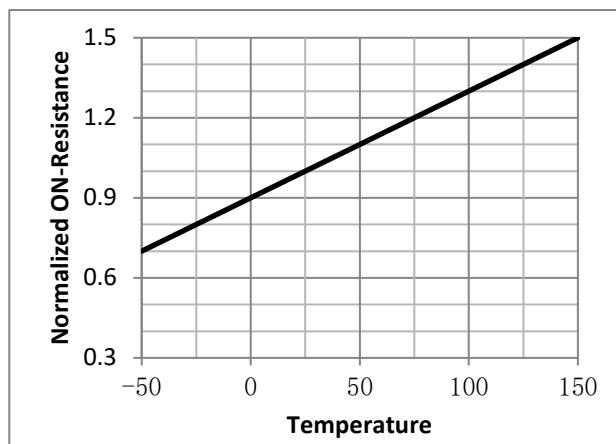


Fig.7 SOA Maximum Safe Operating Area

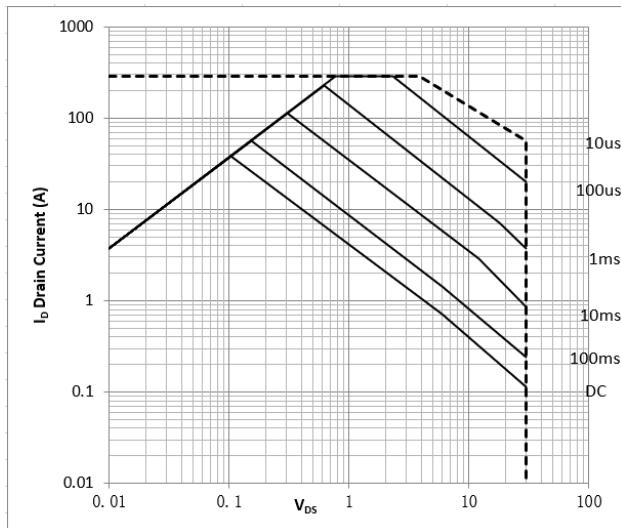


Fig.8 ID-Junction Temperature

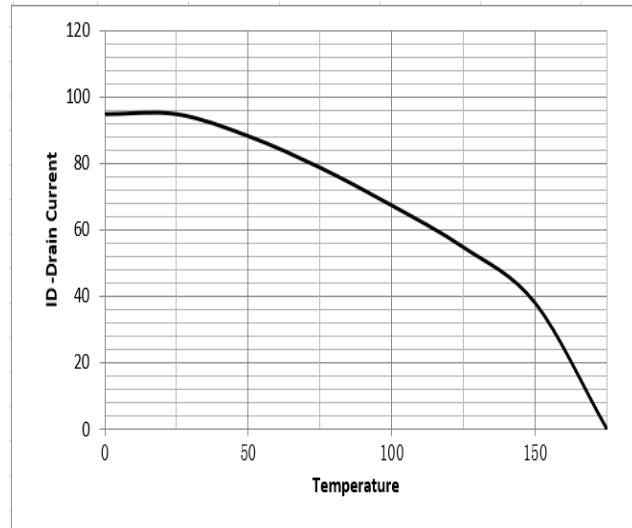


Figure 9. Diode Forward Voltage vs. Current

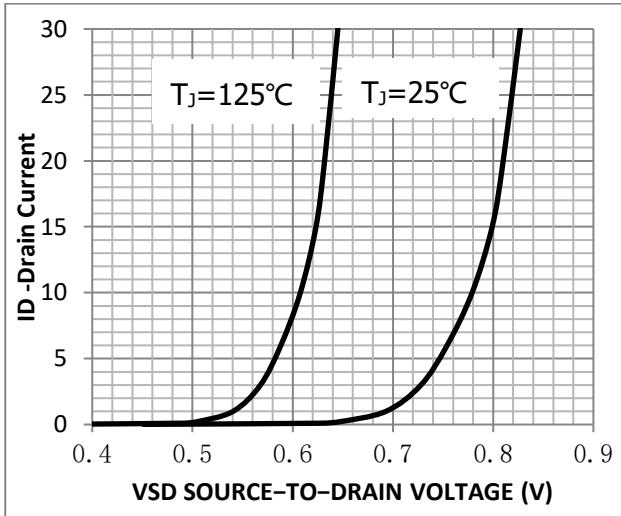


Figure 10. Transfer Characteristics

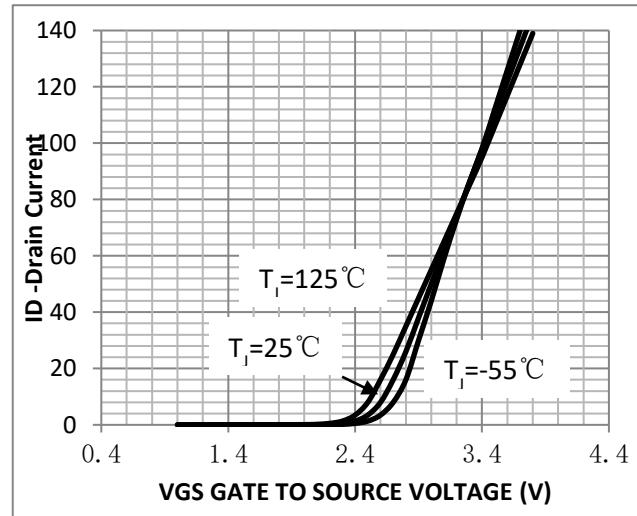




Figure 11. Gate-to-Source and  
Drain-to-Source Voltage vs. Total Charge

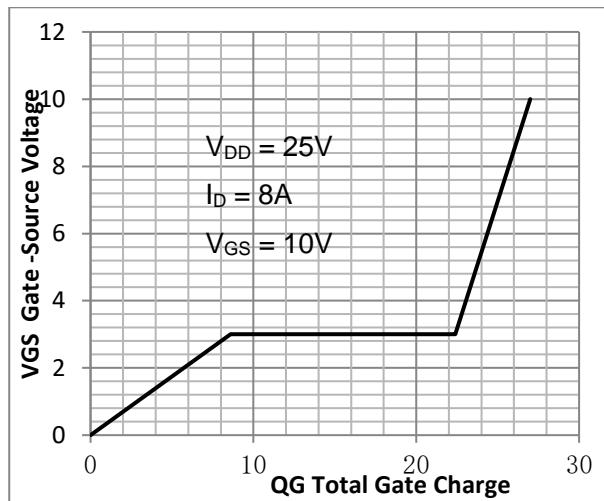


Fig.12 Capacitance Variation

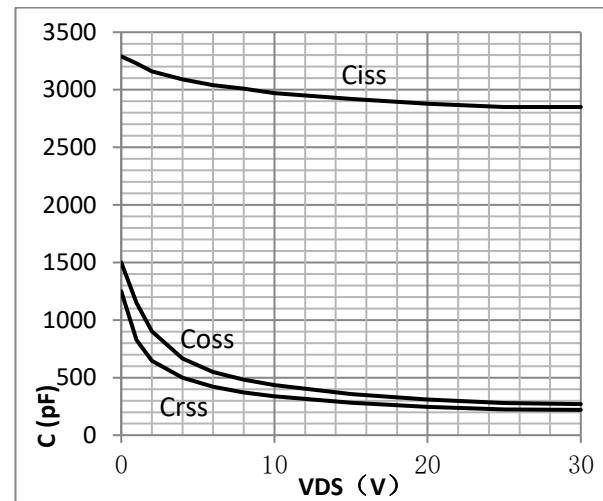


Fig.13 Switching Time Measurement Circuit

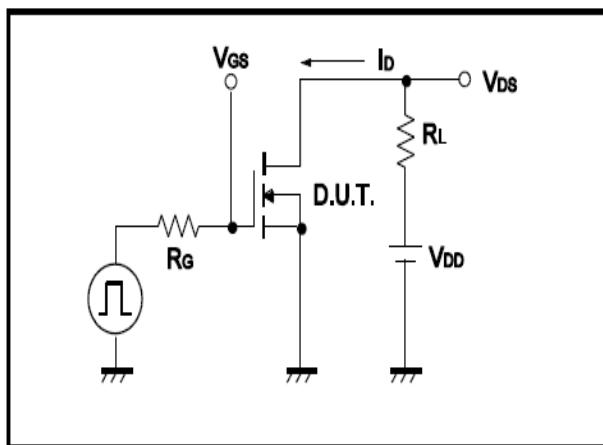


Fig.14 Gate Charge Waveform

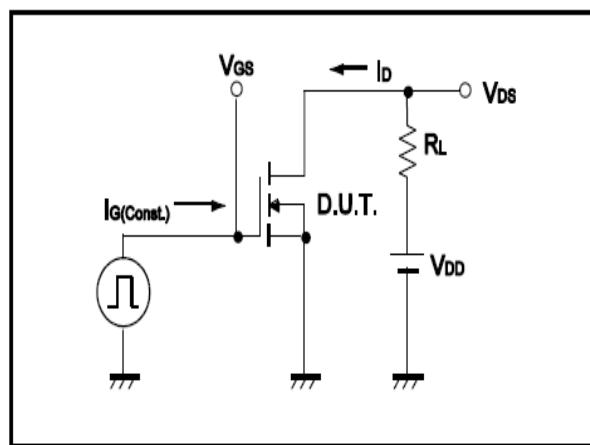


Fig.15 Avalanche Measurement Circuit

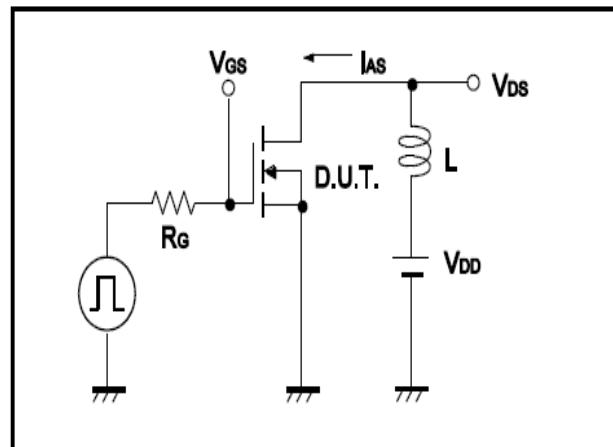


Fig.16 Avalanche Waveform

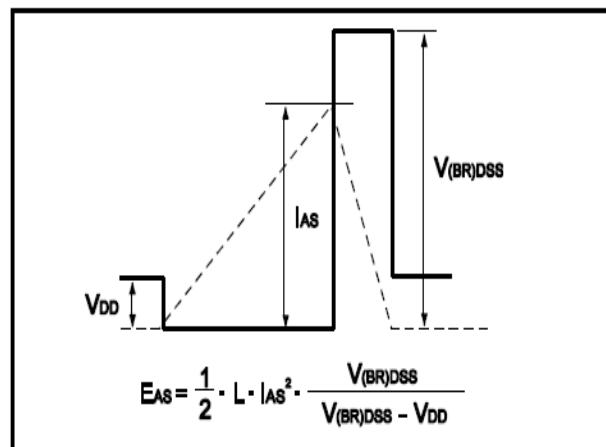
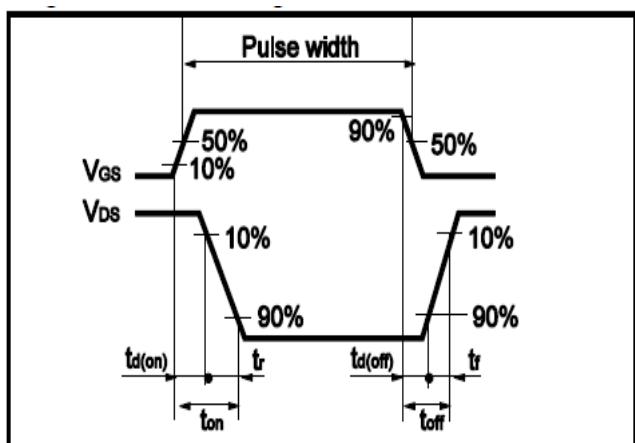




Fig.17 Gate Charge Waveform





## •Dimensions (TO-252)

Unit: mm

SYMBOL	min	max	SYMBOL	min	max
A	2.10	2.50	B	0.85	1.25
b	0.50	0.80	b1	0.50	0.90
b2	0.45	0.70	C	0.45	0.70
D	6.30	6.75	D1	5.10	5.50
E	5.30	6.30	e1	2.25	2.35
L1	9.20	10.60	e2	4.45	4.75
L2	0.90	1.75	L3	0.60	1.10
K	0.00	0.23			

