

ZTX749A

PNP Low Saturation Transistor

• This device are designed with high current gain and low saturation voltage with collector currents up to 2A continuous.



Absolute Maximum Ratings TA=25°C unless otherwise noted

Symbol	Parameter		Value	Units
V_{CEO}	Collector-Emitter Voltage		-35	V
V _{CBO}	Collector-Base Voltage		-45	V
V _{EBO}	Emitter-Base Voltage	(1	-5	V
I _C	Collector Current - Continuous		-2	A
T _J , T _{STG}	Operating and Storage Junction Temperature Ra	nge	-55 (+150)	°C

^{*} These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

1. These ratings are based on a maximum junction temperature of 150°C.

2. These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

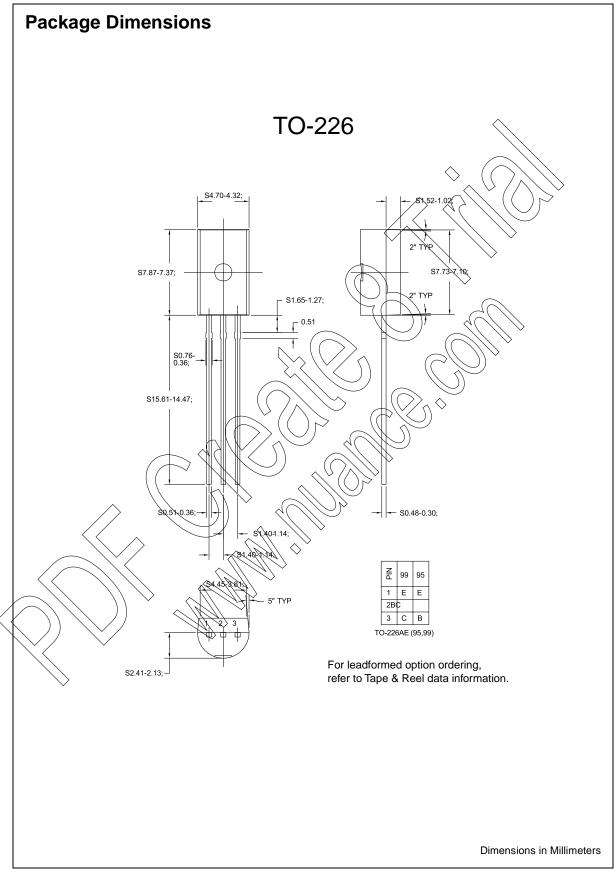
Electrical Characteristics TA725°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units	
Off Characteristics						
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C = 10m/A	-35		V	
BV _{CBO}	Collector-Base Breakdown Voltage	16 = -100 MA-	45		V	
BV _{EBO}	Emitter-Base Breakdown Voltage	1E = 100/1A-	5		V	
I _{CBO}	Collector Cutoff Current	V _{CB} ⇒ -30V		-100	nA	
		$V_{CB} = -30V, T_A = 100^{\circ}C$		-10	μΑ	
I _{EBO}	Emitter Cutoff Current	V _{EB} = -4V		-100	nA	
On Charac	On Characteristics*					
h _{FE}	DC Current Gain	$I_{C} = -50 \text{mA}, V_{CE} = -2 \text{V}$ $I_{C} = -1 \text{A}, V_{CE} = -2 \text{V}$ $I_{C} = -2 \text{A}, V_{CE} = -2 \text{V}$ $I_{C} = -6 \text{A}, V_{CE} = -2 \text{V}$	70 100 75 15	300		
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_C = -1A$, $I_B = -100mA$ $I_C = -2A$, $I_B = -200mA$		-300 -500	mV	
V _{BE} (sat)	Base-Emitter Saturation Voltage	$I_C = -1A, I_B = -100mA$		-1.25	V	
V _{BE} (on)	Base-Emitter On Voltage	I _C = -1A, V _{CE} = -2V		-1	V	
Small-Signal Characteristics						
C _{obo}	Output Capacitance	$V_{CB} = -10V, I_{E} = 0, f = 1MHz$		100	РF	
f _T	Transition Frequency	I _C = -100mA, V _{CE} = -5V f = 100MHz	100			

^{*} Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%

Thermal Characteristics T_A=25°C unless otherwise noted

Symbol	Parameter	Max.	Units
P _D	Total Device Dissipation	1	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	125	°C/W



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