



Product Summary

BV _{DSS}	RDS(on)	ID TA = +25°C
001/	125mΩ @ V _{GS} = -10V	-4.3A
-60V	190mΩ @ V _{GS} = -4.5V	-3.5A

Description and Applications

This MOSFET is designed to minimize the on-state resistance yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor Control
- DC-DC Converters
- Power Management Functions
- Uninterrupted Power Supply

60V P-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Fast Switching Speed
- Low Gate Drive
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
 For automotive applications requiring specific change
- control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotiveproducts/.

• This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

 An Automotive-Compliant Part is Available Under Separate Datasheet (ZXMP6A17GQ)

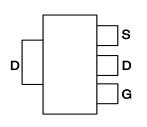
Mechanical Data

- Case: SOT223
- Case Material: Molded Plastic, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 3
- Weight: 0.112 grams (Approximate)

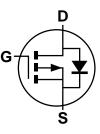


SOT223 (Type DN)





Pin Out - Top View



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
ZXMP6A17GTA	SOT223 (Type DN)	1,000/Tape & Reel
ZXMP6A17GTC	SOT223 (Type DN)	4,000/ Tape & Reel

Notes: 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

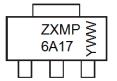
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information

SOT223 (Type DN)



 $\begin{array}{l} \text{ZXMP6A17} = \text{Product Type Marking Code} \\ \text{YWW} = \text{Date Code Marking} \\ \text{Y or } \overrightarrow{\text{Y}} = \underbrace{\text{Year (ex: 1 = 2021)}} \\ \text{WW or } \overrightarrow{\text{WW}} = \text{Week (01 to 53)} \end{array}$

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		Vdss	-60	V	
Gate-Source Voltage		Vgss	±20	V	
		(Note 6)		-4.3	
Continuous Drain Current	Vgs = 10V	T _A = +70°C (Note 6)	ID	-3.5	А
		(Note 5)	-	-3.0	
Pulsed Drain Current	$V_{GS} = 10V$	(Note 7)	I _{DM}	-13.7	А
Continuous Source Current	(Body Diode)	(Note 6)	ls	-4.3	А
Pulsed Source Current (Bod	y Diode)	(Note 7)	I _{SM}	-13.7	А

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Power Dissipation	(Note 5)	5	2.0 16	W	
Linear Derating Factor	(Note 6)	— P _D	3.9 31	mW/°C	
Thermal Resistance, Junction to Ambient	(Note 5)	D	62.5	°C/W	
	(Note 6)	R _{0JA}	32.0		
Thermal Resistance, Junction to Lead	(Note 8)	Rejl	9.8		
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C	

Notes: 5. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.

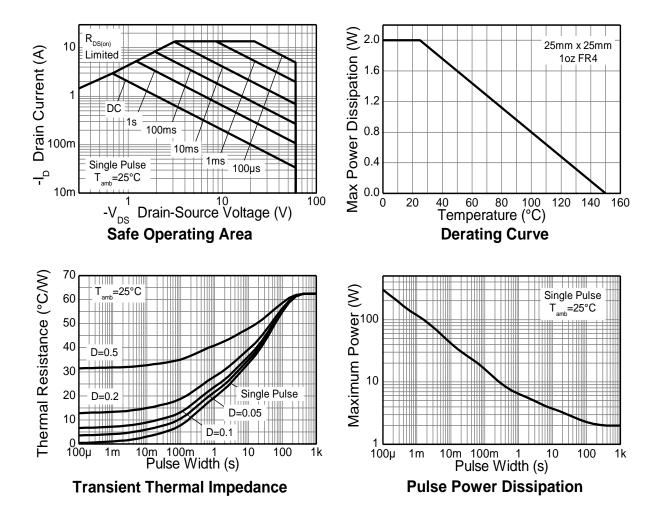
6. Same as Note 5, except the device is measured at t \leq 10sec.

7. Same as Note 5, except the device is pulsed with D = 0.02 and pulse width 300μ s. The pulse current is limited by the maximum junction temperature.

8. Thermal resistance from junction to solder-point (at the end of the drain lead).



Thermal Characteristics





Notes:

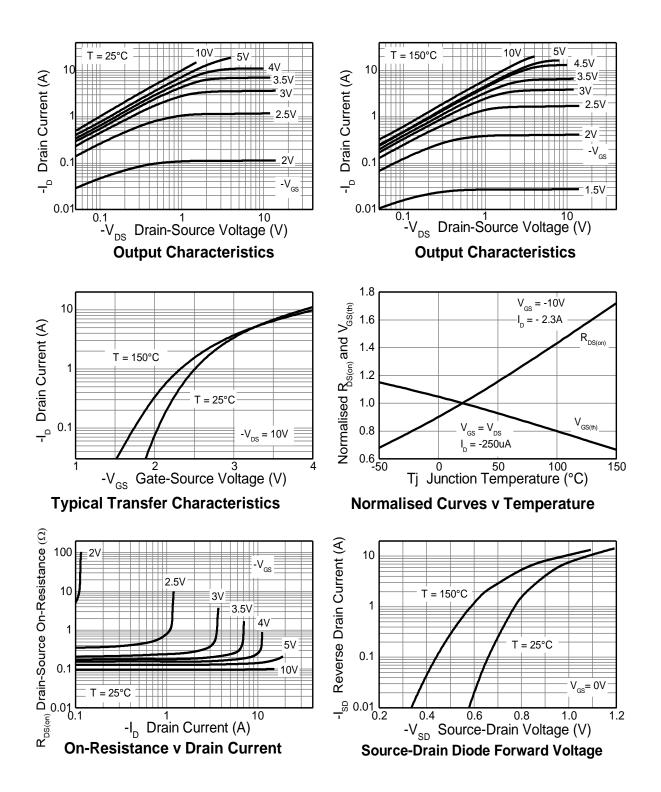
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test	Condition	
OFF CHARACTERISTICS			•	•	•			
Drain-Source Breakdown Voltage	BVDSS	-60			V	I _D = -250µA, V _{GS} = 0V		
Zero Gate Voltage Drain Current	IDSS	_	_	-0.5	μA	V _{DS} = -60V, V	$V_{DS} = -60V, V_{GS} = 0V$	
Gate-Source Leakage	lgss	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$		
ON CHARACTERISTICS								
Gate Threshold Voltage	V _{GS(th)}	-1.0	_	—	V	$I_{D} = -250 \mu A$, $V_{DS} = V_{GS}$		
Statia Drain Source On Basistones (Nate 0)	6		96	125		V_{GS} = -10V, I_D	= -2.2A	
Static Drain-Source On-Resistance (Note 9)	RDS(ON)	—	120	190	mΩ	V _{GS} = -4.5V, I _E) = -1.8A	
Forward Transconductance (Notes 9 & 10)	g fs	_	4.7	_	S	V _{DS} = -15V, I _D = -2.2A		
Diode Forward Voltage (Note 9)	Vsd	_	-0.85	-0.95	V	Is = -2.0A, V _{GS} = 0V, T _J = +25°C		
Reverse Recovery Time (Note 10)	t _{rr}		25.1	_	ns	Is = -1.7A, di/dt = 100A/μs, T _J = +25°C		
Reverse Recovery Charge (Note 10)	Qrr	_	27.2	_	nC			
DYNAMIC CHARACTERISTICS (Note 10)	·		•		•	•		
Input Capacitance	Ciss	_	637	_	pF	V _{DS} = -30V, V _{GS} = 0V - f = 1MHz		
Output Capacitance	Coss	_	70.0	_	pF			
Reverse Transfer Capacitance	Crss	_	53.0	—	pF			
Total Gate Charge (Note 11)	Qg	_	9.0	_	nC	Vgs = -4.5V		
Total Gate Charge (Note 11)	Qg	_	17.7	_	nC		Vps = -30V	
Gate-Source Charge (Note 11)	Q _{gs}		1.6	_	nC	V _{GS} = -10V I _D = -2.2A		
Gate-Drain Charge (Note 11)	Q _{gd}	_	4.4	_	nC			
Turn-On Delay Time (Note 11)	tD(on)	_	2.6	_	ns		•	
Turn-On Rise Time (Note 11)	tr	_	3.4		ns	$V_{DD} = -30V, V_{GS} = -10V$ $I_D = -1A, R_G \cong 6.0\Omega$		
Turn-Off Delay Time (Note 11)	t _{D(off)}	_	26.2		ns			
Turn-Off Fall Time (Note 11)	t _f		11.3	_	ns			

9. Measured under pulsed conditions. Pulse width \leq 300µs; duty cycle \leq 2%. 10. For design aid only, not subject to production testing. 11. Switching characteristics are independent of operating junction temperatures.



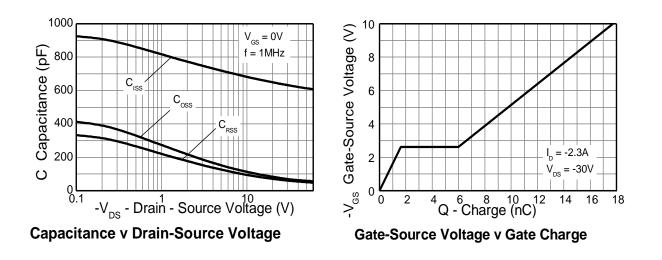
Typical Characteristics



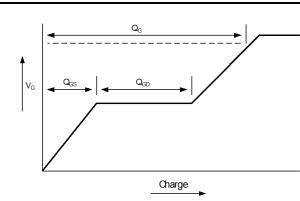


ZXMP6A17G

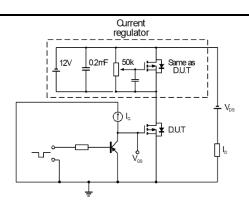
Typical Characteristics (continued)



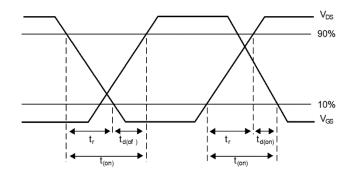
Test Circuits



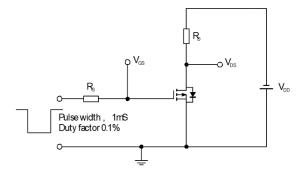
Basic gate charge waveform



Gate charge test circuit





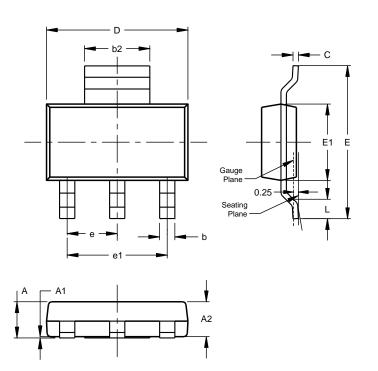


Switching time test circuit



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



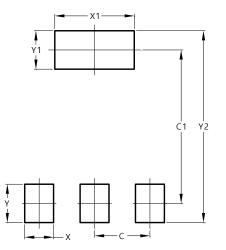
SOT223 (Type DN)					
Dim	Min	Max	Тур		
Α		1.70			
A1	0.01	0.15			
A2	1.50	1.68	1.60		
b	0.60	0.80	0.70		
b2	2.90	3.10			
c	0.20	0.32			
D	6.30	6.70			
ш	6.70	7.30			
E1	3.30	3.70			
e			2.30		
e1			4.60		
Ĺ	0.85				
All [All Dimensions in mm				

SOT223 (Type DN)

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT223 (Type DN)



Dimensions	Value (in mm)
С	2.30
C1	6.40
Х	1.20
X1	3.30
Y	1.60
Y1	1.60
Y2	8.00



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