ZXTD720MC

DUAL 40V PNP LOW SATURATION SWITCHING TRANSISTOR

Features

- V_{CEO} = -40V
- R_{SAT} = 104 mΩ
- I_C = -3A Continuous Collector Current
- Low Equivalent On Resistance
- Low Saturation Voltage (-220mV @ -1A)
- h_{FE} specified up to -3A
- **Lead, Halogen, and Antimony Free/RoHS Compliant (Note 1)**
- **"Green" Devices (Note 2)**

Mechanical Data

- Case: DFN3020B-8
- Case material: Molded Plastic. "Green" Molding Compound.
- UL Flammability Rating 94V-0
- Terminals: Pre-Plated NiPdAu leadframe.
- Nominal package height: 0.8mm
- Moisture Sensitivity: Level 1 per J-STD-020
- Solderable per MIL-STD-202, Method 208
- Weight: 0.013 grams (approximate)

Applications

- DC-DC Converters
- Charging circuits
- Power switches
- Motor control
- CCFL Backlighting

DFN3020B-8

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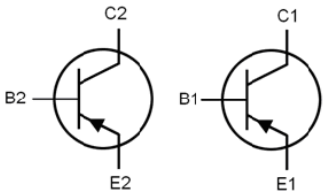
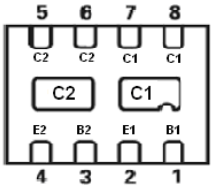


Device symbol



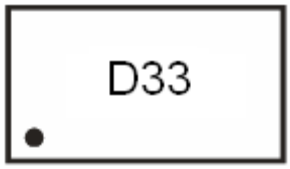
Pin Configuration

Ordering Information

Product	Status	Package	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXTD720MCTA	Active	DFN3020B-8	D33	7	8	3000

Notes: 1. No purposefully added lead. Halogen and Antimony Free.
2. Diodes Inc's "Green" Policy can be found on our website at http://www.diodes.com

Marking Information



D33 = Product type Marking Code
Dot denotes Pin 1

Maximum Ratings

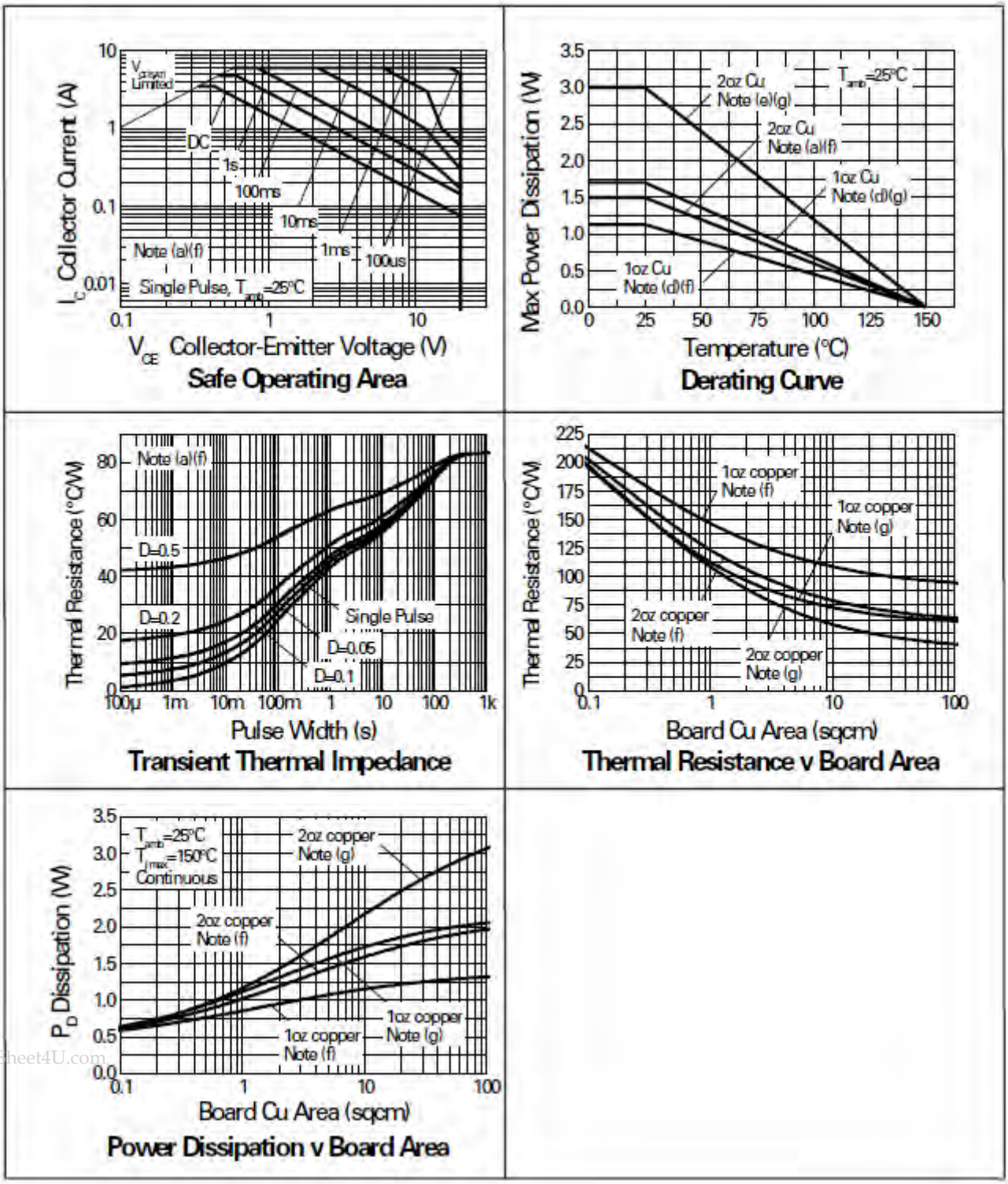
Parameter	Symbol	Limit	Unit
Collector-Base Voltage	V_{CBO}	-50	V
Collector-Emitter Voltage	V_{CEO}	-40	V
Emitter-Base Voltage	V_{EBO}	-7.5	V
Peak Pulse Current	I_{CM}	-4	A
Continuous Collector Current (Notes a and b)	I_C	-3	A
Base Current	I_B	-1	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation at T_A = 25°C (Notes a and f) Linear Derating Factor	P_D	1.5 12	W mW/°C
Power Dissipation at T_A = 25°C (Notes b and f) Linear Derating Factor	P_D	2.45 19.6	W mW/°C
Power Dissipation at T_A = 25°C (Notes c and f) Linear Derating Factor	P_D	1 8	W mW/°C
Power Dissipation at T_A = 25°C (Notes d and f) Linear Derating Factor	P_D	1.13 9	W mW/°C
Power Dissipation at T_A = 25°C (Notes d and g) Linear Derating Factor	P_D	1.7 13.6	W mW/°C
Power Dissipation at T_A = 25°C (Notes e and g) Linear Derating Factor	P_D	3 24	W mW/°C
Junction to Ambient (Notes a and f)	$R_{\theta JA}$	83.3	°C/W
Junction to Ambient (Notes b and f)	$R_{\theta JA}$	51	°C/W
Junction to Ambient (Notes c and f)	$R_{\theta JA}$	125	°C/W
Junction to Ambient (Notes d and f)	$R_{\theta JA}$	111	°C/W
Junction to Ambient (Notes d and g)	$R_{\theta JA}$	73.5	°C/W
Junction to Ambient (Notes e and g)	$R_{\theta JA}$	41.7	°C/W
Junction Temperature	T_J	150	°C
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C

Notes:
a. For a dual device surface mounted on 8 sq cm single sided 2 oz copper on FR4 PCB, in still air conditions **with all exposed pads attached**. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.
b. Measured at t <5 secs for a dual device surface mounted on 8 sq cm single sided 2 oz copper on FR4 PCB, in still air conditions **with all exposed pads attached**. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.
c. For a dual device surface mounted on 8 sq cm single sided 2 oz copper on FR4 PCB, in still air conditions **with minimal lead connections only.**
d. For a dual device surface mounted on 10 sq cm single sided 1 oz copper on FR4 PCB, in still air conditions **with all exposed pads attached**. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.
e. For a dual device surface mounted on 85 sq cm single sided 2 oz copper on FR4 PCB, in still air conditions **with all exposed pads attached**. The copper area is split down the centre line into two separate areas with one half connected to each half of the dual device.
f. For a dual device with one active die.
g. For dual device with 2 active die running at equal power.

Thermal Characteristics and Derating information

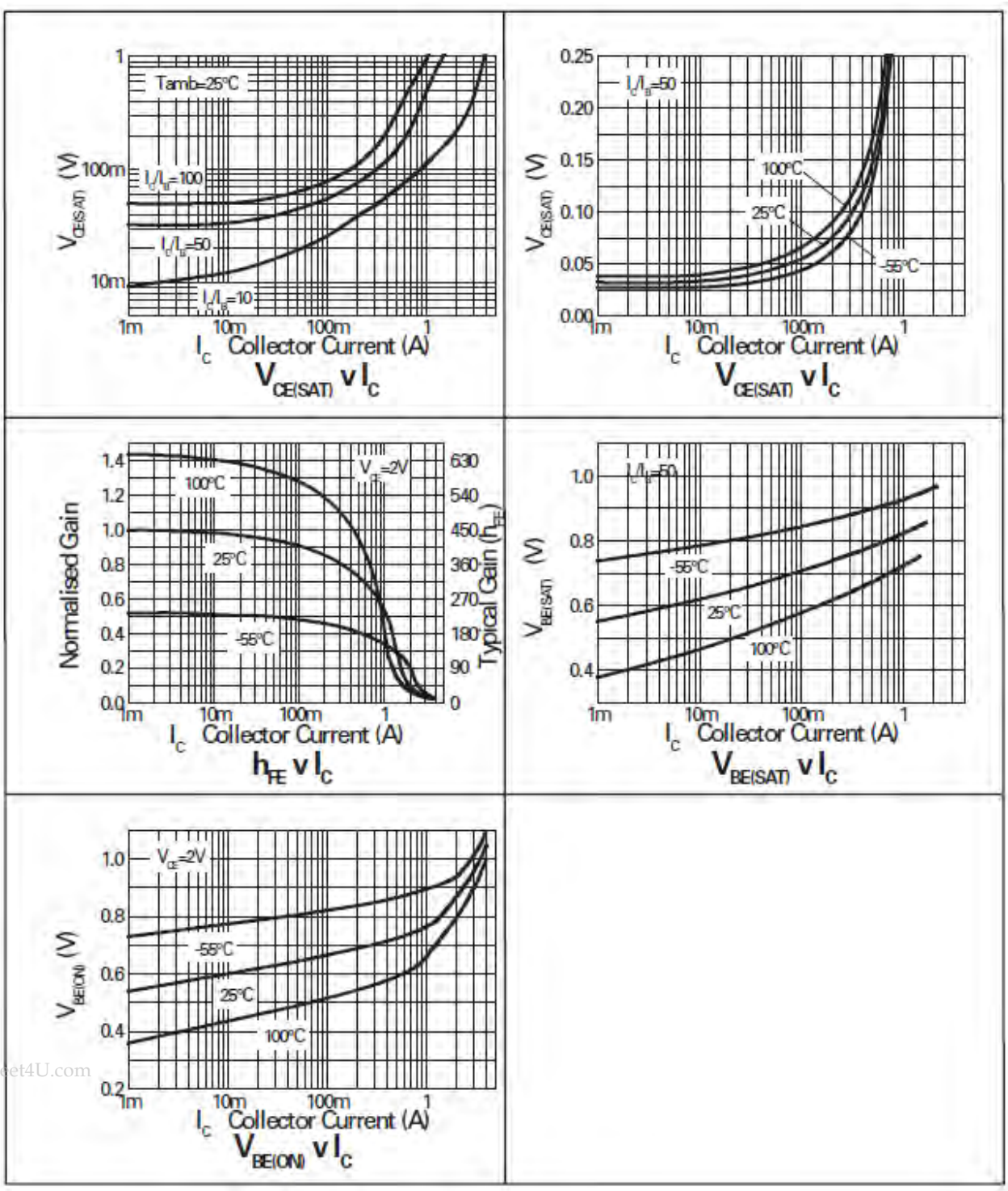


Electrical Characteristics @T_A = 25°C unless otherwise specified

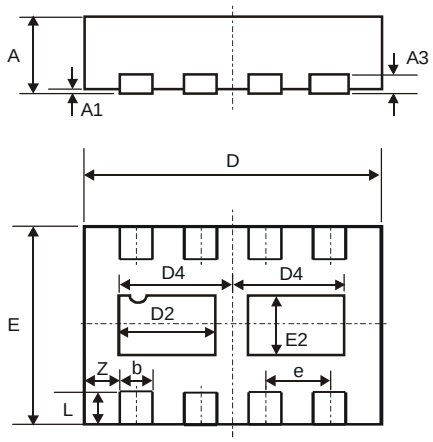
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	$V_{(BR)CBO}$	-50	-80	-	V	I_C = -100µA
Collector-Emitter Breakdown Voltage (Note 3)	$V_{(BR)CEO}$	-40	-70	-	V	I_C = -10mA
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	-7.5	-8.5	-	V	I_E = -100µA
Collector Cutoff Current	I_{CBO}	-	-	-25	nA	V_{CB} = -16V
Emitter Cutoff Current	I_{EBO}	-	-	-25	nA	V_{EB} = -6V
Collector Emitter Cutoff Current	I_{CES}	-	-	-25	nA	V_{CES} = -10V
Static Forward Current Transfer Ratio (Note 3)	h_{FE}	300	480	-	-	I_C = -10mA, V_{CE} = -2V
		300	450	-	-	I_C = -100mA, V_{CE} = -2V
		180	290	-	-	I_C = -1A, V_{CE} = -2V
		60	130	-	-	I_C = -1.5A, V_{CE} = -2V
		12	22	-	-	I_C = -3A, V_{CE} = -2V
Collector-Emitter Saturation Voltage (Note 3)	$V_{CE(sat)}$	-	-25	-40	mV	I_C =- 0.1A, I_B = -10mA
		-	-150	-220		I_C = -1A, I_B = -50mA
		-	-195	-300		I_C = -1.5A, I_B = -100mA
		-	-210	-300		I_C = -2A, I_B = -200mA
		-	-260	-370		I_C = -2.5A, I_B = -250mA
Base-Emitter Turn-On Voltage (Note 3)	$V_{BE(on)}$	-	-0.89	-0.95	V	I_C = -2.5A, V_{CE} = -2V
Base-Emitter Saturation Voltage (Note 3)	$V_{BE(sat)}$	-	-0.97	-1.05	V	I_C = -2.5A, I_B = -250mA
Output Capacitance	C_{obo}	-	19	25	pF	V_{CB} = -10V. f = 1MHz
Transition Frequency	f_T	150	190	-	MHz	V_{CE} = -10V, I_C = -50mA, f = 100MHz
Turn-on Time	t_{on}	-	40	-	ns	V_{CC} = -15V, I_C = -0.75A
Turn-off Time	t_{off}	-	435	-	ns	I_{B1} = I_{B2} = -15mA

Notes: 3. Measured under pulsed conditions. Pulse width = 300 µs. Duty cycle ≤ 2%

Typical Characteristics

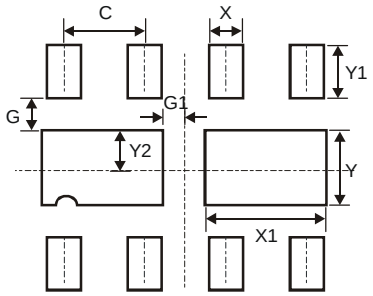


Package Outline Dimensions



DFN3020B-8			
Dim	**Min**	**Max**	**Typ**
A	0.77	0.83	0.80
A1	0	0.05	0.02
A3	-	-	0.15
b	0.25	0.35	0.30
D	2.95	3.075	3.00
D2	0.82	1.02	0.92
D4	1.01	1.21	1.11
e	-	-	0.65
E	1.95	2.075	2.00
E2	0.43	0.63	0.53
L	0.25	0.35	0.30
Z	-	-	0.375
All Dimensions in mm			

Suggested Pad Layout



Dimensions	Value (in mm)
C	0.650
G	0.285
G1	0.090
X	0.400
X1	1.120
Y	0.730
Y1	0.500
Y2	0.365

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