



A Product Line of Diodes Incorporated

**150V NPN LED DRIVING TRANSISTOR IN SOT89** 

ZXTN4004Z

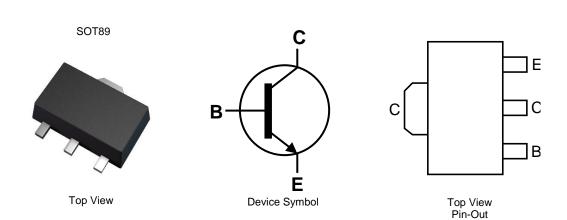
#### Features

- BV<sub>CEO</sub> > 150V
- I<sub>C</sub> = 1A High Continuous Current
- $h_{FE} > 100$  for  $I_C = 150mA$ ,  $V_{CE} = 0.25V$
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

#### **Applications**

LED TV Backlight

- Mechanical Data
- Case: SOT89
- Case Material: Molded Plastic. "Green" Molding Compound; UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads; Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.055 grams (Approximate)



#### Ordering Information (Notes 4 & 5)

Product	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXTN4004ZTA	AEC-Q101	1R8	7	12	1,000 units
ZXTN4004ZQTA	Automotive	1R8	7	12	1,000 units

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

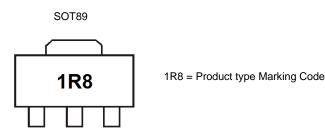
 See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.

3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product\_compliance\_definitions/.

5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## **Marking Information**





**ZXTN4004Z** 

### Absolute Maximum Ratings (@T<sub>A</sub> = +25°C unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	200	V
Collector-Emitter Voltage	V <sub>CEO</sub>	150	V
Emitter-Base Voltage	V <sub>EBO</sub>	7	V
Continuous Collector Current	Ic	1	A
Peak Pulse Current	ICM	3	A
Base Current	IB	500	mA

### Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
	(Note 6)		1		
Power Dissipation	(Note 7)	PD	1.5	W	
	(Note 8)		2.0		
	(Note 6)		125		
Thermal Resistance, Junction to Ambient Air	(Note 7)	R <sub>0JA</sub>	83	°C/W	
	(Note 8)		60		
Thermal Resistance, Junction to Lead	(Note 9)	R <sub>θJL</sub>	13	°C/W	
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C	

### ESD Ratings (Note 10)

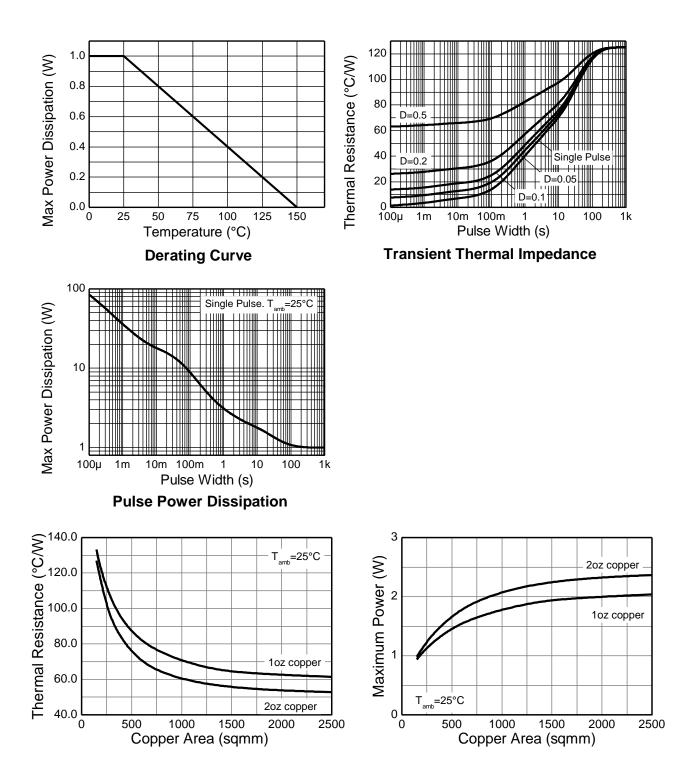
Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	ЗA
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

Notes: 6. For a device mounted with the exposed collector pad on 15mm x 15mm 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured or a device incurred with the exposed collector par on 15mm 102 ct under still air conditions whilst operating in a steady-state.
7. Same as Note 6, except the device is mounted on 25mm x 25mm 1oz copper.
8. Same as Note 6, except the device is mounted on 50mm x 50mm 1oz copper.

Thermal resistance from junction to solder-point (on the exposed collector pad).
 Refer to JEDEC specification JESD22-A114 and JESD22-A115.



## Thermal Characteristics and Derating Information





Electrical Characteristics	$(@T_A = +25^{\circ}C \text{ unless otherwise specified.})$	

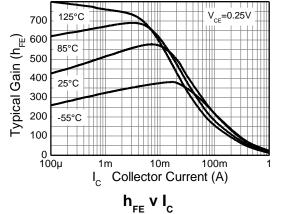
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Emitter Breakdown Voltage (Note 11)	BV <sub>CEO</sub>	150	175	-	V	$I_{\rm C} = 10 {\rm mA}$
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	200	310	-	V	I <sub>C</sub> = 100μA
Collector Cut-Off Current	I <sub>CBO</sub>	-	<1	50	nA	V <sub>CB</sub> = 150V
Collector Cut-Off Current	I <sub>CES</sub>	-	<1	50	nA	V <sub>CE</sub> = 150V
Emitter Cut-Off Current	I <sub>EBO</sub>	-	<1	50	nA	$V_{EB} = 7V$
Static Forward Current Transfer Ratio (Note 11)	h <sub>FE</sub>	200 60 100		- -	-	$I_{C} = 30 \text{mA}, V_{CE} = 5 \text{V}$ $I_{C} = 85 \text{mA}, V_{CE} = 0.20 \text{V}$ $I_{C} = 150 \text{mA}, V_{CE} = 0.25 \text{V}$
Base-Emitter Turn-On Voltage (Note 11)	V <sub>BE(on)</sub>	-	0.71	0.95	V	I <sub>C</sub> = 150mA, V <sub>CE</sub> = 0.25V
Collector-Emitter Saturation Voltage (Note 11)	V <sub>CE(sat)</sub>	-	-	0.25	V	$I_{\rm C} = 100 {\rm mA}, I_{\rm B} = 5 {\rm mA}$
Delay Time	t <sub>(d)</sub>	-	512	-	ns	
Rise Time	t <sub>(r)</sub>	-	426	-	ns	V <sub>CC</sub> = 120V, I <sub>C</sub> = 150mA,
Storage Time	t <sub>(s)</sub>	-	3413	-	ns	-I <sub>B2</sub> = 1.5mA, V <sub>CE</sub> ( <sub>ON</sub> ) = 0.25V
Fall Time	t <sub>(f)</sub>	-	321	-	ns	
Storage Time	t <sub>(s)</sub>	-	65	-	ns	V <sub>CC</sub> = 120V, I <sub>C</sub> = 150mA,
Fall Time	t <sub>(f)</sub>	-	294	-	ns	-I <sub>B2</sub> = 1.5mA, V <sub>CE</sub> ( <sub>ON</sub> ) = 4V

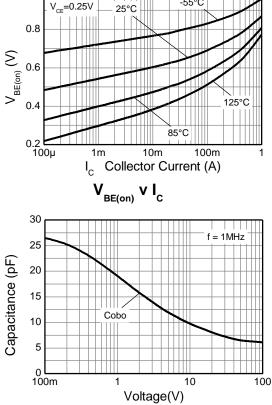
1.0

Note:

11. Measured under pulsed conditions. Pulse width  $\leq$  300µs. Duty cycle  $\leq$  2%.

Electrical Characteristics (@T<sub>A</sub> = +25°C unless otherwise specified.)



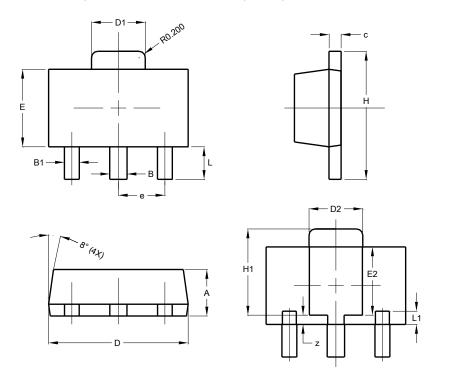


-55°C



# Package Outline Dimensions

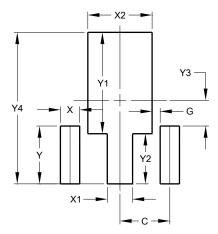
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SOT89						
Dim	Min	Max	Тур			
Α	1.40	1.60	1.50			
В	0.50	0.62	0.56			
B1	0.42	0.54	0.48			
C	0.35	0.43	0.38			
D	4.40	4.60	4.50			
D1	1.62	1.83	1.733			
D2	1.61	1.81	1.71			
Е	2.40	2.60	2.50			
E2	2.05	2.35	2.20			
е	-	-	1.50			
Н	3.95	4.25	4.10			
H1	2.63	2.93	2.78			
L	0.90	1.20	1.05			
L1	0.327	0.527	0.427			
z	0.20	0.40	0.30			
All Dimensions in mm						

### **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value				
	(in mm)				
С	1.500				
G	0.244				
Х	0.580				
X1	0.760				
X2	1.933				
Y	1.730				
Y1	3.030				
Y2	1.500				
Y3	0.770				
Y4	4.530				

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device terminals and PCB tracking.



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