

100V INPUT, 8.2V 30mA REGULATOR TRANSISTOR

Description

The ZXTR2008Z monolithically integrates a transistor, Zener diode and resistor to function as a high voltage linear regulator. The device regulates with an 8.2V nominal output at 15mA. It is designed for use in high voltage applications where standard linear regulators cannot be used. This function is fully integrated into a SOT89 package, minimizing PCB area and reducing number of components when compared with a multi-chip discrete solution.

Features

- Series Linear Regulator Using Emitter-Follower Stage
- Input Voltage = 12V to 100V
- Output Voltage = 8.2V ± 10%
- Fully integrated into a SOT89 package
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Applications

Supply voltage regulation in:

- Networking
- Telecom
- Power Over Ethernet (PoE)

Mechanical Data

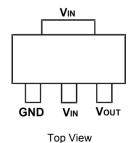
- Case: SOT89
- Case Material: Molded Plastic. "Green" Molding Compound UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208
- Weight: 0.052 grams (approximate)



GND

Internal Device
Schematic

ZXTR2008



Pin-Out

Pin Name	Pin Function
Vin	Input Supply
GND	Power Ground
Vout	Voltage Output

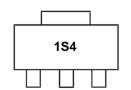
Ordering Information (Note 4)

-						
	Product	Package	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
	ZXTR2008Z-7	SOT89	1S4	7	12	1,000
	ZXTR2008Z-13	SOT89	1S4	13	12	2,500

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.
- 3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html

Marking Information



1S4 = Product Type Marking Code





Absolute Maximum Ratings (Voltage relative to GND, @TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Input Voltage	V _{IN}	-0.3 to 100	V
Continuous Input & Output Current	I _{IN,} I _{OUT}	400	mA
Peak Pulsed Input & Output Current	I _{IM} , I _{OM}	2	Α
Maximum Voltage applied to V _{OUT}	V _{OUT(max)}	14.5	V

Maximum Current at V_{IN} = 48V (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Continuous Output Current (Note 7)		I _{OUT}	42	mA	
Duland Output Current	(Note 8)		800	m A	
Pulsed Output Current	(Note 9)	Іом	160	mA mA	

Thermal Characteristics

Characteristic	Symbol	Value	Unit		
Power Dissipation	(Note 5)	В	1.7	W	
Power Dissipation	(Note 6)	P _D	0.89	vv	
Thermal Resistance, Junction to Ambient	(Note 5)	Б	59		
	(Note 6)	R _{θJA}	112	0000	
Thermal Resistance, Junction to Lead	(Note 10)	R _{θJL}	20	°C/W	
Thermal Resistance, Junction to Case (Note 10)		R _{0JC}	15.7		
Recommended Operating Junction Temperature	TJ	-40 to +125	°C		
Maximum Operating Junction and Storage Tem	T _J , T _{STG}	-65 to +150	°C		

ESD Ratings (Note 11)

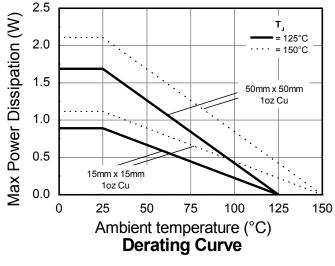
Characteristics	Symbols	Value	Unit	JEDEC Class
Electrostatic Discharge – Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge – Machine Model	ESD MM	400	V	С

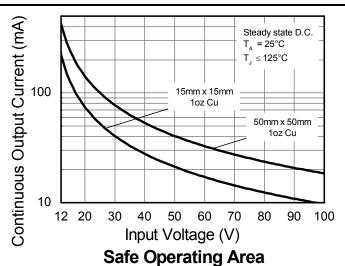
Notes:

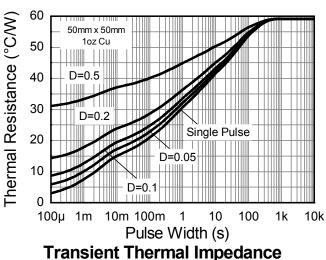
- 5. For a device mounted with the exposed V_{IN} pad on 50mm x 50mm 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in steady-state.
- 6. Same as note 5, except mounted on 15mm x 15mm 1oz copper.
- 7. Same as note 5, whilst operating at V_{IN} = 48V. Refer to Safe Operating Area for other Input Voltages.
- 8. Same as note 5, except measured with a single pulse width = 100 μ s and V_{IN} = 48V.
- 9. Same as note 5, except measured with a single pulse width = 10ms and V_{IN} = 48V.
- 10. $R_{\theta JL}$ = Thermal resistance from junction to solder-point (on the exposed V_{IN} pad). $R_{\theta JC}$ = Thermal resistance from junction to the top of case.
- 11. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

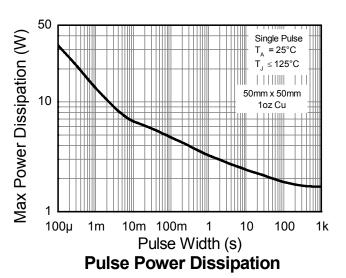


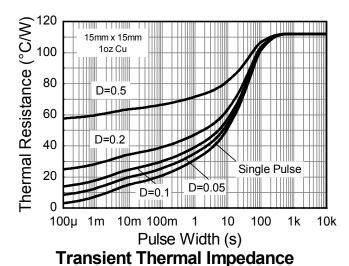
Thermal Characteristics and Derating Information

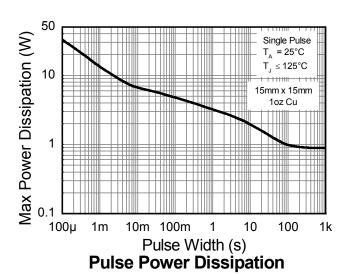
















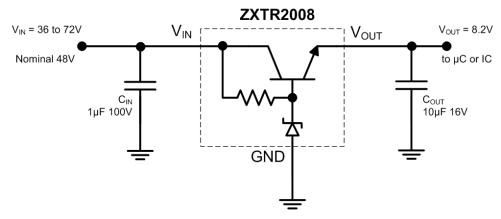
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Output Voltage (Note 12)	Vout	7.38	8.2	9.02	V	V _{IN} = 48V, I _{OUT} = 15mA
Line Regulation (Notes 12 & 13)	ΔV_{OUT}	_	10	300	mV	V _{IN} = 12 to 100V, I _{OUT} = 15mA
Temperature Coefficient	ΔV _{OUT} /ΔΤ	_	10	_	mV/°C	$T_J = -40$ °C to +125°C $V_{IN} = 48V$, $I_{OUT} = 15mA$
Load Regulation (Notes 12 & 14)	ΔVουτ	_	-180 -250	-350 -500	mV	I _{OUT} = 0.1 to 30mA, V _{IN} = 48V I _{OUT} = 0.1 to 100mA, V _{IN} = 48V
Minimum Value of Input Voltage Required to Maintain Line Regulation	V _{IN(MIN)}	12	_		V	_
Quiescent Current	IQ	_	275 650	500 900	μA	V _{IN} = 48V, I _{OUT} = 10μA V _{IN} = 100V, I _{OUT} = 10μA
Power Supply Rejection Ratio	$\Delta V_{in} I \Delta V_{out}$	_	38	_	dB	C _{OUT} = 100nF, I _{OUT} = 15mA, V _{OUT} = 8.2V, V _{IN} =12 to 100V,f=100Hz

Notes:

- 12. Measured under pulsed conditions. Pulse width \leq 300 μ s. Duty cycle \leq 2%.
- 13. Line regulation $\Delta V_{OUT} = V_{OUT}(@V_{IN} = 100V) V_{OUT}(@V_{IN} = 12V)$
- 14. Load regulation $\Delta V_{OUT} = V_{OUT}(@ I_{OUT} = 30 \text{mA}) V_{OUT}(@ I_{OUT} = 0.1 \text{mA})$
 - $\Delta V_{OUT} = V_{OUT} (@ I_{OUT} = 100 \text{mA}) V_{OUT} (@ I_{OUT} = 0.1 \text{mA})$

Typical Application Circuit



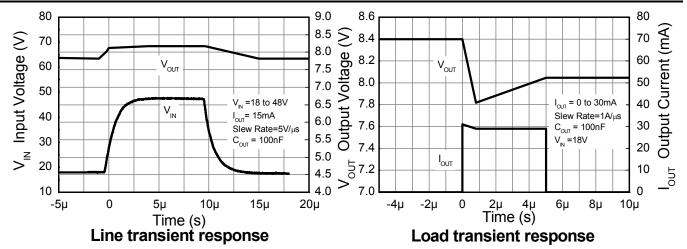
Example of an 8.2V regulated supply from a nominal 48V for powering a Controller IC.

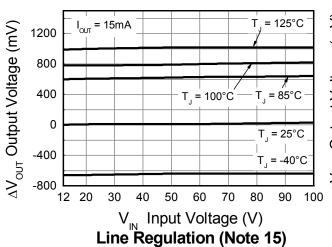
Pin Functions

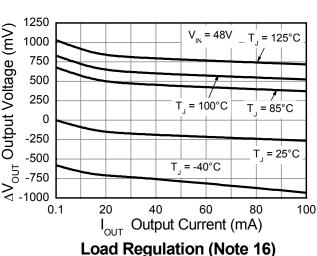
Pin Name Pin Function		Notes
V _{IN} Input Supply To maintain output regulation the input voltage can vary from 12V to 100V w recommended to connect a 1μF capacitor to GND.		To maintain output regulation the input voltage can vary from 12V to 100V with respect to the GND pin. It is recommended to connect a 1µF capacitor to GND.
GND Power Ground This pin sho		This pin should be tied to the system ground.
Vout Voltage Output be drawn from Vout to maintain regulation. The pin can be pulled high to		Outputs a regulated 8.2V. It is recommended to connect a 10µF capacitor to GND. Minimum of 10µA must be drawn from V _{OUT} to maintain regulation. The pin can be pulled high to a maximum of 14.5V with respect to ground.

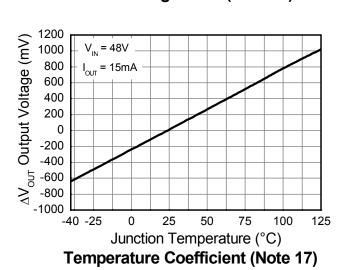


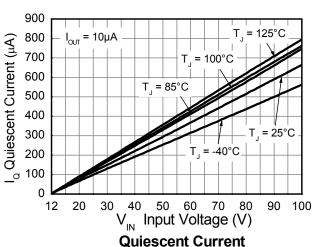












Notes: 15. Line regulation $\Delta V_{OUT} = V_{OUT} - V_{OUT}(@V_{IN} = 12V, I_{OUT} = 15mA, T_J = +25^{\circ}C)$ 16. Load regulation $\Delta V_{OUT} = V_{OUT} - V_{OUT}(@V_{IN} = 48V, I_{OUT} = 0.1mA, T_J = +25^{\circ}C)$

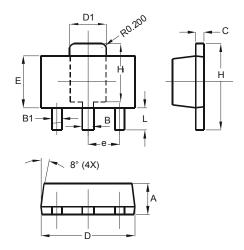
17. Temperature Coefficient $\Delta V_{OUT} = V_{OUT} - V_{O$





Package Outline Dimensions

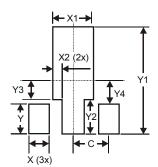
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT89				
Dim	Min	Max		
Α	1.40	1.60		
В	0.44	0.62		
B1	0.35	0.54		
C	0.35	0.44		
D	4.40	4.60		
D1	1.62	1.83		
Е	2.29	2.60		
e	1.50	Тур		
Н	3.94	4.25		
H1	2.63	2.93		
L	0.89	1.20		
All Dimensions in mm				

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Х	0.900
X1	1.733
X2	0.416
Υ	1.300
Y1	4.600
Y2	1.475
Y3	0.950
Y4	1.125
С	1 500





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