

Description

The ZXTR2012FF monolithically integrates a transistor, Zener diode and resistor to function as a high-voltage linear regulator. The device regulates with a 12V nominal output at 15mA. It is designed for use in high-voltage applications where standard linear regulators cannot be used. This function is fully integrated into an SOT23F package, minimizing PCB area and reducing number of components when compared with a multi-chip discrete solution.

Applications

Supply voltage regulation in:

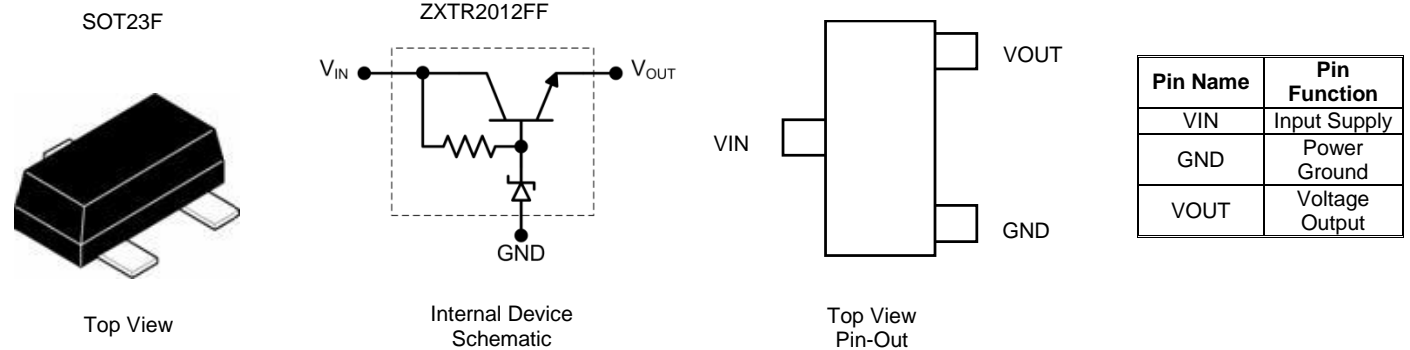
- Networking
- Telecommunications
- Power over Ethernet (PoE)

Features

- Series Linear Regulator Using Emitter-Follower Stage
- Input Voltage = 15V to 100V
- Output Voltage = 12V \pm 10%
- Fully Integrated into a SOT23F Package
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

Mechanical Data

- Case: SOT23F
- Case material: Molded Plastic. "Green" Molding Compound. UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Plated Leads. Solderable per MIL-STD-202, Method 208
- Weight: 0.012 grams (Approximate)

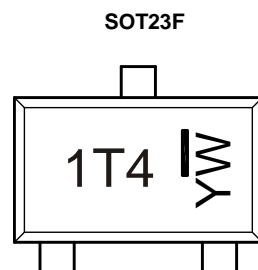


Ordering Information (Note 4)

| Product | Package | Marking | Reel Size (inches) | Tape Width (mm) | Quantity per Reel |
|--------------|---------|---------|--------------------|-----------------|-------------------|
| ZXTR2012FF-7 | SOT23F | 1T4 | 7 | 8 | 3,000 |

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.
 3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



- 1T4 = Product Type Marking Code
 YW = Date Code Marking
 Y = Year : 0~9
 W = Week : A~Z : 1~26
 a~z : 27~52
 z represents 52 & 53 week

Absolute Maximum Ratings (Voltage relative to GND, @ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit |
|--------------------------------------|-------------------|-------------|------|
| Input Supply Voltage | V_{IN} | -0.3 to 100 | V |
| Continuous Input & Output Current | I_{IN}, I_{OUT} | 550 | mA |
| Peak Pulsed Input & Output Current | I_{IM}, I_{OM} | 2 | A |
| Maximum Voltage Applied to V_{OUT} | $V_{OUT(MAX)}$ | 18 | V |

Maximum Current at $V_{IN} = 48\text{V}$ (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit |
|---------------------------|-----------|-------|------|
| Continuous Output Current | I_{OUT} | 36 | mA |
| Pulsed Output Current | I_{OM} | 880 | mA |
| | | 180 | |

Thermal Characteristics

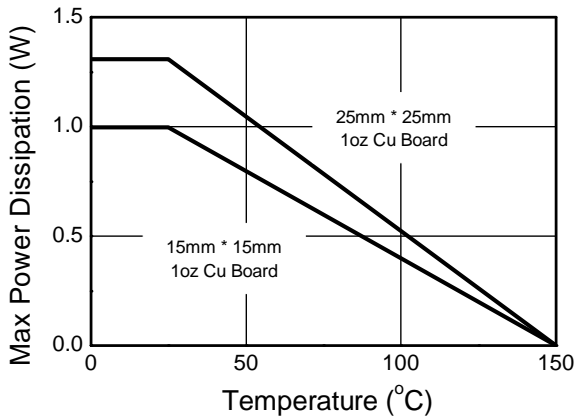
| Characteristic | Symbol | Value | Unit |
|--|-----------------|-------------|--------------------|
| Power Dissipation | P_D | 1.3 | W |
| | | 1 | |
| Thermal Resistance, Junction to Ambient | $R_{\theta JA}$ | 95 | $^\circ\text{C/W}$ |
| | | 126 | |
| Thermal Resistance, Junction to Lead | $R_{\theta JL}$ | 59 | |
| Thermal Resistance, Junction to Case | $R_{\theta JC}$ | 38 | |
| Maximum Operating Junction and Storage Temperature Range | T_J, T_{STG} | -65 to +150 | $^\circ\text{C}$ |

ESD Ratings (Note 11)

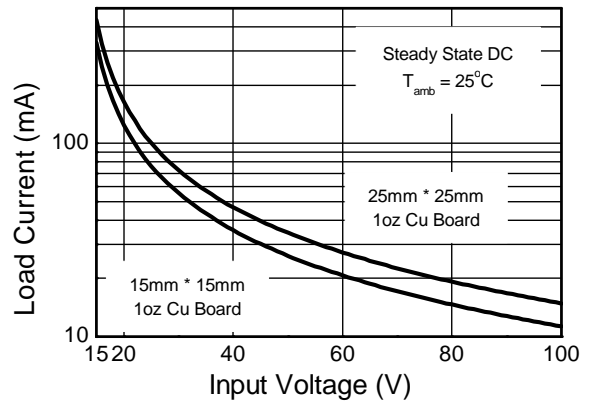
| Characteristics | Symbols | Value | Unit | JEDEC Class |
|--|---------|-------|------|-------------|
| Electrostatic Discharge – Human Body Model | ESD HBM | 4,000 | V | 3A |
| Electrostatic Discharge – Machine Model | ESD MM | 400 | V | C |

- Notes:
- For a device mounted with the exposed V_{IN} pad on 25mm x 25mm 1oz copper that is on a single-sided 1.6mm FR-4 PCB; device is measured under still air conditions whilst operating in steady-state.
 - Same as note 5, except mounted on 15mm x 15mm 1oz copper.
 - Same as note 5, whilst operating at $V_{IN} = 48\text{V}$. Refer to Safe Operating Area for other Input Voltages.
 - Same as note 5, except measured with a single pulse width = 100 μs and $V_{IN} = 48\text{V}$.
 - Same as note 5, except measured with a single pulse width = 10ms and $V_{IN} = 48\text{V}$.
 - $R_{\theta JL}$ = Thermal resistance from junction to solder-point (on the exposed V_{IN} pad).
 - $R_{\theta JC}$ = Thermal resistance from junction to the top of case.
 - Refer to JEDEC specification JESD22-A114 and JESD22-A115.

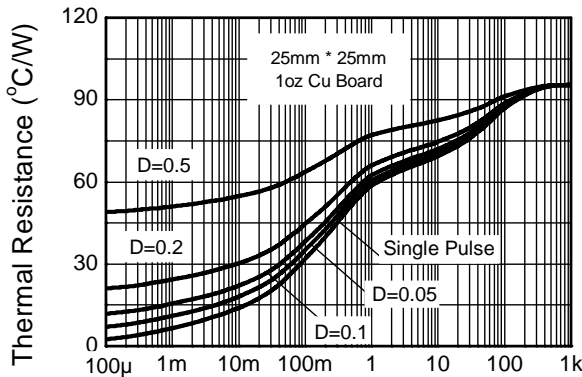
Thermal Characteristics and Derating Information



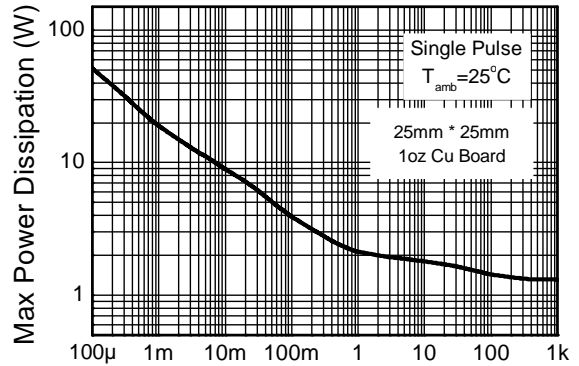
Derating Curve



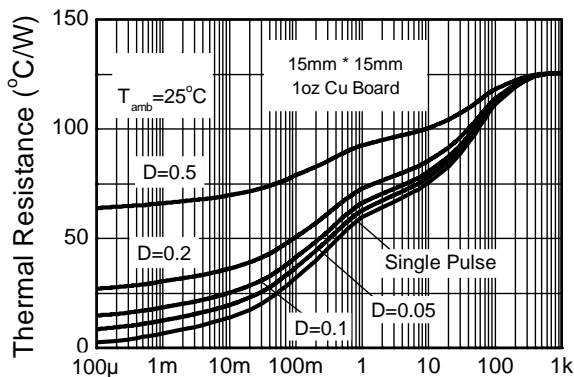
Safe Operating Area



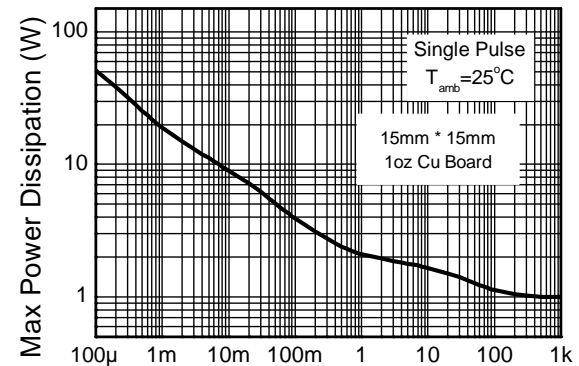
Transient Thermal Impedance



Pulse Power Dissipation



Transient Thermal Impedance

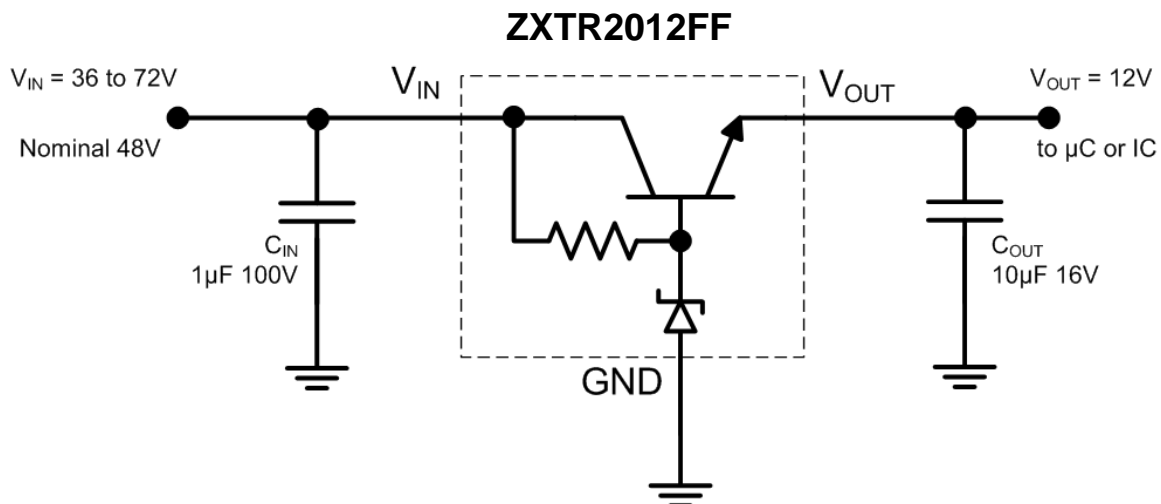


Pulse Power Dissipation

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
|---|-------------------------------------|------|--------------|--------------|-------|---|
| Output Voltage (Note 12) | V _{OUT} | 10.8 | 12 | 13.2 | V | V _{IN} = 48V, I _{OUT} = 15mA |
| Line Regulation (Notes 12 & 13) | ΔV _{OUT} | — | 240 | 750 | mV | V _{IN} = 15V to 72V, I _{OUT} = 15mA |
| Temperature Coefficient | ΔV _{OUT} /ΔT | — | 8.0 | — | mV/°C | T _J = -40°C to +125°C V _{IN} = 48V, I _{OUT} = 15mA |
| Load Regulation (Notes 12 & 14) | ΔV _{OUT} | — | -450 -600 | -600 -750 | mV | I _{OUT} = 0.1mA to 30mA, V _{IN} = 48V I _{OUT} = 0.1mA to 100mA, V _{IN} = 48V |
| Minimum Value of Input Voltage Required to Maintain Line Regulation | V _{IN(MIN)} | 15 | — | — | V | — |
| Quiescent Current | I _Q | — | 240 590 | 400 900 | μA | V _{IN} = 48V, I _{OUT} = 10μA V _{IN} = 100V, I _{OUT} = 10μA |
| Power Supply Rejection Ratio | ΔV _{IN} /ΔV _{OUT} | — | 45 | — | dB | C _{OUT} = 100nF, I _{OUT} = 15mA, V _{OUT} = 12V, V _{IN} = 15V to 100V, f = 100Hz |

Notes:
 12. Measured under pulsed conditions. Pulse width ≤ 300μs. Duty cycle ≤ 2%.
 13. Line regulation ΔV_{OUT} = V_{OUT}(@ V_{IN} = 72V) - V_{OUT}(@ V_{IN} = 15V).
 14. Load regulation ΔV_{OUT} = V_{OUT}(@ I_{OUT} = 30mA) - V_{OUT}(@ I_{OUT} = 0.1mA).
 ΔV_{OUT} = V_{OUT}(@ I_{OUT} = 100mA) - V_{OUT}(@ I_{OUT} = 0.1mA).

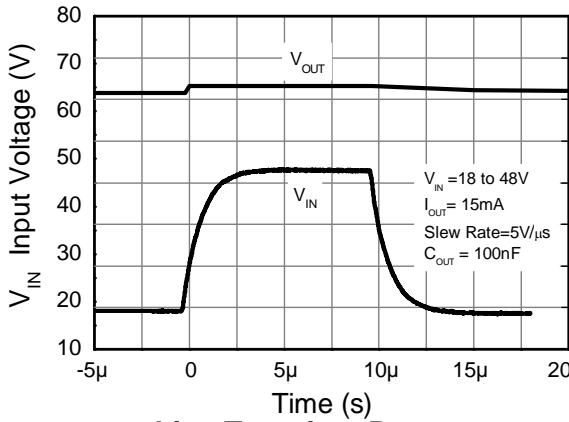
Typical Application Circuit


Example of an 12V regulated supply from a nominal 48V for powering a Controller IC.

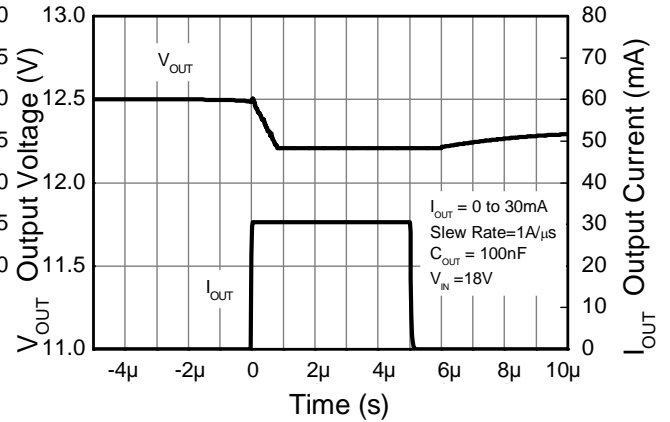
Pin Functions

| Pin Name | Pin Function | Notes |
|------------------|----------------|---|
| V _{IN} | Input Supply | To maintain output regulation the input voltage can vary from 15V to 100V with respect to the GND pin. It is recommended to connect a 1μF capacitor to GND. |
| GND | Power Ground | This pin should be tied to the system ground. |
| V _{OUT} | Voltage Output | Outputs a regulated 12V. It is recommended to connect a 10μF capacitor to GND. Minimum of 10μA must be drawn from V _{OUT} to maintain regulation. The pin can be pulled high to a maximum of 18V with respect to ground. |

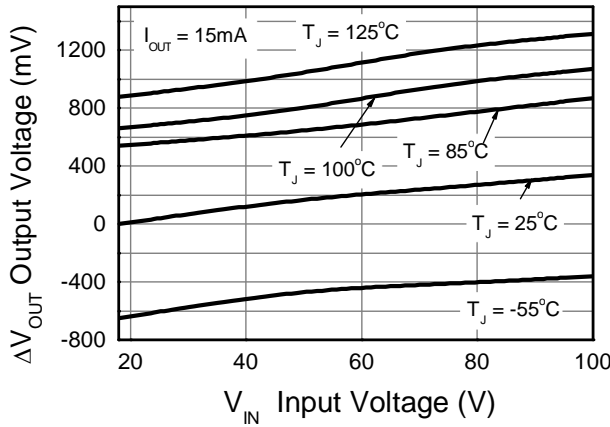
Typical Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)



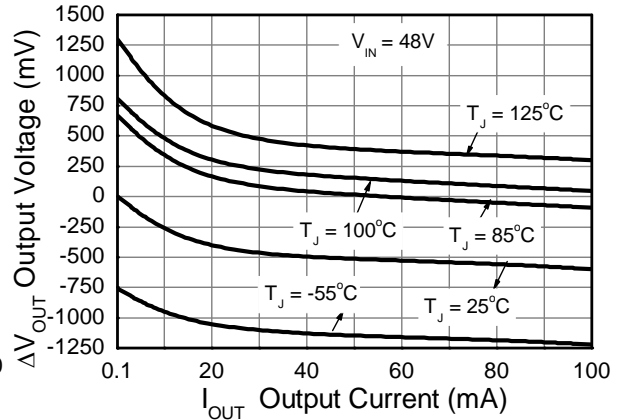
Line Transient Response



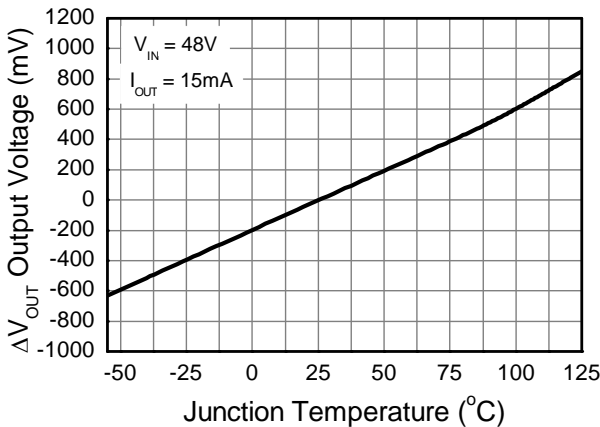
Load Transient Response



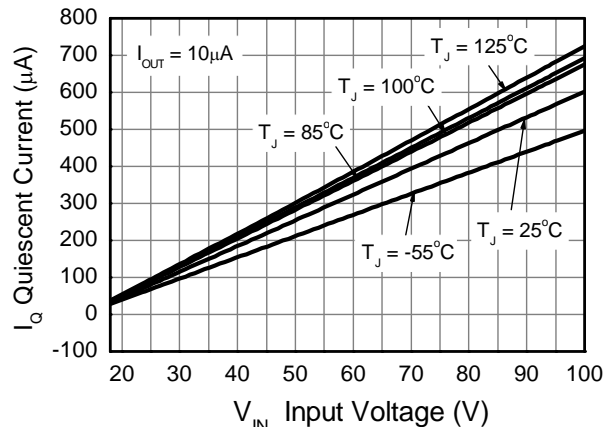
Line Regulation (Note 15)



Load Regulation (Note 16)



Temperature Coefficient (Note 17)



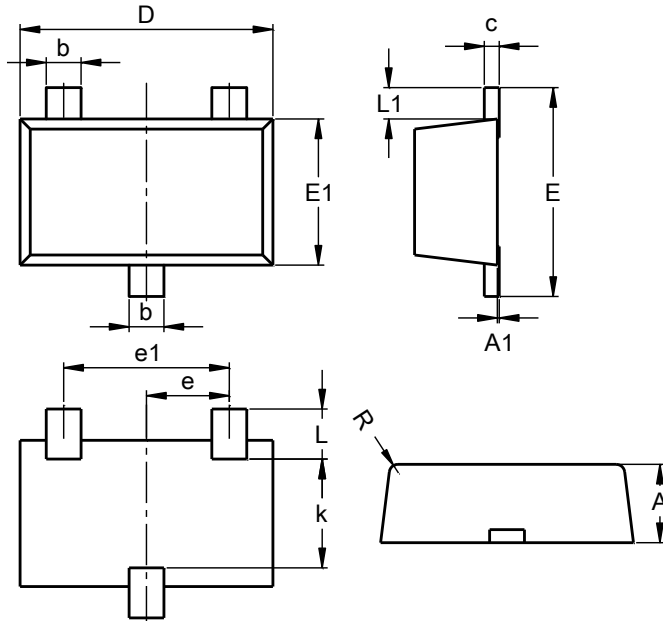
Quiescent Current

Notes: 15. Line regulation $\Delta V_{OUT} = V_{OUT} - V_{OUT}$ (@ $V_{IN} = 15\text{V}$, $I_{OUT} = 15\text{mA}$, $T_J = +25^\circ\text{C}$).
16. Load regulation $\Delta V_{OUT} = V_{OUT} - V_{OUT}$ (@ $V_{IN} = 48\text{V}$, $I_{OUT} = 0.1\text{mA}$, $T_J = +25^\circ\text{C}$).
17. Temperature Coefficient $\Delta V_{OUT} = V_{OUT} - V_{OUT}$ (@ $V_{IN} = 48\text{V}$, $I_{OUT} = 15\text{mA}$, $T_J = +25^\circ\text{C}$).

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT23F

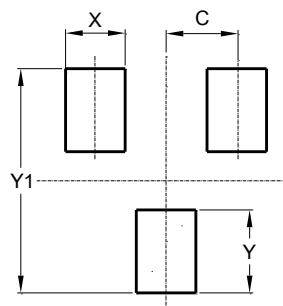


| SOT23F | | | |
|-----------------------------|----------|------|------|
| Dim | Min | Max | Typ |
| A | 0.80 | 1.00 | 0.90 |
| A1 | 0.00 | 0.10 | 0.01 |
| b | 0.35 | 0.50 | 0.44 |
| c | 0.10 | 0.20 | 0.16 |
| D | 2.80 | 3.00 | 2.90 |
| e | 0.95 REF | | |
| e1 | 1.90 REF | | |
| E | 2.30 | 2.50 | 2.40 |
| E1 | 1.50 | 1.70 | 1.65 |
| k | 1.20 | - | - |
| L | 0.30 | 0.65 | 0.50 |
| L1 | 0.30 | 0.50 | 0.40 |
| R | 0.05 | 0.15 | - |
| All Dimensions in mm | | | |

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT23F



| Dimensions | Value (in mm) |
|------------|---------------|
| C | 0.95 |
| X | 0.80 |
| Y | 1.110 |
| Y1 | 3.000 |

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