⬙TDK

Asceta™ iQL Series DC/DC Power Modules
48V Input, 3.3V / 60A /198W Output
Quarter Brick

The Asceta™ power module series provides exceptionally high true useable power in an industry standard quarter brick format. Its 91% full load efficiency (92% at 70% load) and superior thermal performance make the Asceta series of DC-DC converters is ideally suited for tight space, power-hungry, and thermal challenging distributed power architectures in demanding thermal environments. This rugged building block is designed to serve as the core of your high reliability system. A wide output voltage trim range, -20 to +10%, and remote sensing are standard feature enhancing versatility.

Standard Features:

- **RoHS Compliant**
- Standard Quarter Brick
- Nominal Size: 2.28" × 1.45" × 0.5"
  (57.9mm × 36.8mm × 12.7mm)
- Up to 60A of output current
- Output power – up to 198W
- Power density: 120W / in^3
- Efficiency – up to 93%
- Full load efficiency at 48V input: 91%
- Efficiency at 70% load and 48V input: 92%
- Single board design with high usable power
  47A at 65°C, 200LFM (no base-plate)
  51A at 65°C, 200LFM (with base-plate)
- Industry standard output voltage trim
- Remote sense
- Basic insulation – 1500Vdc
- Negative remote on/off logic
- Monotonic start-up into a pre-biased output
- Constant switching frequency
- Auto-recovery protection for input UVP
- Auto-recovery over-current protection
- Auto-recovery over-temperature protection
- Latched over-voltage protection

- UL 60950 (US and Canada), VDE 0805, CB scheme (IEC950)
- CE Mark (EN60950)
- EMI: CISPR 22 A or B with external filter
- Multiple patents
- ISO Certified manufacturing facilities
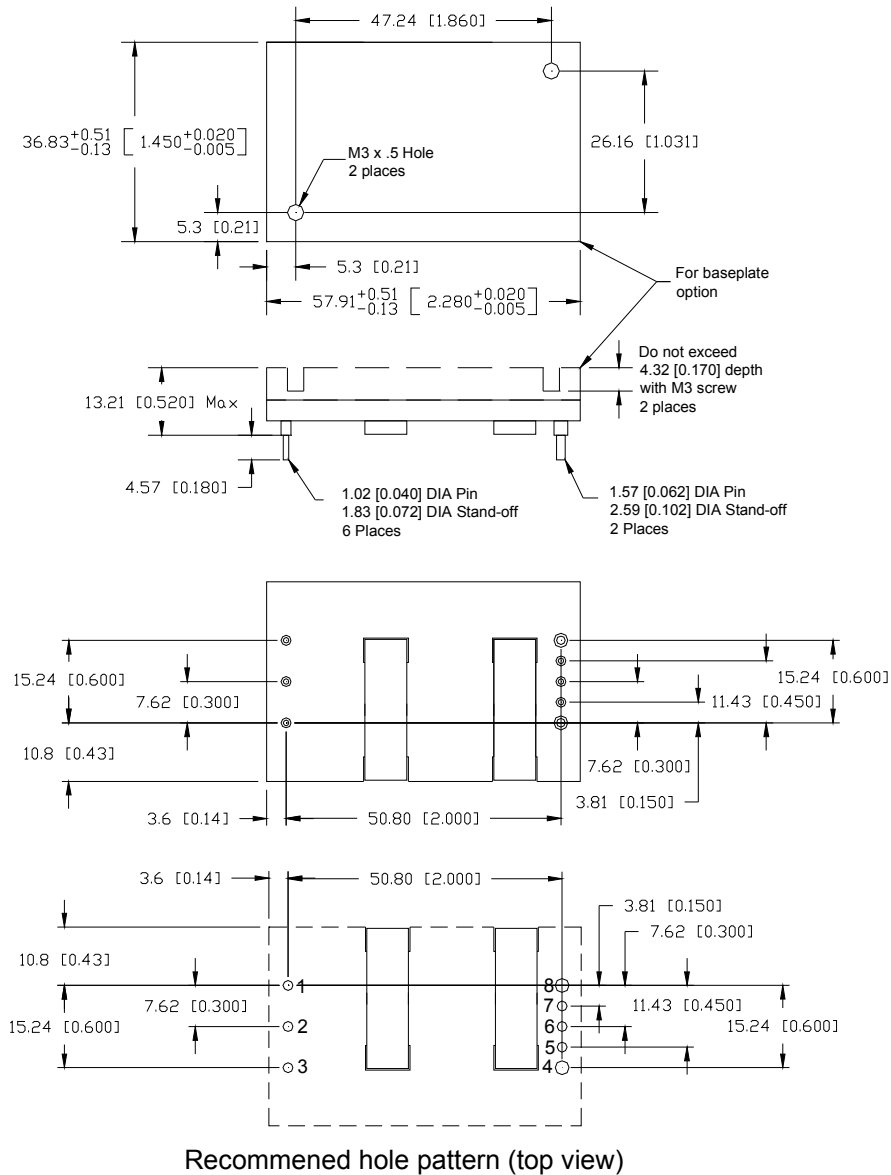
Optional Features:

- Positive remote on/off logic
- Latched over-current protection
- Latched over-temperature protection
- Auto-recovery OVP
- 3.68 mm (0.145") Thru-hole pins
- Base-plate option

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Mechanical Specification:

Dimensions are in mm [in]. Unless otherwise specified tolerances are: x.x ± 0.5 [0.02], x.xx and x.xxx ± 0.25 [0.010].

47.24 [1.860]

36.83 $^{+0.51}_{-0.13}$ [1.450 $^{+0.020}_{-0.005}$]

26.16 [1.031]

M3 x .5 Hole
2 places

5.3 [0.21]

5.3 [0.21]

57.91 $^{+0.51}_{-0.13}$ [2.280 $^{+0.020}_{-0.005}$]

For baseplate
option

Do not exceed
4.32 [0.170] depth
with M3 screw
2 places

13.21 [0.520] Max

4.57 [0.180]

1.02 [0.040] DIA Pin
1.83 [0.072] DIA Stand-off
6 Places

1.57 [0.062] DIA Pin
2.59 [0.102] DIA Stand-off
2 Places

15.24 [0.600]

7.62 [0.300]

10.8 [0.43]

3.6 [0.14]

50.80 [2.000]

15.24 [0.600]

11.43 [0.450]

7.62 [0.300]

3.81 [0.150]

3.6 [0.14]

50.80 [2.000]

3.81 [0.150]

7.62 [0.300]

10.8 [0.43]

7.62 [0.300]

15.24 [0.600]

11.43 [0.450]

15.24 [0.600]

Recommened hole pattern (top view)

Pin Assignment:

PIN	FUNCTION	PIN	FUNCTION
1	Vin(+)	4	Vo(-)
2	On/Off	5	Sense(-)
3	Vin(-)	6	Trim
		7	Sense(+)
		8	Vo(+)

Pin base material is copper with gold plating. The maximum module weight with base plate is 70g (55g open-frame).

Absolute Maximum Ratings:

Stress in excess of Absolute Maximum Ratings may cause permanent damage to the device.

Characteristic		Min	Max	Unit	Notes & Conditions
Continuous Input Voltage		-0.5	80	Vdc	
Transient Input Voltage		---	100	Vdc	100mS max.

Isolation Voltage
 	
    Input to Output
    Input to Base-plate
    Output to Base-plate
		
		--- 	
		--- 	
		--- 	
			
			1500
			1500
			500
				
				Vdc
				Vdc
				Vdc
					
					Basic Insulation
					Basic Insulation
					Operation Insulation

Storage Temperature		-55	125	˚C	
Operating Temperature Range (Tc)		-40	115	˚C	Measured at the location specified in the thermal measurement figure. Maximum temperature varies with model number, output current, and module orientation – see curve in thermal performance section of the data sheet.

Input Characteristics:

Unless otherwise specified, specifications apply over all Rated Input Voltage, Resistive Load, and Temperature conditions.

Characteristic		Min	Typ	Max	Unit	Notes & Conditions
Operating Input Voltage		36	48	75	Vdc	
Maximum Input Current	60A	---	6	7	A	Vin = 0 to Vin,max, Io=Io,max, Vo=Vo,nom
Turn-on Voltage		---	34.2	---	Vdc	
Turn-off Voltage		---	32.4	---	Vdc	
Hysteresis		---	1.8	---	Vdc	
Startup Delay Time from application of input voltage		---	18	---	mS	Vo = 0 to 0.1*Vo,nom; on/off =on, Io=Io,max, Tc=25˚C
Startup Delay Time from on/off		---	2	---	mS	Vo = 0 to 0.1*Vo,nom; Vin = Vi,nom, Io=Io,max,Tc=25˚C
Output Voltage Rise Time		---	24	---	mS	Io=Io,max,Tc=25˚C, Vo=0.1 to 0.9*Vo,nom
Inrush Transient		---	---	0.1	A^2s	Exclude external input capacitors
Input Reflected Ripple		---	20	---	mApp	See input/output ripple and noise measurements figure; BW = 20 MHz
Input Ripple Rejection		---	58	---	dB	@120Hz

* Engineering Estimate

Caution: The power modules are not internally fused. An external input line normal blow fuse with a maximum value of 15A is required; see the Safety Considerations section of the data sheet.

Electrical Data:

iQL48060A033V-000 through -0x9: 3.3V, 60A, 198W Output

| Characteristic	Min	Typ	Max	Unit	Notes & Conditions
Output Voltage Initial Setpoint	3.23	3.3	3.37	Vdc	Vin=Vin,nom; Io=Io,max; Tc = 25˚C
Output Voltage Tolerance	3.20	3.3	3.40	Vdc	Over all rated input voltage, load, and temperature conditions to end of life
Efficiency at 100% Load	---	91	---	%	Vin=Vin,nom; Io=Io,max; Tc = 25˚C
Efficiency at 70% Load (42A)	---	92	---	%	Vin=Vin,nom; Io=70% of Io,max; Tc = 25˚C
Line Regulation	---	2	6.6	mV	Vin=Vin,min to Vin,max, Io and Tc fixed
Load Regulation	---	3	15	mV	Io=Io,min to Io,max, Vin and Tc fixed
Temperature Regulation	---	5	50	mV	Tc=Tc,min to Tc,max, Vin and Io fixed
Output Current	0	---	60	A	At Io < 20% of Io,max, the load transient performance may degrade slightly
Output Current Limiting Threshold	---	70	---	A	Vo = 0.9*Vo,nom, Tc<Tc,max
Short Circuit Current	---	2	---	A	Vo = 0.25V, Tc = 25C (hiccup mode)
Output Ripple and Noise Voltage	---	35	100*	mVpp	Vin=48V, Io ≥ Io,min, Tc=25˚C. Measured across one 0.1uF, one 1.0 uF, and one 47uF ceramic capacitors, and one 440uF electrolytic capacitor located 2 inches away – see input/output ripple measurement figure; BW = 20MHz
	---	2	---	mVrms	
Output Voltage Adjustment Range	80	---	110	%Vo,nom	
Output Voltage Sense Range	---	---	---	%Vo,nom	
Dynamic Response: Recovery Time to 10% of Peak Deviation	---	200	---	μS	di/dt = 0.1A/uS, Vin=Vin,nom; load step from 50% to 75% of Io,max, Tc=25˚C with at least one 1.0 uF, one 47uF ceramic capacitors, and one 440uF electrolytic capacitor across the output terminals
Transient Voltage	---	80	---	mV	
Output Voltage Overshoot during startup	0	0	---	mV	Vin=Vin,nom; Io=Io,max,Tc=25˚C
Switching Frequency	---	130	---	kHz	Fixed
Output Over Voltage Protection	3.8*	4.1	4.4*	V	
External Load Capacitance	488	---	40,000 †	uF	Cext,min required for the 100% load dump. Minimum ESR > 2.5 mΩ
Isolation Capacitance	---	450	---	pF	
Isolation Resistance	15	---	---	MΩ	
Vref		1.225		V	Required for trim calculation

* Engineering Estimate

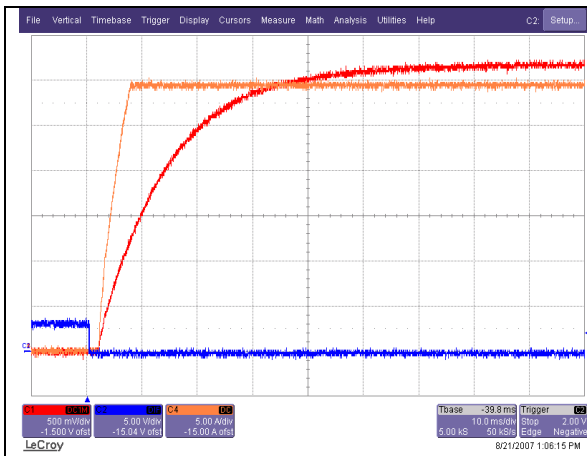† Contact TDK Innoveta for applications that require additional capacitance or very low ESR

Electrical Characteristics: **iQL48060A033V-000 through -0x9: 3.3V, 60A, 198W Output**
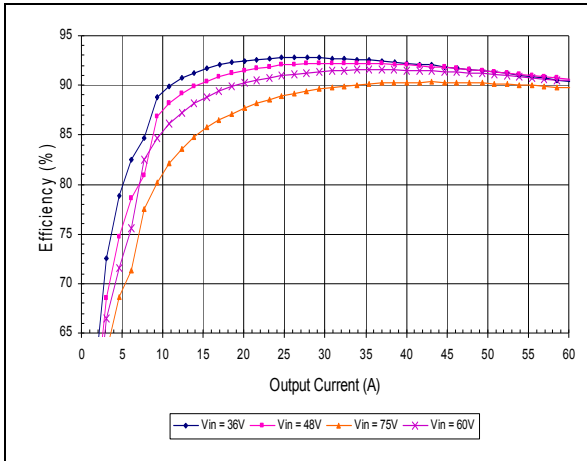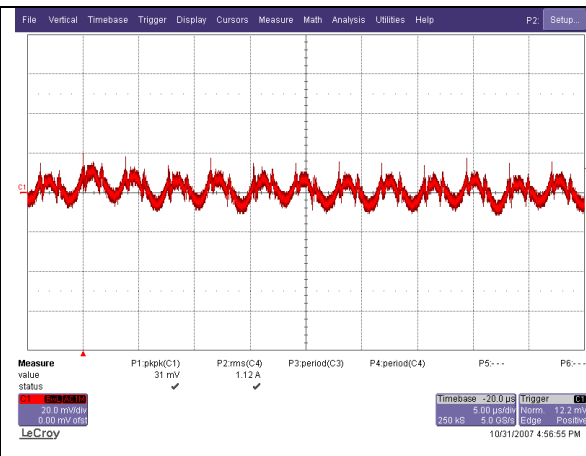

Efficiency vs. Input Voltage at Ta=25C, (test in socket)
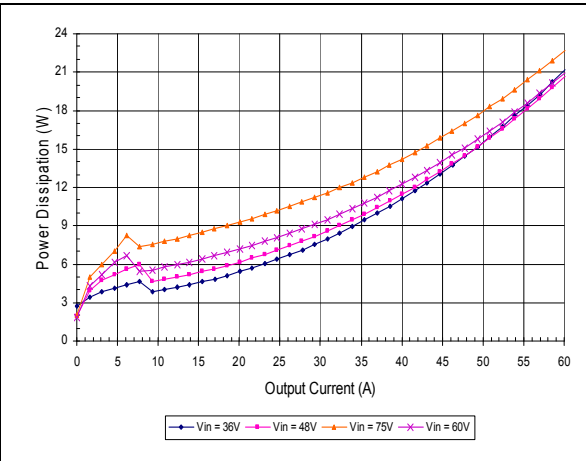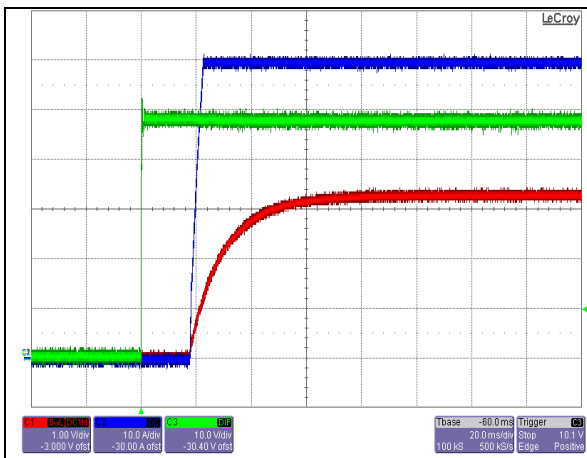

Power Dissipation vs. Input Voltage at Ta=25C
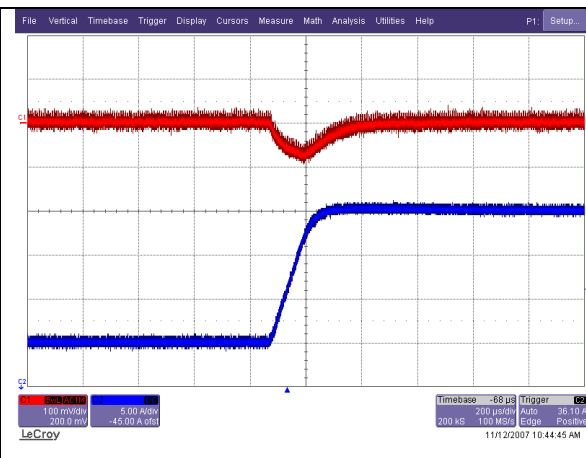

Start-up from on/off Switch at 48V input and Full Load.
Ch. 1: Vo   Ch. 2: ON/OFF   Ch. 4: Io

Typical Output Ripple at 48V Input and Full Load at
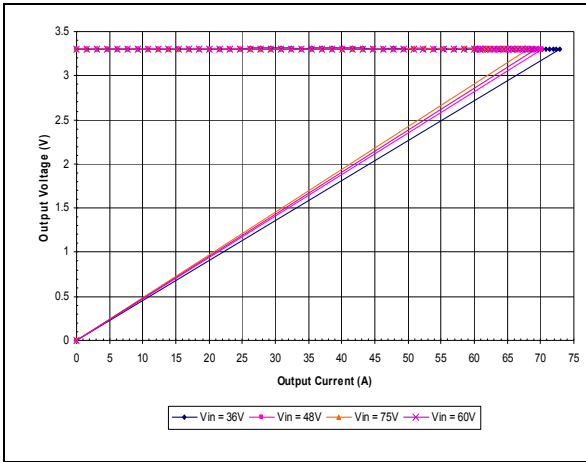Ta=25C   Ch. 1: Vo

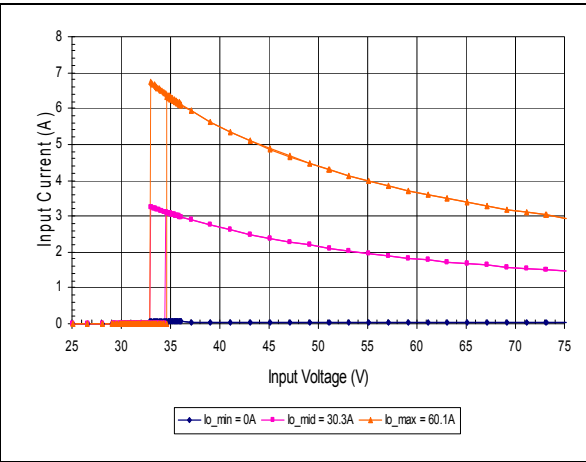Start-up from Input Voltage Application at Full Load.
Ch. 1: Vo   Ch. 2: Io   Ch. 3: Vin

Load Transient Response. Load Step from 50% to 75%
of Full Load with di/dt= 0.1A/uS. Ch. 1: Vo   Ch. 2: Io

Electrical Characteristics: iQL48060A033V-000 through -0x9: 3.3V, 60A, 198W Output

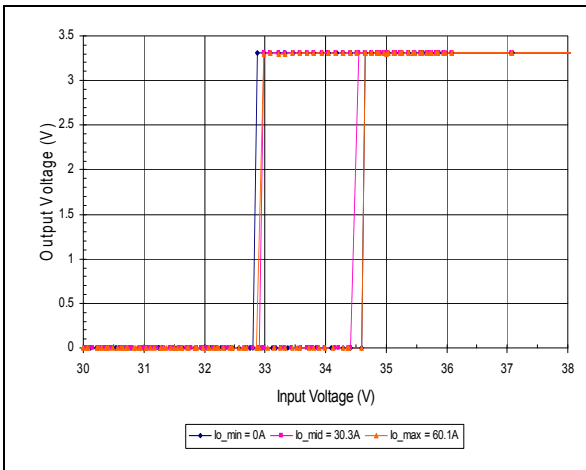Output Current Limit Characteristics vs. Input Voltage

Typical Input Current vs. Input Voltage Characteristics
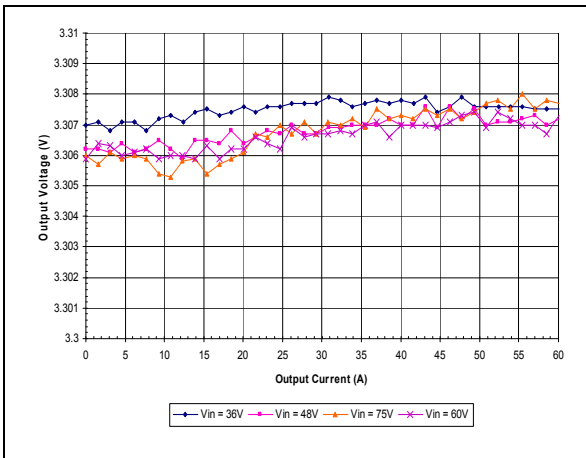
Typical Output Voltage vs. Load Current at Ta=25C.
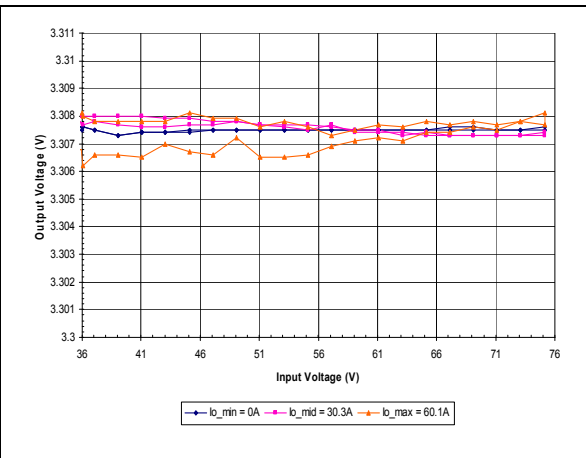
Typical Output Voltage vs. Input Voltage at Ta=25C.

Typical Output Voltage vs. Low Voltage Input Turn-on and Turn-off at Ta=25C

% Change of Vout	Trim Down Resistor (Ohm)	% Change of Vout	Trim Up Resistor (Ohm)
-10	40.9K	+5	176.7K
-20	15.3K	+10	90.1K

e.g. trim up 5%

$$\text{Rup} := \left\lceil \frac{5.11 \times 3.3 \cdot (100 + 5)}{1.225 \times 5} - \frac{511}{5} - 10.22 \right\rceil \cdot K$$
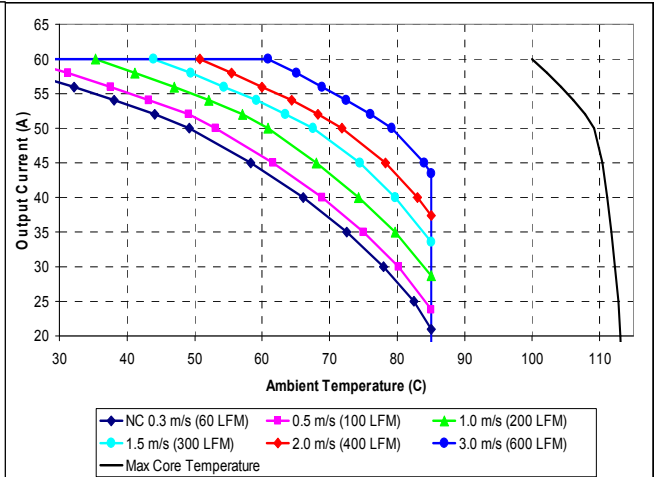
Calculated Resistor Values for Output Voltage Adjustment

Thermal Performance: iQL48060A033V-000 through -009: 3.3V, 60A, 198W Output
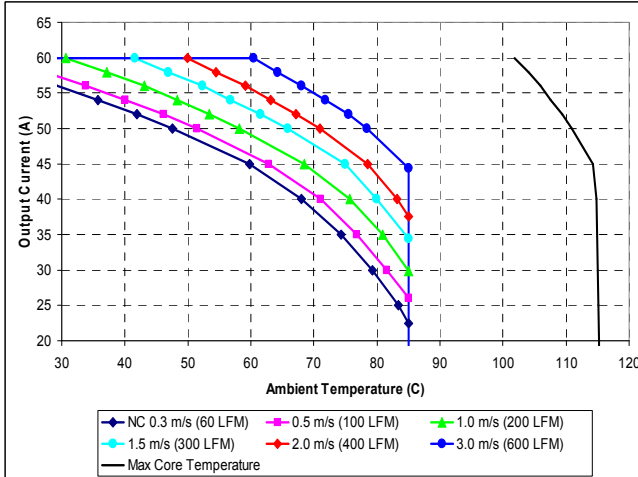
Maximum output current vs. ambient temperature at nominal input voltage **with no base plate** for airflow rates natural convection (0.3m/s) to 3.0m/s with airflow from pin 3 to pin 1.

Maximum output current vs. ambient temperature at nominal input voltage **with no base plate** for airflow rates natural convection (0.3m/s) to 3.0m/s with airflow from pin 1 to pin 3.
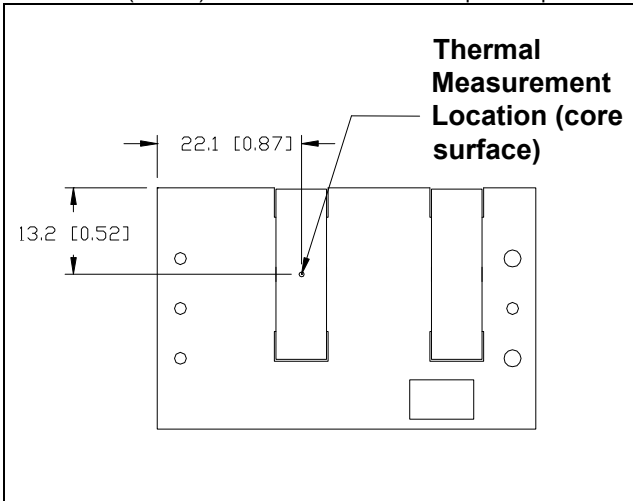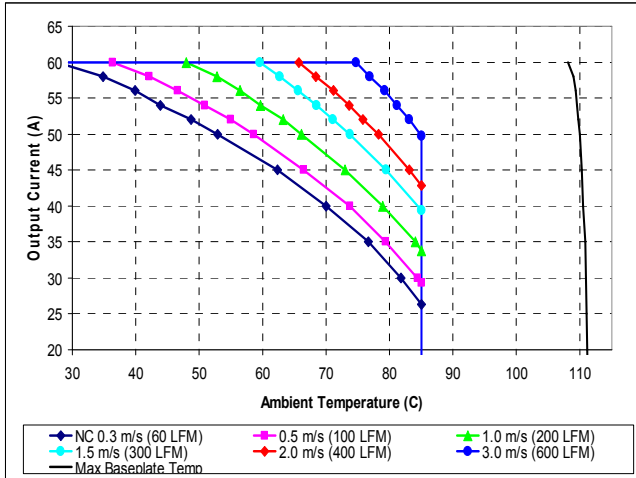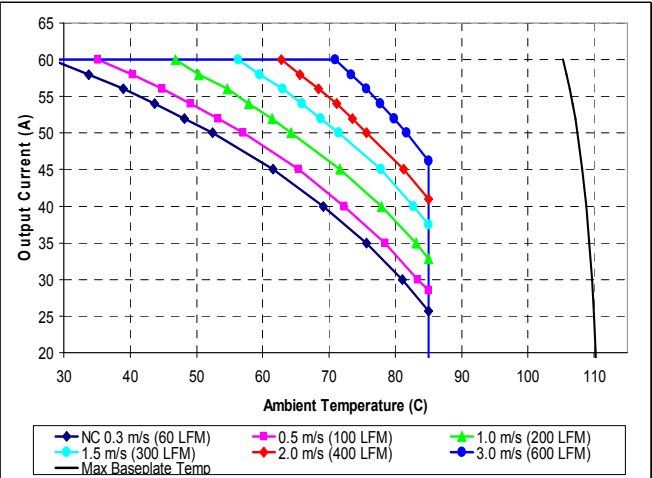

Thermal measurement location – top view

The thermal curves provided are based upon measurements made in TDK Innoveta's experimental test setup that is described in the Thermal Management section. Due to the large number of variables in system design, TDK Innoveta recommends that the user verify the module's thermal performance in the end application. The critical component should be thermo- coupled and monitored, and should not exceed the temperature limit specified in the derating curve above. It is critical that the thermocouple be mounted in a manner that gives direct thermal contact otherwise significant measurement errors may result.

Thermal Performance: iQL48060A033V-000 through -009: 3.3V, 60A, 198W Output

Maximum output current vs. ambient temperature at nominal input voltage **with base plate** for airflow rates natural convection (0.3m/s) to 3.0m/s with airflow from pin 3 to pin 1.

Maximum output current vs. ambient temperature at nominal input voltage **with base plate** for airflow rates natural convection (0.3m/s) to 3.0m/s with airflow from pin 1 to pin 3.
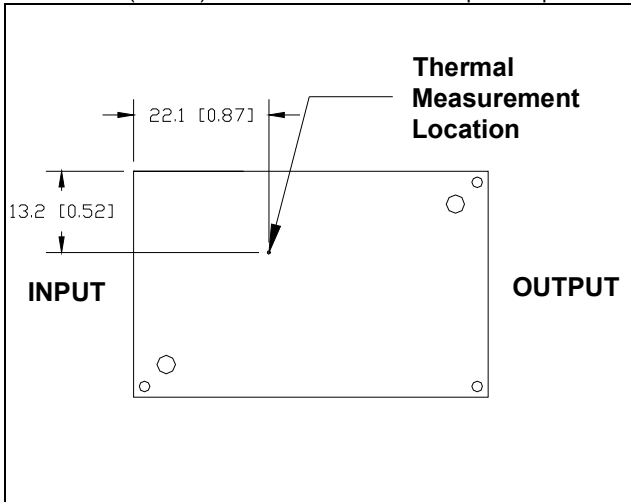
Thermal measurement location for base plate option – top view

The thermal curves provided are based upon measurements made in TDK Innoveta's experimental test setup that is described in the Thermal Management section. Due to the large number of variables in system design, TDK Innoveta recommends that the user verify the module's thermal performance in the end application. The critical component should be thermo- coupled and monitored, and should not exceed the temperature limit specified in the derating curve above. It is critical that the thermocouple be mounted in a manner that gives direct thermal contact otherwise significant measurement errors may result.

Thermal Management:

An important part of the overall system design process is thermal management; thermal design must be considered at all levels to ensure good reliability and lifetime of the final system. Superior thermal design and the ability to operate in severe application environments are key elements of a robust, reliable power module.

A finite amount of heat must be dissipated from the power module to the surrounding environment. This heat is transferred by the three modes of heat transfer: convection, conduction and radiation. While all three modes of heat transfer are present in every application, convection is the dominant mode of heat transfer in most applications. However, to ensure adequate cooling and proper operation, all three modes should be considered in a final system configuration.

The open frame design of the power module provides an air path to individual components. This air path improves convection cooling to the surrounding environment, which reduces areas of heat concentration and resulting hot spots.

Test Setup: The thermal performance data of the power module is based upon measurements obtained from a wind tunnel test with the setup shown in the wind tunnel figure. This thermal test setup replicates the typical thermal environments encountered in most modern electronic systems with distributed power architectures. The electronic equipment in networking, telecom, wireless, and advanced computer systems operates in similar environments and utilizes vertically mounted printed circuit boards (PCBs) or circuit cards in cabinet racks.

The power module is mounted on a 0.087 inch thick, 12-layer, 2oz/layer PCB and is vertically oriented within the wind tunnel. Power is routed on the internal layers of the PCB. The outer copper layers are thermally decoupled from the converter to better simulate the customer's application. This also results in a more conservative derating.

The cross section of the airflow passage is rectangular with the spacing between the top of the module and a parallel facing PCB kept at a constant (0.5 in). The power module's orientation with respect to the airflow direction can have a significant impact on the unit's thermal performance.

Thermal Derating: For proper application of the power module in a given thermal environment, output current derating curves are provided as a design guideline in the
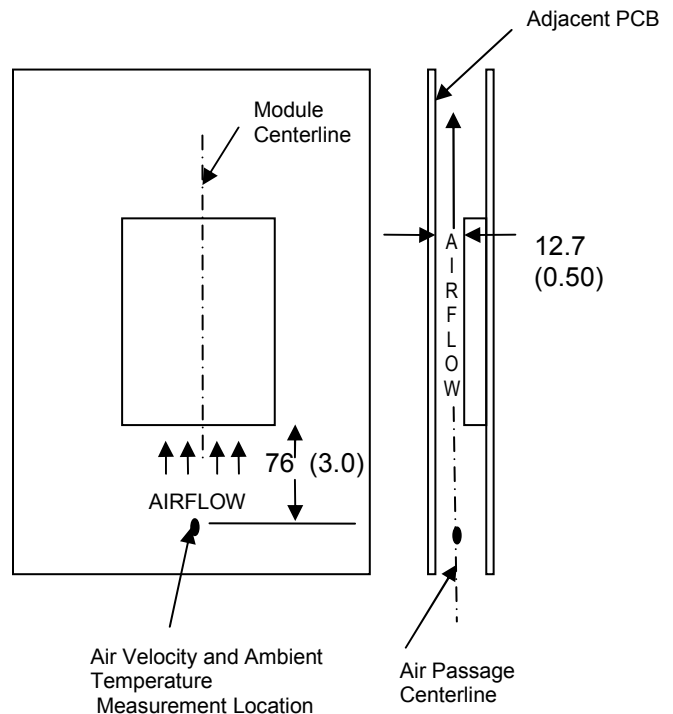
Wind Tunnel Test Setup Figure
Dimensions are in millimeters and (inches).

Thermal Performance section for the power module of interest. The module temperature should be measured in the final system configuration to ensure proper thermal management of the power module. For thermal performance verification, the module temperature should be measured at the component indicated in the thermal measurement location figure on the thermal

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performance page for the power module of interest. In all conditions, the power module should be operated below the maximum operating temperature shown on the derating curve. For improved design margins and enhanced system reliability, the power module may be operated at temperatures below the maximum rated operating temperature.

Heat transfer by convection can be enhanced by increasing the airflow rate that the power module experiences. The maximum output current of the power module is a function of ambient temperature (T_{AMB}) and airflow rate as shown in the thermal performance figures on the thermal performance page for the power module of interest. The curves in the figures are shown for natural convection through 3 m/s (600 ft/min). The data for the natural convection condition has been collected at 0.3 m/s (60 ft/min) of airflow, which is the typical airflow generated by other heat dissipating components in many of the systems that these types of modules are used in. In the final system configurations, the airflow rate for the natural convection condition can vary due to temperature gradients from other heat dissipating components.

Heatsink Usage: For applications with demanding environmental requirements, such as higher ambient temperatures or higher power dissipation, the thermal performance of the power module can be improved by attaching a heatsink or cold plate. The iQL platform is designed with a base plate with two M3 X 0.5 through-threaded mounting fillings for attaching a heatsink or cold plate. The addition of a heatsink can reduce the airflow requirement; ensure consistent operation and extended reliability of the system. With improved thermal performance, more power can be delivered at a given environmental condition.

Standard heatsink kits are available from TDK Innoveta Inc. for vertical module mounting in two different orientations

(longitudinal – perpendicular to the direction of the pins and transverse – parallel to the direction of the pins). The heatsink kit contains four M3 x 0.5 steel mounting screws and a precut thermal interface pad for improved thermal resistance between the power module and the heatsink. The screws should be installed using a torque-limiting driver set between 0.35-0.55 Nm (3-5 in-lbs).

The system designer must use an accurate estimate or actual measure of the internal airflow rate and temperature when doing the heatsink thermal analysis. For each application, a review of the heatsink fin orientation should be completed to verify proper fin alignment with airflow direction to maximize the heatsink effectiveness. For TDK Innoveta standard heatsinks, contact TDK Innoveta Inc. for latest performance data

Operating Information:

Over-Current Protection: The power modules have current limit protection to protect the module during output overload and short circuit conditions. During overload conditions, the power modules may protect themselves by entering a hiccup current limit mode. The modules will operate normally once the output current returns to the specified operating range. A latched over-current protection option is also available. Consult the TDK Innoveta technical support for details.

Output Over-Voltage Protection: The power modules have a control circuit, independent of the main control loop, that reduces the risk of over voltage appearing at the output of the power module during a fault condition. If there is a fault in the main regulation loop, the over voltage protection circuitry will latch the power module off once it detects the output voltage condition as specified on the Electrical Data page. To remove the module from the latched condition, either cycle the input power or toggle the remote ON/OFF pin providing that over-voltage conditions have been removed. The reset time of the ON/OFF pin should be 500ms or longer.

An optional non-latching OVP protection feature is also available. Consult the TDK Innoveta technical support for details.

Thermal Protection: When the power modules exceed the maximum operating temperature, the modules will turn-off to safeguard the units against thermal damage. The module will auto restart as the unit is cooled below the over temperature threshold. A latched over-temperature protection option is also available. Consult the TDK Innoveta technical support for details.

Remote On/Off: - The power modules have an internal remote on/off circuit. The user must supply an open-collector or compatible switch between the Vin(-) pin and the on/off pin. The maximum voltage generated by the power module at the on/off terminal is 15V. The maximum allowable leakage current of the switch is 50uA. The switch must be capable of maintaining a low signal Von/off < 0.8V while sinking 400uA.

The standard on/off logic is negative logic. The module will turn on if pin 2 is connected to pin 3, and it will be off if pin 2 is left open. If the negative logic feature is not being used, pin 2 should be shorted to pin 3.

An optional positive logic is available. The power module will turn on if pin 2 is left open and will be off if pin 2 is connected to pin 3. If the positive logic circuit is not being used, terminal 2 should be left open.
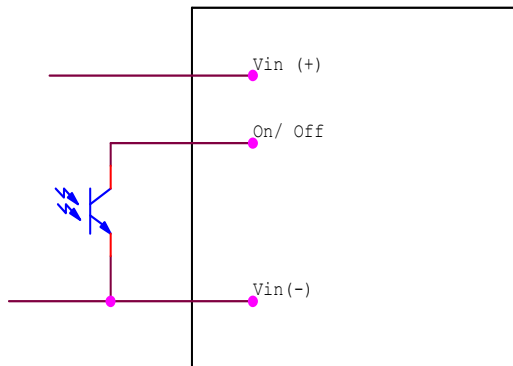
On/Off Circuit for positive or negative logic
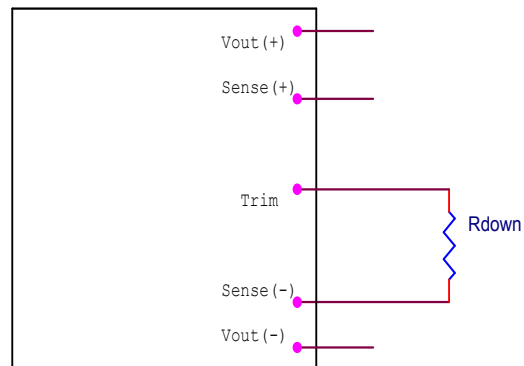
Circuit to decrease output voltage

Output Voltage Adjustment: The output voltage of the module may be adjusted by using an external resistor connected between the trim pin 6 and either the Sense (+) or Sense (-) pin. If the voltage trim feature is not used, pin 6 should be left open. Care should be taken to avoid injecting noise into the module's trim pin. A small 0.01uF capacitor between the power module's trim pin and Sense (-) pin may help to avoid this.

With a resistor between the trim pin and Sense (-) pin, the output voltage is adjusted down. To adjust the output voltage down a percentage of Vout (Δ%) from Vo,nom, the trim resistor should be chosen according to the following equation:

$$R_{down} = 5.11 \times (\frac{100}{\Delta\%} - 2) \qquad (k\Omega)$$

Where

Δ%=100×(Vo,nom - Vdesired) / Vo_nom

The current limit set point does not increase as the module is trimmed down, so the available output power is reduced.
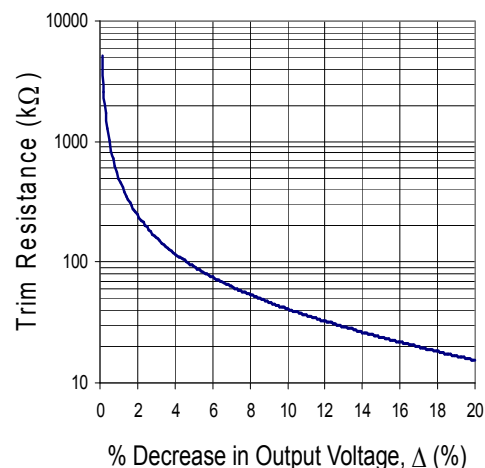
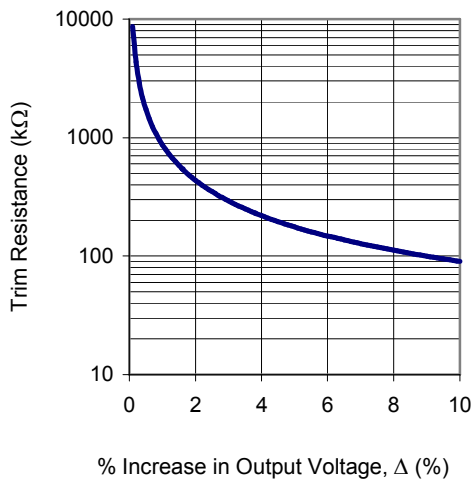With a resistor between the trim pin and sense (+) pin, the output voltage is adjusted up. To adjust the output voltage up a percentage of Vout (Δ%) from Vo,nom the trim resistor (in kΩ) should be chosen according to the following equation:

$$R_{up} = 5.11 \times (\frac{V_{0,nom} \times (100 + \Delta\%)}{Vref \times \Delta\%} - \frac{100}{\Delta\%} - 2)$$
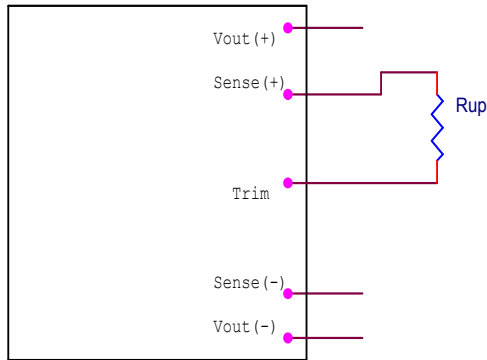
Circuit to increase output voltage

% Increase in Output Voltage, Δ (%)

The value of Vref can be found in the Electrical Data section of this data sheet. The maximum power available from the power module is fixed. As the output voltage is trimmed up, the maximum output current must be decreased to maintain the maximum rated power of the module. It is also desirable to slightly increase the input voltage while trimming up the output with heavy load current.

As the output voltage is trimmed up, the output over-voltage protection set point is not adjusted. Trimming the output voltage too high may cause the output over voltage protection circuit to be triggered.

Remote Sense: The power modules feature remote sense to compensate for the effect of output distribution drops. The output voltage sense range defines the maximum voltage allowed between the output power terminals and output sense terminals, and it is found on the electrical data page for the power module of interest. If the remote sense feature is not being used, the Sense(+) pin should be connected to the Vo(+) pin and the Sense (-) pin should be connected to the Vo(-) pin.

The output voltage at the Vo(+) terminal can be increased by either the remote sense or the output voltage adjustment feature.

As the output voltage increases due to the use of the remote sense, the maximum load current must be decreased for the module to remain below its maximum power rating.

EMC Considerations: TDK Innoveta power modules are designed for use in a wide variety of systems and applications. With the help of external EMI filters and careful layout, it is possible to meet CISPR 22 class A or B requirement. For assistance with designing for EMC compliance, please contact TDK Innoveta technical support.

Input Impedance: The source impedance of the power feeding the DC/DC converter module will interact with the DC/DC converter. To minimize the interaction, one or more 220uF to 470uF/100V input electrolytic capacitors should be present if the source inductance is greater than 4uH.

<u>Reliability:</u>

The power modules are designed using TDK Innoveta's stringent design guidelines for component derating, product qualification, and design reviews. Early failures are screened out by both burn-in and an automated final test. The MTBF is calculated to be greater than 3M hours at nominal input, full load, and Ta = 40˚C using

the Telcordia TR-332 issue 6 calculation method.

Improper handling or cleaning processes can adversely affect the appearance, testability, and reliability of the power modules. Contact TDK Innoveta technical support for guidance regarding proper handling, cleaning, and soldering of TDK Innoveta's power modules.

Quality:

TDK Innoveta's product development process incorporates advanced quality planning tools such as FMEA and Cpk analysis to ensure designs are robust and reliable. All products are assembled at ISO certified assembly plants.

Input/Output Ripple and Noise Measurements:
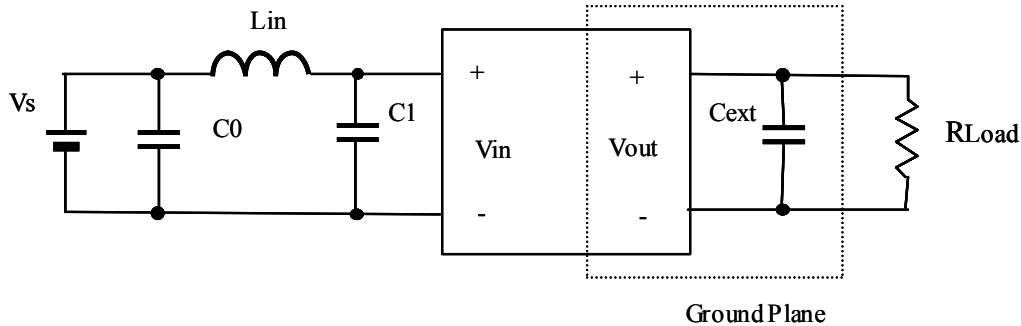
The input reflected ripple is measured with a current probe and oscilloscope. The ripple current is the current through a 12μH differential mode inductor, Lin, with esr ≤ 10 mΩ, feeding a capacitor, C1, esr ≤ 700 mΩ @ 100kHz, across the module input voltage pins. The capacitor C1 across the input shall be at least one 220μF/100V electrolytic capacitor along with two 1uF to 2.2uF/100V ceramic capacitors. Two 220μF/100V electrolytic capacitors and two 2.2uF/100V ceramic capacitors are recommended. A 220μF/100V capacitor for C0 is also recommended.

The output ripple measurement is made approximately 7 cm (2.75 in.) from the power module using an oscilloscope and BNC socket. The capacitor Cext is located about 5 cm (2 in.) from the power module; its value varies from code to code and is found on the electrical data page for the power module of interest under the ripple & noise voltage specification in the Notes & Conditions column.

Safety Considerations:

For safety agency approval of the system in which the DC-DC power module is installed, the power module must be installed in compliance with the creepage and clearance requirements of the safety agency. The isolation is basic insulation. For applications requiring basic insulation, care must be taken to maintain minimum creepage and clearance distances when routing traces near the power module.

As part of the production process, the power modules are hi-pot tested from primary and secondary at a test voltage of 1500Vdc.

To preserve maximum flexibility, the power modules are not internally fused. An external input line normal blow fuse with a maximum value of 15A is required by safety agencies. A lower value fuse can be selected based upon the maximum dc input current and maximum inrush energy of the power module.

When the supply to the DC-DC converter is less than 60Vdc, the power module meets all of the requirements for SELV. If the input voltage is a hazardous voltage that exceeds 60Vdc, the output can be considered SELV only if the following conditions are met:

1) The input source is isolated from the ac mains by reinforced insulation.
2) The input terminal pins are not accessible.
3) One pole of the input and one pole of the output are grounded or both are kept floating.
4) Single fault testing is performed on the end system to ensure that under a single fault, hazardous voltages do not appear at the module output.

Warranty:

TDK Innoveta's comprehensive line of power solutions includes efficient, high-density DC-DC converters. TDK Innoveta offers a three-year limited warranty. Complete warranty information is listed on our web site or is available upon request from TDK Innoveta.