

TOSHIBA TLX-8062S-C3X

PANEL CHARACTERISTICS

Unit : STN-LCD DD 16 BITS
 Display format : 640 X 480
 Colors : 8 (passive matrix)

TEK932 BIOS & JUMPER INFORMATION

BIOS : VAE_BGA
BIOS not tested with Panel at TEKNOR.
 Jumper: W10: Short 1-2

TECHNICAL INFORMATION

Consult the technical data sheets that came with your panel to adjust the power VLCD.

Display Flat Panel and CRT simultaneously.

DISPLAY INTERCONNECTION (or use a dapter - see before)

TEK932		EXTERNAL	FLAT PANEL	
Signal Name	J23: PIN #		PIN #	Signal Name
ENAVDD	1			
Vcc (+5V)	2		CN1-5	VDD
ENVEE	3		CN1-4	DISP
STNDBYE*	4			
ENABLK	5			
M/DE	7			
ACTI	8			
LP/DE	10		CN1-2	LP
FLM	11		CN1-1	FP
SHFCLK	13		CN1-3	SCP
P0	15		CN1-15	UD7
P1	16		CN1-14	UD6
P2	18		CN1-13	UD5
P3	19		CN1-12	UD4
P4	21		CN2-9	LD7
P5	22		CN2-8	LD6
P6	24		CN2-7	LD5
P7	25		CN2-6	LD4
P8	27		CN1-11	UD3
P9	28		CN1-10	UD2
P10	30		CN1-9	UD1
P11	31		CN1-8	UD0
P12	33		CN2-5	LD3
P13	34		CN2-4	LD2
P14	36		CN2-3	LD1
P15	37		CN2-2	LD0
P16	39			
P17	40			
P18	42			
P19	43			
P20	45			
P21	46			
P22	48			
P23	49			
		N.C.		
POWER LCD		VLCD	CN1-7	VEE
GND	6-9-12-14-17-20-23-26-29-32-35-38-41-44-47-50		CN1-6,CN2-1,CN2-10	GND

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