HITACHI

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FOR MESSRS:	DATE: Nov.12,2010
I OK MESSINS.	DAIL : NOV. 12,2010

CUSTOMER'S ACCEPTANCE SPECIFICATIONS

TX14D11VM1CAA

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*When product will be discontinued, customer will be informed by HITACHI with twelve months prior announcement.

ACCEPTED BY;		PROPOSED BY; Len	Lle	<u>~</u>
KAOHSIUNG HITACHI	Sh.	7B64PS 2701-TX14D11VM1CAA-5	PAGE	1-1/1
FLECTRONICS COLLTD	No	10041 0 2101-17(14D11VW11077-3	I AGE	1 - 1/ 1

RECORD OF REVISION

DATE	SHEET No.		SU	MMARY				
Nov.28,'03	7B64PS 2703-	3. GENERAL DATA	4					
	TX14D11VM1CAA-2 PAGE 3-1/1	ADDED : (14) Vie	ew Direction 6	O'clock				
	7B64PS 2706-	6.1 OPTICAL CHA	RACTERISTIC	S OF LCD				
	TX14D11VM1CAA-2	ITEM	SYMBOL	CONDITIO	ON TYP.			
	PAGE 6-1/3		θ x	φ=0°,K≥	5.0 (50)			
		\/ioving Aroo	$\theta \mathbf{x}$	<i>φ</i> =180°,Κ≧	≥ 5.0 (50)			
		Viewing Area	θ y	<i>φ</i> =90°,Κ≧				
			θ y	<i>φ</i> =270°,K≧	≥ 5.0 (80)			
				\		<u> </u>		
		ITEM	SYMBOL	CONDITIO	ON TYP.			
			θ x	φ=0°,K≥	5.0 65			
			$\theta \mathbf{x}$	<i>φ</i> =180°,K≧				
		Viewing Area	θ y	<i>φ</i> =90°,Κ≧				
			θ y	<i>φ</i> =270°,K≧	≥ 5.0 50			
				,	•			
	7B64PS 2708-	8.5 INTERNAL PIN	CONNECTIO	N				
	TX14D11VM1CAA-2 PAGE 8-5/5	CORRECTED C			A5B040HF1			
	7B64PS 2709-	9. DIMENSIONAL OUTLINE						
	TX14D11VM1CAA-2	CORRECTED: The dimension of CN1						
	PAGE 9-2/2	21.25 → (17.35)						
May.18.'04 7B64PS 2704-	7B64PS 2704-	4.2 ENVIRONMENT	TAL ABSOLUT	E MAXIMUM	RATINGS			
	TX14D11VM1CAA-3 PAGE 4-1/1	ITEM	OPERATING	STORAGE	── COMMENT			
		Temperature	MIN. MAX10 70	MIN. MAX30 80	(Note 2,3,6,7,8,	10)		
			1					
			OPERATING	STORAGE				
		ITEM	MIN. MAX.	MIN. MAX.	COMME	١T		
		Temperature	-20 70		(Note 2,3,6,7,8,	10 12)		
		Note 2 : Ta at -30°	<u> </u>	<u> </u>		.0,.2)		
			\downarrow					
		Note 2 : For storage For operati	e condition Ta ng condition T		·	100h.		
		Addendum :						
		Note 12 : When LCI	M be operated	l less than 0°	$\mathbb C$, the lift time $\mathfrak C$	of CFL w		
		be reduce	•					
				ON will be lo	nger when the	ambient		
		temperatu	ire below 0°C	and confirming	ng the character	istics of		
		inverter is	s necessary.					
					1			
AOHSIUN	IG HITACHI		7DC4DC 0700	TV4.4D44\/N	1CAA-5 PAGE	[2-1/3		
	11 1 / /	.		. 🗶 ๅ /	11 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	- '/-'1/		

RECORD OF REVISION

DATE	SHEET No.	SUMMARY								
May.18.'04	7B64PS 2706-	6.1 OPTICAL CHARA			CTERI	ISTICS OF LCD				
	TX14D11VM1CAA-3 PAGE 6-1/3		ITEN	И	SYMB	OL	CONDITIO N	MIN.	TYP.	MAX.
					Х			-	0.61	-
				Red	у			-	0.33	-
			Color		х			-	0.30	-
			Tone	Green	у		/ 0° 0 0°	-	0.57	-
			(Primary		х		$\phi = 0^{\circ}, \theta = 0^{\circ}$	-	0.14	-
			Color)	Blue	у			-	0.08	-
					х			-	0.29	-
				White	у			-	0.29	-
							\downarrow			
			ITEN	М	SYMB	OL	CONDITIO N	MIN.	TYP.	MAX.
					Х			0.56	0.61	0.66
				Red	у			0.28	0.33	0.38
			0	0	х		$\phi = 0^{\circ}, \ \theta = 0^{\circ}$	0.25	0.30	0.35
			Color Tone	Green	у			0.52	0.57	0.62
			(Primary	Dive	х			0.09	0.14	0.19
			Color)	Blue	у			0.03	0.08	0.13
May.13,'08	7B64PS 2705- TX14D11VM1CAA-4 PAGE 5-1/3		3 MECHAN nged:		HARAC	T		. 1	NOTE	
				ITEM		SPI	ECIFICATION	N	NOTE	-
			Pen In	put Pre	ssure		0.1~0.8N	R0.8,	Polyace	etal Pen
			Finger				0.1~1.0N	R8,	Silicon F	Rubber
						1	<u> </u>			
				ITEM		SPI	ECIFICATION	١	NOTE	
		Pen Input Pre		put Pre	ssure		1.2N max.	R0.8, Polyacetal Per		etal Pen
						1.2N max.	R8,	R8, Silicon Rubber		

7B64PS 2702-TX14D11VM1CAA-5 PAGE 2-2/3

KAOHSIUNG HITACHI DATE Nov.12,'10 Sh. No.

KAOHSIUNG HITACHI

RECORD OF REVISION

DATE	SHEET No.	SUMMARY
May.13,'08	7B64PS 2708-	8.5 INTERNAL PIN CONNECTION
	TX14D11VM1CAA-4	Changed:
	PAGE 8-5/5	CN1 JAE : FA5B040HF1(Sn plating) → FA5B040HP1R3000 (Au plating)
	7B64PS 2709-	9. DIMENSIONAL OUTLINE
	TX14D11VM1CAA-4	
	PAGE 9-2/2	The lot label size and position is changed.
	7B64PS 2712-	12.1 LOT MARK
	TX14D11VM1CAA-4 PAGE 12-1/1	Changed: 5 digits for production number
		6 digits for production number
		12.3 LOCATION OF LOT MARK Changed:
		(90)
		HITACHI TX14D11VM1CAA Lot No. & Production Control No. Solid No. & CAUTION RIGH VOLTAGE
		↓ ← (26)
		TX14D11VM1CAA REV: 8041T (5D) 123456 HITACHI MADE IN TAIWAN
		Added: 12.4 REVISION(Rev.) CONTROL
		Rev No. ITEM
		- CN1 JAE : FA5B040HF1
		A CN1 JAE : FA5B040HP1R3000
Nov.12,'10 7B64PS 2710- TX14D11VM1CAA-5 PAGE 10-5/5		10.3 APPEARANCE SPECIFICATION Changed : Blistering Puffiness 0.4mm max. → 0.6mm max.
AOHSIUN	G HITACHI	Sh
LECTRON	IICS CO.,LTD. DATI	E Nov.12,'10 No. 7B64PS 2702-TX14D11VM1CAA-5 PAGE 2-3/3

3.GENERAL DATA

(1) Part Name TX14D11VM1CAA

(2) Module Dimensions 167.0(W)mm x 109.0(H)mm x (10.9)(D)mm max.

(3) LCD Active Area 115.2(W)mm x 86.4(H)mm

(4) Dot Pitch 0.12(W)mm x 3(R,G,B)(W) x 0.36(H)mm

(5) Resolution 320x3(R,G,B))(W)x240(H) dots

(6) Color Pixel Arrangement R,G,B Vertical stripe

(7) LCD Type Transmissive Color TFT LCD (Normally White)

(8) Display Type Active Matrix

(9) Number of Colors 262k Colors (R,G,B 6bit parallel)

(10) Backlight Cold Cathode Fluorescent Tube (U type CFL) x 1

(11) Weight (220)g

(12) Interface 40pin (C-MOS)

(13) Power Supply Voltage 3.3V only (Include Timing Controller and Power Unit)

(14) View Direction 6 O'clock

(15) Touch Panel Resistance type

The surface is antiglare type

4. ABSOLUTE MAXIMUM RATINGS

4.1 ELECTRICAL	ARSOLUTE	MILMIXAM	RATINGS	OF LCD	
4.1 LLLUINUAL	ADOULUIL			OI LOD	

ITEM	SYMBOL	MIN.	MAX.	UNIT	COMMENT
Power Supply for Logic	VDD	-0.3	4.0	V	
Input Voltage	VI	-0.2	VDD+0.2		(Note 1)
Input Current	li	0	1	Α	
Statio Floatricity	VESD0	-	±100	V	(Note 2,3)
Static Electricity	VESD1	-	±8	kV	(Note 2,4)

VSS=0V

Note 1: DTMG,DCLK,RD0~RD5,GD0~GD5,BD0~BD5.

Note 2 : 200pF-250 Ω 25 $^{\circ}$ C - 70%RH

Note 3: Interface Pin Connector.

Note 4: The surface of metal bezel and LCD panel.

4.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	OPERATING		STORAGE		COMMENT	
I I \sqsubset IVI	MIN.	MAX.	MIN.	MAX.	COMMENT	
Temperature	-20	70	-30	80	(Note 2,3,6,7,8,10,12)	
Humidity	(Not	te 1)	1)	Note 1)	Without condensation	
Vibration	-	4.9m/s ² (0.5G)	-	19.6m/s ² (2G) (Note 5)	(Note 4)	
Shock	-	29.4m/s ² (3G)	-	490m/s ² (50G) (Note 5)	XYZ directions (Note 9)	
Corrosive Gas	Not Aco	ceptable	Not Acceptable			
CFL Lift Time	50,000 h (Average) (Note 11)		-		at 25°C , IL=4.0mA max.	

Note 1 : Ta≤40°C : 85%RH max.

Ta> 40° C: Absolute humidity must be lower than the humidity of 85%RH at 40° C.

Note 2 : For storage condition Ta at -30 $^{\circ}\!\mathbb{C}$ < 48h , at 80 $^{\circ}\!\mathbb{C}$ < 100h.

For operating condition Ta at -20° C < 100h

Note 3: Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note 4:5Hz~100Hz(Except resonance frequency)

Note 5: This LCM will resume normal operation after finishing the test.

Note 6: The response time will be slower as low temperature.

Note 7 : Only operation is guarantied at operating temperature. Contrast, response time, another display quality are evaluated at +25°C.

Note 8 : When LCM is operated over 60° C ambient temperature, the ICFL of LCM should be adjusted to 3mA max.

Note 9: Pulse Width: 10ms

Note 10: This is panel surface temperature, not ambient temperature.

Note 11: When brightness reached 50% of initial brightness.

Note 12: When LCM be operated less than 0° C, the lift time of CFL will be reduced. The rise time of CFL ON will be longer when the ambient temperature below 0° C and confirming the characteristics of inverter is necessary.

KAOHSIUNG HITACHI	DATE	Nov.12,'10 Sh	n. 7B64PS 2704-TX14D11VM1CAA-5 PAGE 4-1/1	
ELECTRONICS CO.,LTD.		Nc		l

5. ELECTRICAL CHARACTERISTICS

5.1 ELECTRICAL CHARACTERISTICS OF LCD

Ta=25°C,VSS=0V

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Power Supply Voltage	VDD	-	3.0	3.3	3.6	V
Input Voltage for Logic	VI	"H" level	2.0	-	VDD	V
(Note 1)	VI	"L" level	VSS	-	8.0	V
Power Supply Current (Note 2)	IDD	VDD-VSS=3.3V	-	(150)	-	mA
Vsync Frequency	fV	-	52	60	68	Hz
Hsync Frequency	fH	-	13.1	15.2	17.7	kHz
DCLK Frequency	fCLK	-	4.85	5.85	7.0	MHz

Note 1: DTMG,DCLK, RD0~RD5,GD0~GD5,BD0~BD5.

Note 2 : f V=60Hz,Ta=25°C, Pattern used as display pattern : All Black.

Note 3: Need to make sure of flickering and rippling of display when setting the frame frequency in your set.

5.2 ELECTRICAL CHARACTERISTICS OF TOUCH PANEL

5.2.1 OPERATING CONDITION

ITEM	SPECIFICATION
Operating Voltage	5VDC max.

5.2.2 ELECTRICAL CHARACTERISTICS

ITEM		SPECIFICATION	NOTE
Resistance XR-XI		210~640 Ω	
Between Terminal YT-YB		240~680 Ω	
Insulation Resistance X-Y		20M Ω min.	Operating Voltage: 25V DC
Lincarity	X	1.5% max.	(Note 1)
Linearity	Υ	1.5% max.	(Note 1)
Chattering		10ms max.	

5.2.3 MECHANICAL CHARACTERISTICS

ITEM	SPECIFICATION	NOTE
Pen Input Pressure	1.2N max.	R0.8, Polyacetal Pen
Finger	1.2N max.	R8, Silicon Rubber
Surface Hardness	2H min.	

5.2.4 OPTICAL CHARASTERISTICS

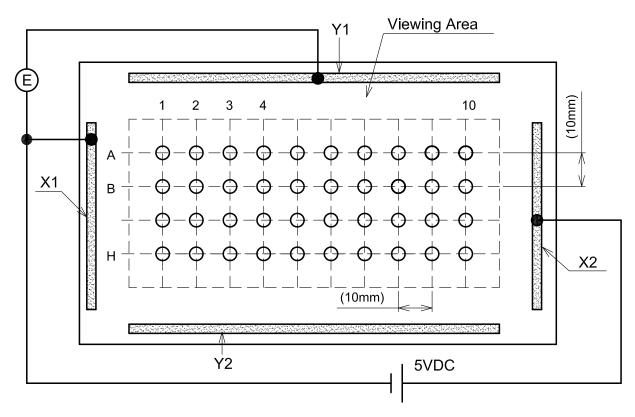
ITEM	SPECIFICATION	NOTE
Transparency	76% min	

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Note 1: Operating Voltage 5V DC.

Note 2: Test Condition.

(a) Y axis linearity testing method, 100g, VX1-VX2=5V, VOUT=VY1.

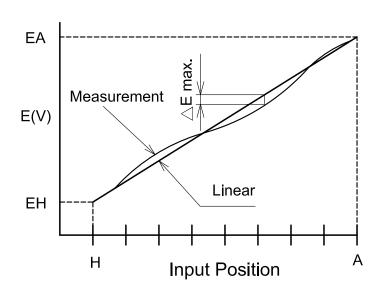


(b) X axis linearity testing method, VY1-VY2=5V, VOUT=VX1.

Note 3: Calculation

(a) Y axis linearity

Linearity=
$$\frac{\triangle E \text{ max.}}{EA - EH} \times 100(\%)$$



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5.3 ELECTRICAL CHARACTERISTICS OF BACKLIGHT

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTE
Lamp Voltage	VL	-	760	-	Vrms	Ta=25°C
Frequency	fL	-	55	-	kHz	
Lamp Current (1Lamp)(Note 6)	IL	3.0	4.0	6.0	mΑ	Ta=25°C
Starting Discharge Voltage	VS (Note 2)	1300	-	-	Vrms	Ta=5°C

- Note 1 : Please design your lamp driving circuit (inverter) according to the above specifications, and inform HITACHI about it.
- Note 2 : Starting discharge voltage is increased when LCM is operating under low temperature.
 - Please check the characteristics of your inverter before applying to your set.
- Note 3 : Average life time of CFL will be decreased when LCM is operating under low temperature.
- Note 4: Under lower driving frequency of an inverter, a certain Backlight system (CFL & CFL reflection sheet) may generate a sound noise. Before designing the inverter, please consider the driving frequency and noise.
- Note 5: When IL is over 6.0mA, it may cause uneven contrast near CFL location, due to heat dispersion form CFL.
- Note 6: We recommend to equip protection circuit (To stop output) which works under abnormal operation to the inverter for CFL

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6. OPTICAL CHARACTERISTICS

6.1 OPTICAL CHARACTERISTICS OF LCD

Ta=25°C (Backlight on)

ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE
		θx	<i>φ</i> =0°,K≥5.0	-	65	-	deg	1~5
Minusing Area		$\theta \mathbf{x}$	<i>φ</i> =180°,K≧5.0		65		deg	1~5
Viewing Area		θ y	<i>φ</i> =90°,K ≥ 5.0		70		deg	1~5
		θ y	ϕ =270°, K \geq 5.0	-	50	-	deg	1~5
Contrast Ratio		K	$\phi = 0^{\circ}, \theta = 0^{\circ}$	120	350	-	-	5
Response Time (rise+fall)		tr+tf	ϕ =0°, θ =0°	-	(45)	-	ms	6
	Red	х		0.56	0.61	0.66	-	
		у		0.28	0.33	0.38	-	
	Green	х		0.25	0.30	0.35	-	
Color Tone	Gleen	у	$\phi = 0^{\circ}$, $\theta = 0^{\circ}$	0.52	0.57	0.62	-	
(Primary Color)	Blue	х	$\varphi = 0$, $\theta = 0$	0.09	0.14	0.19	ı	
	Blue	у		0.03	0.08	0.13	-	
	White	х		0.24	0.29	0.34	-	
	vviiite	У		0.24	0.29	0.34	-	

(Measurement condition : HITACHI standard)

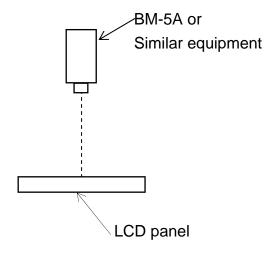
(Note 3~6): See next page.

Note 1 : Driving Condition

Display Pattern : White Raster

ICFL Current: 4mA

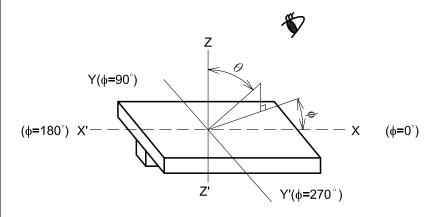
Note 2 : Measurement Condition (Transmitance)



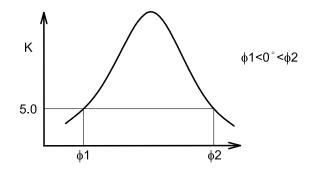
KAOHSIUNG HITACHI	DATE	N 40 40 S	Sh.	ZD04D0 0700 TV/44D44V/M40AA 5	DACE	C 1/2
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Note 3 : Definition of θ and ϕ (Normal) Viewing direction

Note 5 : Definition of contrast "K" $K = \frac{\text{White Brightness}}{\text{Black Brightness}}$



Note 4 : Definition of Viewing angle $\phi 1$ and $\phi 2$

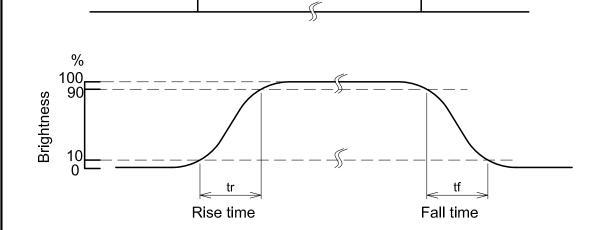


Contrast ratio "K" vs Viewing angle "\phi"

b=0°
BM-5A or Similar equipment

Note 6: Definition optical response time

Black



White

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Black

6.2 OPTICAL CHARACTERISTICS OF BACKLIGHT

ITEM	MIN.	TYP.	MAX.	UNIT	NOTE
Brightness	1	280	-	cd/m ²	IL=4.0 mA (Note 1,2)
Rise Time	1	3	-	Minute	IL=4.0 mA Brightness 80%
Brightness Uniformity	1	-	±25	%	Under mentioned (Note 1,3)

(Measurement condition: HITACHI standard)

CFL:0h operation, Ta=25°C

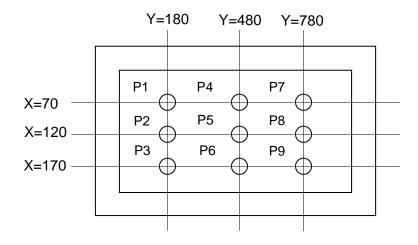
Display data should all be "ON"

Note 1 : Measurement after 10 minutes from CFL operating.

Average value of 9 points (Note 3)

Note 2: Brightness control: 100%.

Note 3: Measurement of the following 9 places on the display.



Note 4: Definition of the brightness tolerance.

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7.BLOCK DIAGRAM I/F(CN1) Timing Power Supply Driver Controller TFT-LCD Gate Power G240 Data / Clock Circuit Timing Signals D960 Source Driver CN₂ GND -VL · **CFL** Touch Panel Touch Panel Signals KAOHSIUNG HITACHI Sh. DATE Nov.12,'10 7B64PS 2707-TX14D11VM1CAA-5 PAGE 7-1/1 ELECTRONICS CO.,LTD.

8.INTERFACE TIMING 8.1 INTERFACE TIMING

	ITEM	MIN.	TYP.	MAX.	UNIT	SYMBOL	REMARKS
DCLK	Cycle time	(142)	(171)	(206)		tclk	
	Low level Width	12	-	-		twcL	
	High level Width	12	-	-	ns	twch	
	Rise time	-	-	25		trclk	
	Fall time	-	-	25		tfCLK	
	Duty	0.45	0.5	0.55	-	D	D= tclkl/clk
Hsync	Set up time	5			nc	tsн	for DCLK
	Hold time	10			ns	tнн	IOI DOLK
	Cycle	370	(385)	397	tour	thp	
	Valid width	4	(5)	-	tclk	twн	
	Rise/Fall time	-	-	30	ns	tHr,tHf	
Vsync	Set up	0	-	-	tclk	tsv	for Hsync
	Hold	2	-	-	ICLK	thv	101 TISYTIC
	Cycle	251	(253)	261	t HP	tvp	
	Valid width	2	(2)		LHP	twv	
	Rise/Fall time	-	-	50	ns	t ∨r, t ∨f	
DTMG	Set up time	5	-	-	ns	tsı	for DCLK
	Hold time	10	-	-	115	tнı	IOI DOLK
	Rise/Fall time	-	-	30	ns	tır,tıf	
	Horizontal back porch	28	(35)	-	tclk	t HBP	
	Horizontal front porch	22	(30)	-	ICLK	t HFP	
	Vertical back porch	6	(7)	-	t HP	t vbp	
	Vertical front porch	5	(6)	-	LHP	t VFP	
Data	Set up time	5	-	-	ne	t sd	for DCLK
	Hold time	10	-	-	ns	thd	IOI DOLK
	Rise/Fall time	-	-	25	ns	tDr,tDf	

Note: Vsync Cycle No. should be set to odd.

KAOHSIUNG HITACHI	D 4 T F	Nov. 40 340	Sh.	7D04D0 0700 TV44D44V4A0AA 5 DA	<u>о</u> г	0.4/5
ELECTRONICS CO.,LTD.	DATE	Nov.12,'10	No.	7B64PS 2708-TX14D11VM1CAA-5 PA	GE	8-1/5

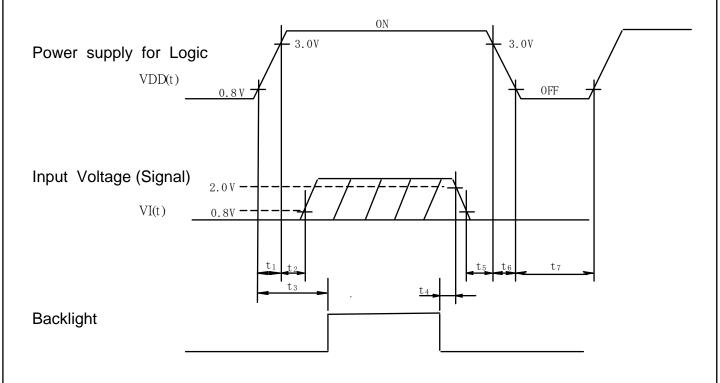
8.2 Timing Chart (Data is latched negative edge trigger of DCLK) t_{Hf} , t_{Vf} t_{Hr} , t_{Vr} t_{If}, t_{Df} t_{Ir}, t_{Dr} VSYNC, HSYNC, DTMG, V_{IH}min R0~5, G0~5, B0~5 V_{IL}max. DCLK t_{HD} Invalid Data Invalid Data DATA $t_{ m HI}$ DTMG DCLK $\mathbf{t}_{\underline{SH}}$ Hsync t_{HV} Vsync t_{HP} $\mathsf{t}_{\mathtt{WH}}$ Hsync t_{HBP} t_{HFP} DTMG t_{VP} -sf Vsync t_{VFP} DTMG

Note 1: DTMG is definition of the above timing for Hsync and Vsync.

Note 2: No matter when Hsync and Vsync is inputted, this LCM can be drove only DTMG Signal. DTMG should be set to low level when it is not input valid data.

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8.3 POWER ON/OFF SEQUENCE



Note 1 : $0V \le VI(t) \le VDD(t)$

VI(t) and VDD(t) is a surfeit of condition for power on/off.

Note 2 : Input Voltage(Signal) should not be set high impedance when power on.

8.4 RELATIONSHIP BETWEEN DISPLAYED COLOR AND INPUT DATA

	COLOR &	GRAY								DΛ	тл (SIGN	1 / 1							
	GRAY	SCALE								DA	IA	יוטוכ	NAL.							
	SCALE	LEVELS	R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	B0	B1	B2	В3	B4	B5
	Black	-	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	-	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Green	-	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Cyan	-	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
Color	Red	-	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	-	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	-	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\uparrow	GS1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Red	↑	\rightarrow				\downarrow						\downarrow					\downarrow			
Reu	\downarrow	\downarrow				\downarrow						\downarrow					\downarrow			
	Brighter	GS61	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	\downarrow	GS62	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	GS1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
Green	↑	\downarrow				\downarrow						\downarrow					\downarrow			
Green	\downarrow	\downarrow				\downarrow						\downarrow					\downarrow			
	Brighter	GS61	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0
	\downarrow	GS62	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0
	Green	GS63	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
Dive	\uparrow	\downarrow			•	\downarrow						\downarrow					\downarrow			
Blue	\downarrow	\				\downarrow						\downarrow					\downarrow			
	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1
	\downarrow	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	Blue	GS63	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
															•					

KAOHSIUNG HITACHI			Sh.			
	DATE	Nov 12 '10		7B64PS 2708-TX14D11VM1CAA-5	PAGE	8-4/5
ELECTRONICS CO.,LTD.	, , , , _	Nov.12,'10	۷o.	. 20 0 2. 00 15 11 7 11 7 11 7 11 7		0 ./0

8.5 INTERNAL PIN CONNECTION

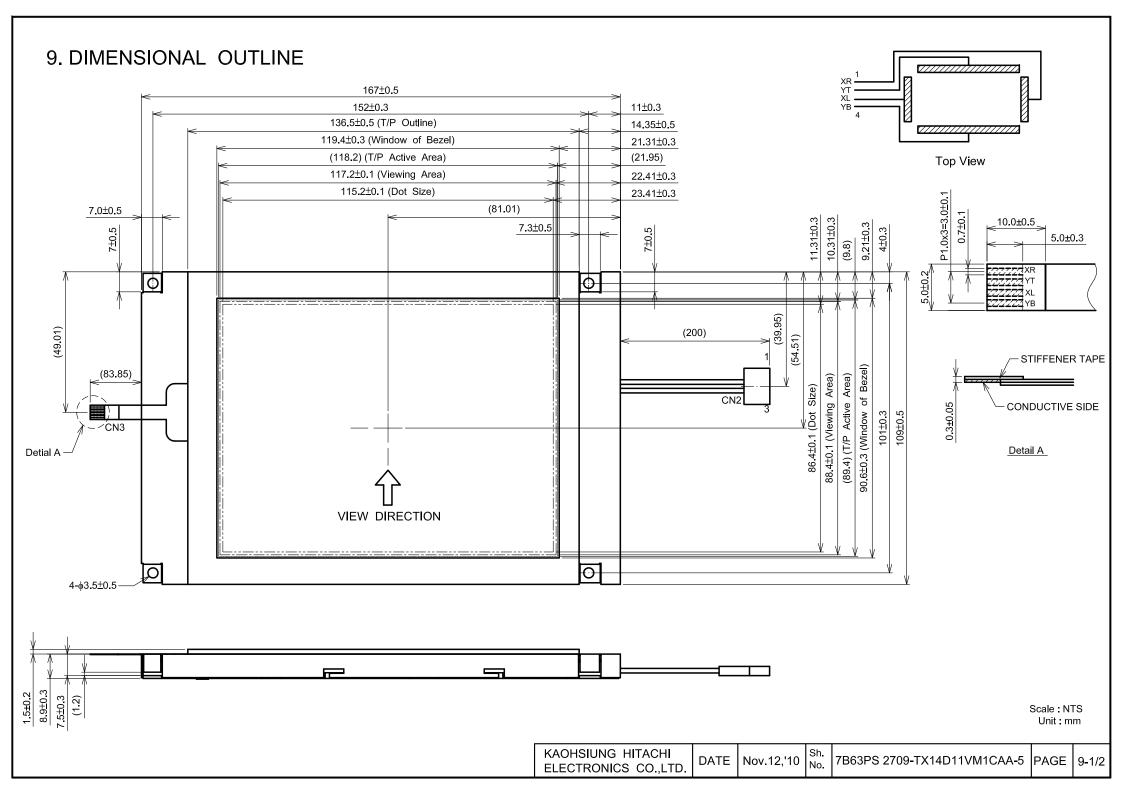
CN1 JAE : FA5B040HP1R3000(Au plating) (Suitable FPC : $t0.3\pm0.03$ mm $, 0.5\pm0.03$ mm pitch)

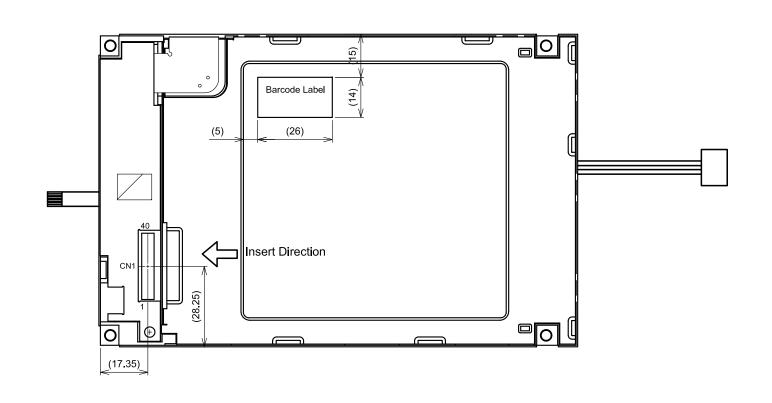
PIN No.	SIGNAL	FUNCTION
1	VDD	Power Supply for Logic
2	VDD	Power Supply for Logic
3	VDD	Power Supply for Logic
4	VDD	Power Supply for Logic
5	NC	No Connection
6	DTMG	Timing Signal for Data
7	VSS	GND
8	DCLK	Dot Clock
9	VSS	GND
10	NC	No Connection
11	VSS	GND
12	B5	
13	B4	Blue Data
14	В3	
15	VSS	GND
16	B2	
17	B1	Blue Data
18	B0	
19	VSS	GND
20	G5	
21	G4	Green Data
22	G3	
23	VSS	GND
24	G2	
25	G1	Green Data
26	G0	
27	VSS	GND
28	R5	
29	R4	Red Data
30	R3	
31	VSS	GND
32	R2	
33	R1	Red Data
34	R0	
35	(IC)	No Connection
36	VSS	GND
37	NC	No Connection
38	NC	No Connection
39	NC	No Connection
40	NC	No Connection

CN2 JST Housing: BHR-03VS-1

PIN	SIGNAL	LEVEL	FUNCTION
No.			
1	VCFL	-	Power Supply for CFL
2	NC	-	No connection
3	VSS	-	GND for CFL

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ELECTRONICS CO.,LTD.		1101112, 10	No.	75011 0 2700 17(151111111071107110110110110110110110110110	, ,,	





Scale : NTS Unit : mm

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10. APPEARANCE STANDARD

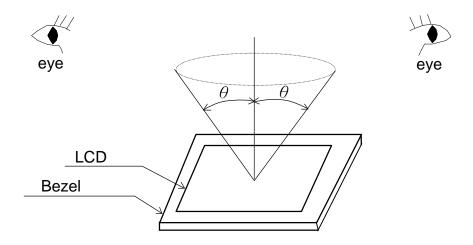
10.1 APPEARANCE INSPECTION CONDITION

Visual inspection should be done under the following condition.

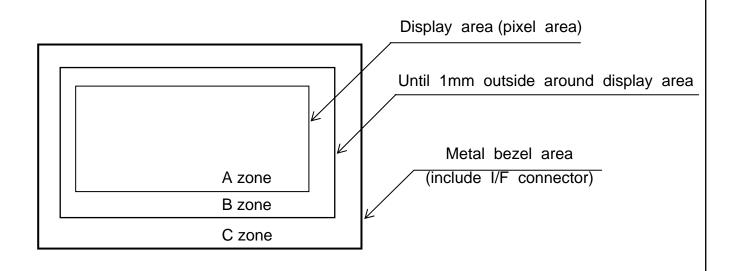
- (1) The inspection should be done in a dark room.(More than 1000(lx) and non-directive)
- (2) The distance between eyes of an inspector and the LCD module is 30cm.
- (3) The viewing zone is shown the figure.

The θ is defined as $\theta \leq$ 45° for LCM power off

 $\theta \leq 5^{\circ}$ for LCM power on



10.2 DEFINITION OF ZONE



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10.3 APPEARANCE SPECIFICATION

(1)LCD Appearance

*) If the problem related to this section occurs about this item, the responsible persons of both party (Customer and HITACHI) will discuss the matter in detail.

No.	ITEM		CRITE	RIA		APPLIED ZONE
	Scratches	Length L(mm)	Width W(mm)	Maximum number acceptable	Minimum space	
		Ignored	W≦0.02	Ignored	-	A,B
			0.02 <w≦0.04< td=""><td>10</td><td>-</td><td></td></w≦0.04<>	10	-	
		L≦20	W≦0.04	10	-	
•	Dent	•	ne is acceptable HITACHI standa	ard)		А
	Wrinkles in Polarizer	Same as abov				А
•	Bubbles	Average D(n	diameter	Maximum accep		
		D≦	0.2	Igno		٦ .
		0.2 <d≦< td=""><td>0.3</td><td>12</td><td></td><td>A</td></d≦<>	0.3	12		A
		0.3 <d≦< td=""><td>0.5</td><td>3</td><td>}</td><td></td></d≦<>	0.5	3	}	
		0.5 < D		noi	ne	
•	Stains		Filamentous (Line shape)		
	Foreign	Length	Width	Maxim	um number	
	Materials	L(mm)	W(mm)	aco	ceptable	A,B
		L≦2.0	W≦C		gnored	A,D
L	Dark Spot	L≦3.0	0.03 <w≦0< td=""><td>0.05</td><td>6</td><td></td></w≦0<>	0.05	6	
С		L≦2.5	0.05 <w≦0< td=""><td>.1</td><td>1</td><td></td></w≦0<>	.1	1	
			Round(Do	<u>_</u>		
D		Average diamete			num Space	
		D(mm)	acceptabl	e		
		D<0.2	Ignored		-	
		0.2≦D<0.3	10		0 mm	A,B
		0.3≦D<0.4	5	3	30 mm	
		0.4≦D	none		<u>-</u>	
		The total numb		nentous + Roun	d=10	
	Oalan Tana	<u> </u>	t easily are accep			Δ.
	Color Tone		y HITACHI STA	ANDARD		A
	Color Uniformity	Same as abov	<u>e</u>	D.A.		A
	Dot Defect				aximum umber	
					ceptable	
		Sparkle mode	1 dot	400	4	
			2 dots		1	
			Total (Note.(3	3)-(f))	5	A
		Black mode	1 dot	, , , , ,	5	7
			2 dots		2	7
			Total (Note.(3	3)-(f))	5	7
			Total (Note.(3		10	

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(2) CFL BACKLIGHT APPEARANCE

No.	ITEM		APPLIED ZONE			
С	Dark Spots White Spots	Average diam D(mm)	eter	Maximum	number acceptable	
F	Foreign Materials	D≦0.4			ignored	A
L.	(Spot)	0.4 <d< td=""><td></td><td></td><td>none</td><td></td></d<>			none	
	Foreign Materials (Line)	Width W(mm)		ngth nm)	Maximum number acceptable	
Α		W≤0.2	L≦	2.5	1	Α
С		VV <u>≦</u> 0.∠	2.5	<L	None	
K		0.2 <w< td=""><td></td><td>-</td><td>none</td><td></td></w<>		-	none	
L	Scratches	Width	Ler	ngth	Maximum number	
I		W(mm)	L(n	nm)	acceptable	
G		W≦0.1	,	-	ignored	_
H		0.1 < W ≦ 0.2	L≦	11.0	1	Α
Т		U. I \ VV <u>≥</u> U.Z	11.0) <l< td=""><td>None</td><td></td></l<>	None	
		0.2 <w< td=""><td></td><td>-</td><td>none</td><td></td></w<>		-	none	

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(3)Touch panel appearance

Visual inspection should be done under the following condition.

- *) The inspection should be done in a dark room. (more than 500 (lx) and non-directive)
- *) The distance between eyes of an inspector and the LCD module is 30 cm.

*) The viewing angle ≤ 60°.

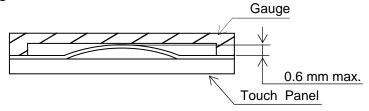
No.	ITEM		CRITERIA						
	Scratches	Width W(mm)	Ler L(m	ngth nm)	Maximum number acceptable				
		W>0.1	L≧	10	None	A,B			
		0.10 \ge W > 0.05	L<	(10	4 pcs max.				
		0.05≧W	L<	(10	Ignored				
	Foreign	Fil	amentous	(Line sha	pe)				
T O	Materials	Width W(mm)		ngth nm)	Maximum number acceptable				
U	Dark Spot	W>0.10	-		Dust (circular)	A,B			
C		0.10≧W>0.05	3<	< <u>L</u>	None				
П		0.05≧W	L≦	≦3	Ignored				
Р		Round(Dot shape)							
A N		Average diam D(mm)	eter		kimum number acceptable	A,B			
Е		D>0.35			None				
L		0.35≧D>0.2	25		6 psc max.	В			
		D≦0.25			Ignored	A,B			
	Newton Ring (Touch Panel)	Need to discuss with customer							
	Touch Panel Uncleanliness	No conspicuous dirt		А					
	Rubbing Scratch	To be judged by HIT	ACHI stand	dard		-			

(4) Glass indentation

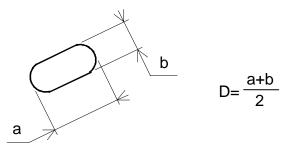
ITEM	SPECIFICATIONS						
Common Indentation	Z	X Y Z ≤5.0 ≤3.0 ≤1.1					
Corner Broken	X Z Z	X Y Z ≤3 ≤3 ≤1.1					
Proceeding Crack	None						
Other	Y≦1						

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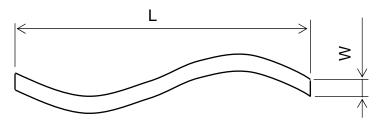
Blistering Puffiness



Note 1: Definition of average diameter (D)



Note 2: Definition of length (L) and width (W)



Note 3: Definition of dot defect

- (a) Dot Defect : Defect Area > 1/2 dot
- (b) Sparkle mode: Brightness of dot is more than 30% at Black raster.
- (c) Black mode: Brightness of dot is less than 70% at R.G.B raster.
- (d) 1 dot: Defect dot is isolated, not attached to other defect dot.
- (e) N dot: N defect dots are consecutive (fig.1).

 (N means the number of defect dots.)

)						
R	G	В	R	G	В	R	G	В
				Х				

2 dots defect included defect dot "X" is defined as follows.

Adjacent dots to defect dot "X":



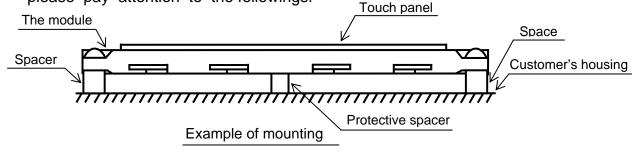
- (f) Counting definition of adjacent dots (1 set) : same as 1 dot defect.
- (g) Those wiped out easily are acceptable.

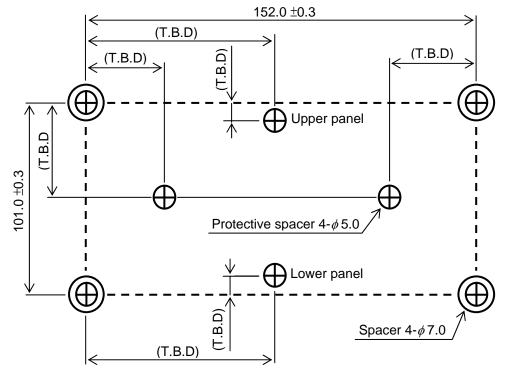
KAOHSIUNG HITACHI	DATE	Nov 12 '10	Sh.	7DC4DC 2740 TV44D44\/M44C4A F	DACE	10 5/5
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11. PRECAUTION IN DESIGN

11.1 MOUNTING PRECAUTION

Please mount the LCD Module by using mounting holes provided. While mounting please pay attention to the followings.





Unit: mm

Scale: NTS

Location of spacers

- (1) To prevent the module cover from being pressed, the distance between the module and the fitting plate, which means the length of the spacers, should be shorter than 1.0mm.
- (2) The use of protective spacers are recommend in order to protect the module from shock.
- (3) For the module to be used at upright position, the case shall have a structure where the touch panel screen does not shift with its own weight.

11.2 PRECAUTIONS AGAINST ELECTROSTATIC DISCHARGE

As this module contains C-MOS LSIs, it is not strong against electrostatic discharge. Make certain that the operator's body is connected to the ground through a list band, etc. And don't touch I/F pins directly.

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11.3 HANDLING PRECAUTIONS

- (1) Since the Touch Panel on the top, and the frame on the bottom tend to be easily damaged, they should be with full care so as not to get them touched, pushed or rubbed by a piece on glass, tweezers and anything else which are harder a pencil lead 2H.
- (2) As the adhesives used for adhering upper/lower polarizer's and frame are made of organic substances which will be deteriorated by a chemical reaction with such chemicals as acetone, toluene, ethanol and isopropyl alcohol. The following are recommended for use:

normal hexane

Please contact with us when it is necessary for you to use chemicals other than the above.

- (3) Lightly wipe to clean the dirty surface with absorbent cotton or other soft material like chamois, soaked in the recommended chemicals without scrubbing it hardly. Always wipe the surface horizontally or vertically. Never give a wipe in a circle. To prevent the display surface from damage and keep the appearance in good state, it is sufficient, in general, to wipe it with absorbent cotton.
- (4) Immediately wipe off saliva or water drop attached on the display area because it may cause deformation or faded color.
- (5) Fogy dew deposited on the surface may cause a damage, stain or dirt to the polarizer.

When you need to take out the LCD module from some place at low temperature for test, etc.

It is required to be warmed them up to temperature higher than room temperature before taking them out.

- (6) Touching the display area or I/F pins with bare hands or contaminating them are prohibited, because the stain on the display area and poor insulation between terminals are often caused by being touched with bare hands.

 (Some cosmetics are detrimental to polarizer's.)
- (7) In general, the glass is fragile so that, especially on its periphery, tends to be cracked or chipped in handling. Please not give the LCD module sharp shocks by falling, etc.
- (8) Maximum pressure to the surface must be less than 1.96×10⁴ Pa.

 And if the pressure area is less than 1cm², maximum pressure must be less than 1.96N.
- (9) Since the metal width is narrow on these locations (see page 9-1/2), please careful with handling.
- (10) Top sheets shall be cleaned gently using a soft cloth such as those used for glasses.

Hard wiping accumulated dust will leave scars on the surface even using a cloth.

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11.4 OPERATION PRECAUTION

- (1) Using a LCM module beyond its maximum ratings may result in its permanent destruction.
 - LCM module's should usually be used under recommended operating conditions shown in chapter 4. Exceeding any of these conditions may adversely affect its reliability.
- (2) Response time will be extremely delayed at lower temperature than the specified operating temperature range and on the other hand LCD's shows dark blue at higher temperature.
 - However those phenomena do not main defects of the LCD module. Those phenomena will disappear in the specified operating temperature range.
- (3) If the display area is pushed hard during operation, some display patterns will be abnormally display.
- (4) A slight dew depositing on terminals may cause electrochemical reaction which leads to terminal open circuit. Please operate the LCD module under the relative condition of 40°C 85%RH.
- (5) Resistance range: Your controller shall be set up to allow the resistance range of Touch Panel specified in our CAS.
- (6) Pointed position of Touch Panel may shift owing to a change in resistance of Touch Panel depending on the operation condition. To compensate this shift, the set shall be given a calibration function.
- (7) Input shall be made with a stylus pen (poly acetal, R0.8). Chances are very high that use of a metal piece including a ball point pen or sharp edge will impair accuracy.
- (8) The Touch Panel is an auxiliary input device. The system shall be designed to have other input device.

11.5 STORAGE

In case of storing LCD module for a long period of time (for instance, for years) for the purpose of replacement use, the following precautions necessary.

- (1) Store the LCD modules in a dark place; do not expose them to sunlight or ultraviolet rays.
- (2) Keep the temperature between 10° C and 35° C at normal humidity.
- (3) Store the LCD modules in the container which is used for shipping from us.
- (4) No articles shall be left on the surface over an extended period of time.

11.6 SAFETY

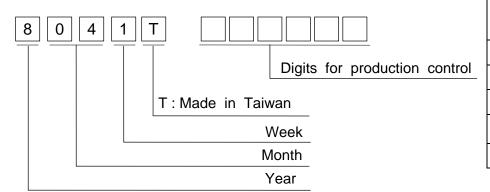
Wear finger cots or gloves whenever handling or assembling a Touch Panel its glass edges are sharp.

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12. DESIGNATION OF LOT MARK

12.1 LOT MARK

Lot mark is consisted of 5 digits for production lot and 6 digits for production control.



Year	Figure in
	lot mark
2010	0
2011	1
2012	2
2013	3
2014	4

Month	Figure in	Month	Figure in
Month	lot mark	Month	lot mark
Jan.	01	Jul.	07
Feb.	02	Aug.	08
Mar.	03	Sep.	09
Apr.	04	Oct.	10
May	05	Nov.	11
Jun.	06	Dec.	12

Week	Figure in		
(day in calendar)	lot mark		
1~ 7	1		
8~14	2		
15~21	3		
22~28	4		
29~31	5		

12.2 SERIAL No.

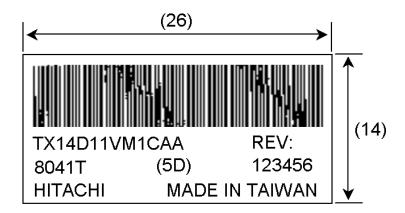
Serial No. is consisted of 6 digits number (000001~999999).

12.3 LOCATION OF LOT MARK

Label is bring attached on the back side of module.

12.4 REVISION(Rev.) CONTROL

Rev No.	ITEM
-	CN1 JAE: FA5B040HF1
Α	CN1 JAE: FA5B040HP1R3000



KAOHSIUNG HITACHI		Nov. 40.740	Sh.	7DC4DC 0740 TV44D44V/M4CAA 5	ם כר	10 1/1
ELECTRONICS CO.,LTD.	DATE	Nov.12,'10	No.	7B64PS 2712-TX14D11VM1CAA-5	PAGE	12-1/1

13. PRECAUTION FOR USE

- (1) A limit sample should be provided by the both parities on an occasion when the both parties agree to its necessity.
 Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.
- (2) On the following occasions, the handling of the problem should be decided through discussion and agreement between responsible persons of the both parties.
 - (1) When a question is arisen in the specifications.
 - (2) When a new problem is arisen which is not specified in this specifications.
 - (3) When an inspection specifications change or operating condition change by customer is reported to HITACHI, and some problem is arisen in the specification due to the change.
 - (4) When a new problem is arisen at the customer's operating set for sample evaluation.
- (3) Regarding the treatment for maintenance and repairing, both parties will discuss it in six months later after latest delivery of this product.

The precaution that should be observed when handling LCM have been explained above.

If any points are unclear or if you have any requests, please contact with HITACHI.