

PROTECTION PRODUCTS

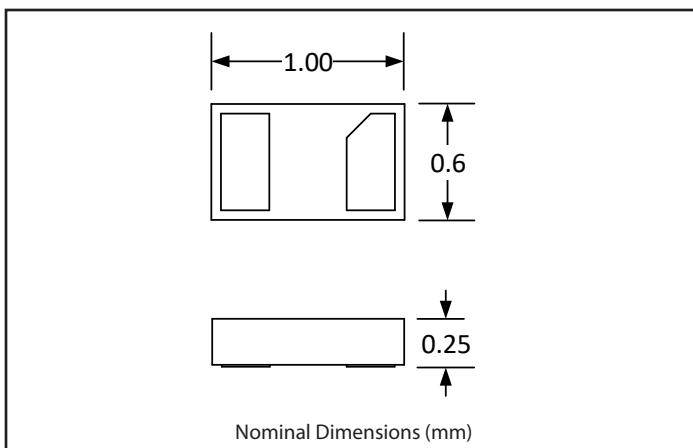
Description

μClamp® series of TVS arrays are designed to protect sensitive electronics from damage or latch-up due to ESD. They feature large cross-sectional area junctions for conducting high transient currents. They offer desirable characteristics for board level protection including fast response time, low operating and clamping voltage, and no device degradation.

The μClamp5501ZV features robust surge capability (60A, $t_p = 8/20\mu s$), low clamping voltage, and low dynamic resistance (0.03Ω typical). They may be used to meet the ESD immunity requirements of IEC 61000-4-2 ($\pm 30kV$ air, $\pm 30kV$ contact discharge). Each device will protect one unidirectional line operating at 5.5 volts.

μClamp5501ZV is in a DFN 1.0 x 0.6 x 0.25mm 2 Lead package, measuring 1.0 x 0.6 x 0.25mm. Leads are spaced at a pitch of 0.65mm and are finished with lead-free NiAu. The combination of small size and high ESD and surge capability makes them ideal for use in applications such as cellular phones, battery protection, and VBUS protection.

Package Dimension



Features

- High ESD withstand Voltage: $\pm 30kV$ (Contact) and $\pm 30kV$ (Air) per IEC 61000-4-2
- High peak pulse current capability: 60A ($t_p = 8/20\mu s$)
- Ultra-small package (1.0 x 0.6 x 0.25mm)
- Protects one unidirectional I/O or power line
- Low ESD clamping voltage
- Low dynamic resistance: 0.03Ω Typical
- Working voltage: +5.5V
- Solid-state silicon-avalanche technology

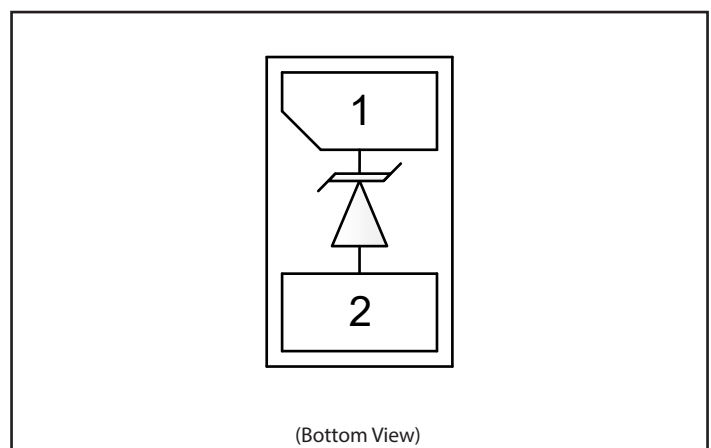
Mechanical Characteristics

- Package: DFN 1.0 x 0.6 x 0.25mm 2 Lead
- Pb-Free, Halogen Free, RoHS/WEEE Compliant
- Molding compound flammability rating: UL 94V-0
- Lead Finish: NiAu
- Marking: Marking code
- Packaging: Tape and Reel

Applications

- Cellular Handsets & Accessories
- Battery Protection
- Notebooks & Handhelds
- USB VBus
- Digital Lines

Schematic & Pin Configuration



Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power ($t_p = 8/20\mu s$)	P_{PK}	600	W
Peak Pulse Current ($t_p = 8/20\mu s$)	I_{PP}	60	A
ESD per IEC 61000-4-2 (Air) ⁽¹⁾ ESD per IEC 61000-4-2 (Contact) ⁽¹⁾	V_{ESD}	± 30 ± 30	kV
Operating Temperature	T_{OP}	-55 to +85	°C
Storage Temperature	T_{STG}	-55 to +150	°C

Electrical Characteristics (T=25°C unless otherwise specified)

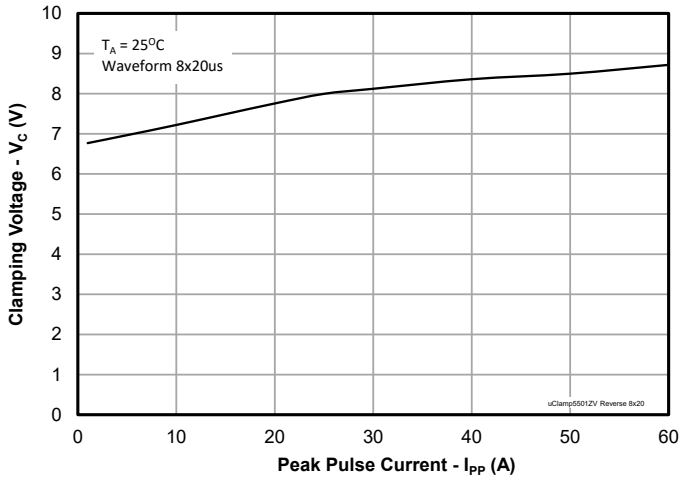
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Reverse Stand-Off Voltage	V_{RWM}	Pin 1 to 2			5.5	V
Reverse Breakdown Voltage	V_{BR}	$I_t = 1mA$, Pin 1 to 2	5.8	6.4	7.5	V
Reverse Leakage Current	I_R	$V_{RWM} = 5.5V$, Pin 1 to 2		50	500	nA
Clamping Voltage	V_C	$t_p = 8/20\mu s$, Pin 1 to 2	$I_{PP} = 10A$	7.2	8	V
			$I_{PP} = 40A$	8.4	9.5	
			$I_{PP} = 60A$	8.8	10	
Forward Voltage	V_F	$t_p = 8/20\mu s$, Pin 2 to 1	$I_{PP} = 10A$	1.2	2	V
			$I_{PP} = 40A$	2	2.5	
			$I_{PP} = 60A$	2.5	3	
ESD Clamping Voltage ²	V_C	$tp = 0.2/100ns$, Pin 1 to 2	$I = 4A$	6.7		V
			$I = 16A$	7.1		
Dynamic Resistance ^{2,3}	R_{DYN}	$tp = 0.2/100ns$, Pin 1 to 2		0.03		Ω
Junction Capacitance	C_J	$V_R = 0V$, $f = 1MHz$		445	500	pF

Notes:

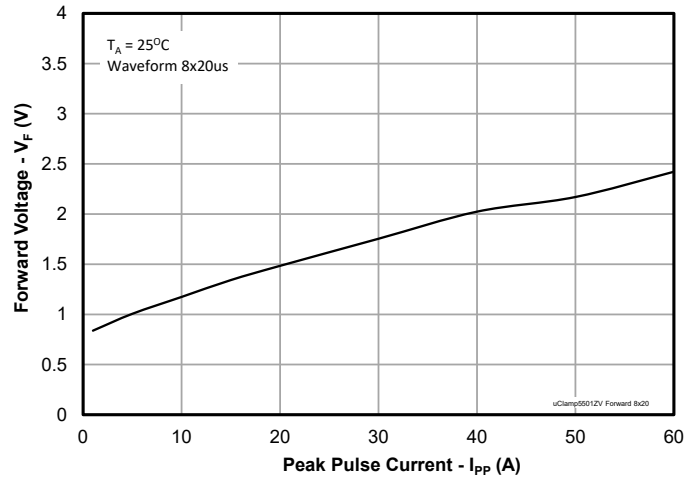
- ESD gun return path connected to ESD ground plane.
- Transmission Line Pulse Test (TLP) Settings: $t_p = 100ns$, $t_r = 0.2ns$, I_{TLP} and V_{TLP} averaging window: $t_1 = 70ns$ to $t_2 = 90ns$
- Dynamic resistance calculated from $I_{TLP} = 4A$ to $I_{TLP} = 16A$

Typical Characteristics

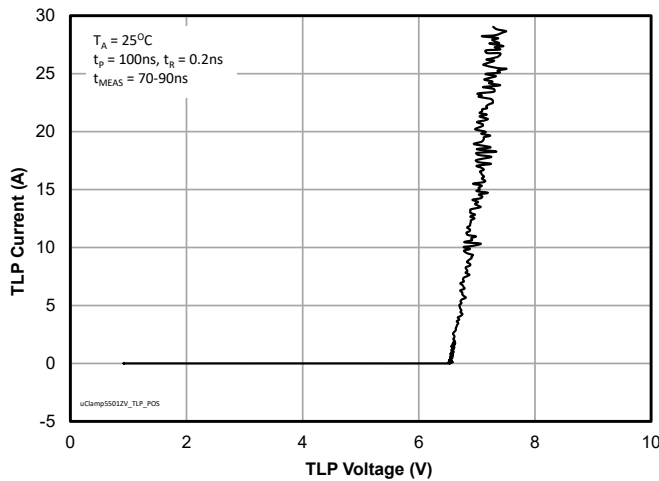
Clamping Voltage vs. Peak Pulse Current ($t_p=8/20\mu s$)



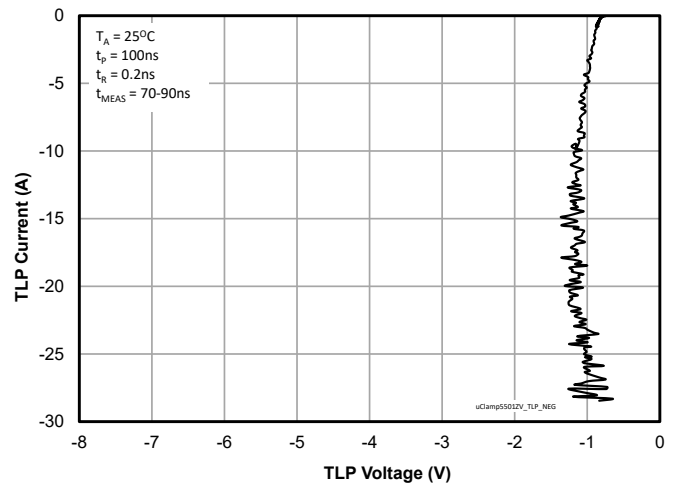
Forward Voltage vs. Peak Pulse Current ($t_p=8/20\mu s$)



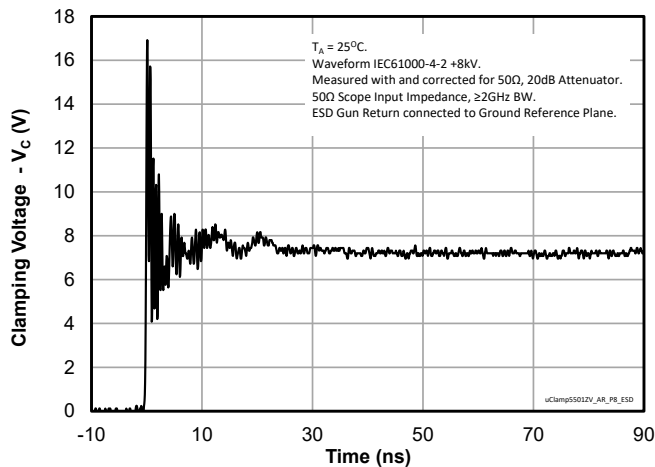
TLP Characteristic (Positive Pulse)



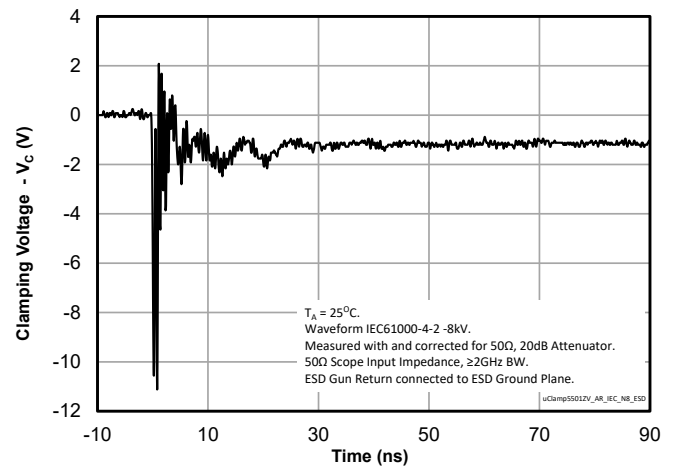
TLP Characteristic (Negative Pulse)



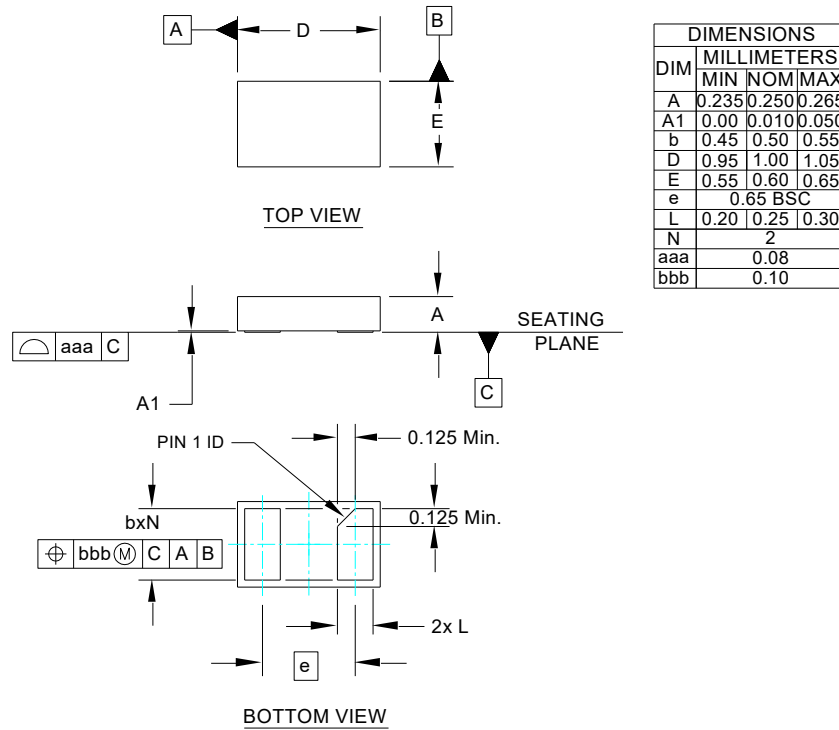
ESD Clamping (+8kV Contact per IEC 61000-4-2)



ESD Clamping (-8kV Contact per IEC 61000-4-2)



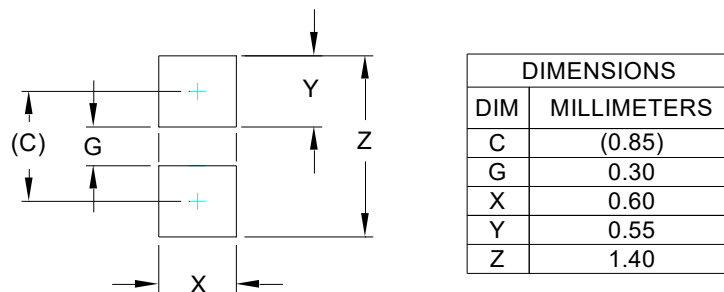
Outline Drawing - DFN 1.0 x 0.6 x 0.25mm 2 Lead



NOTES:

1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).

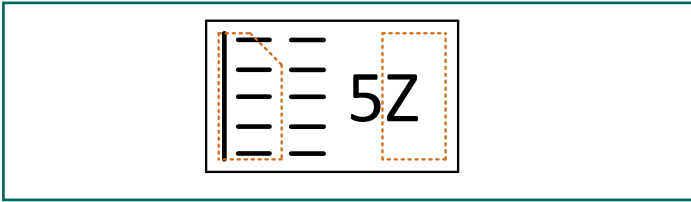
Land Pattern - DFN 1.0 x 0.6 x 0.25mm 2 Lead



NOTES:

1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
2. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.

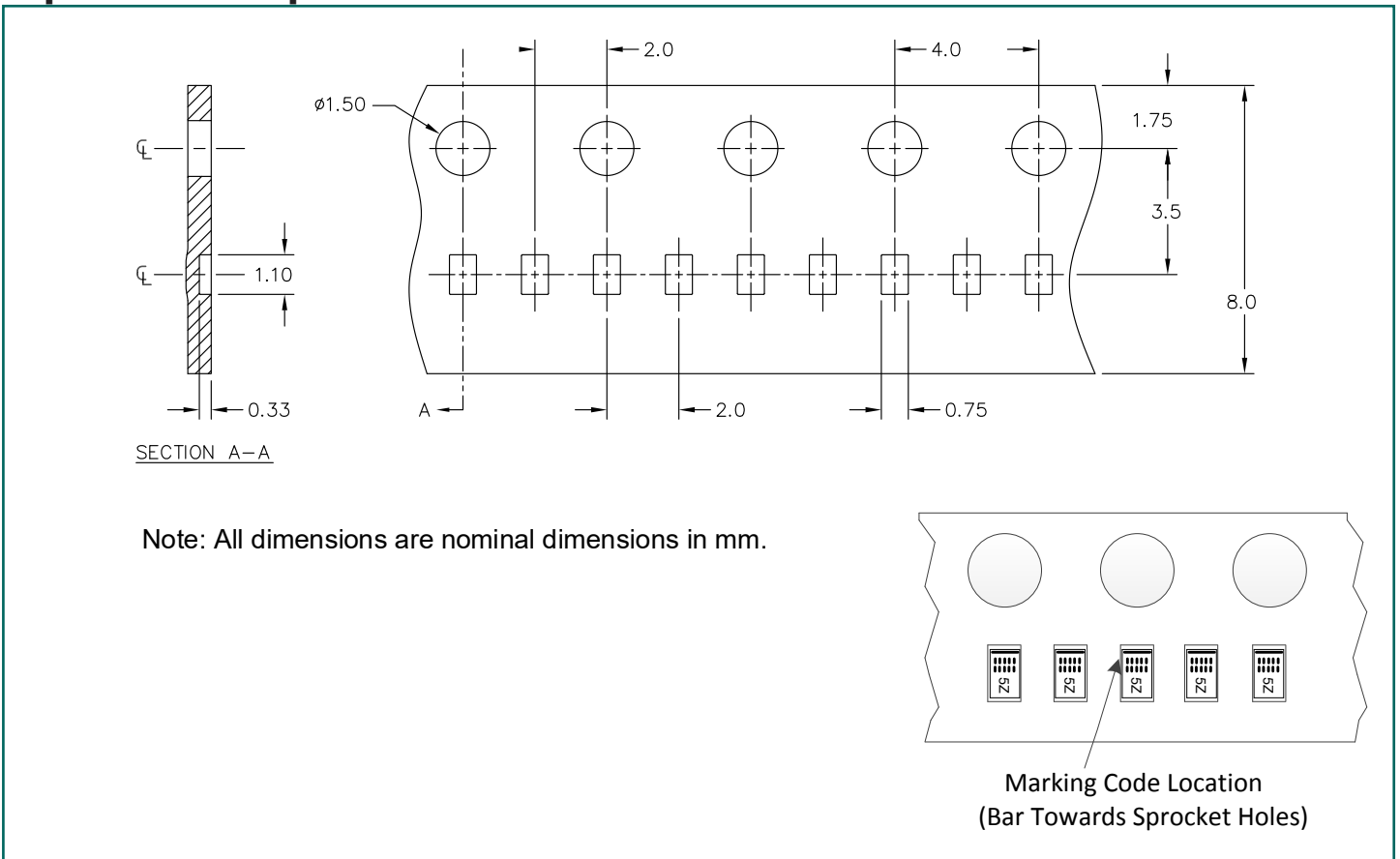
Marking Code



Notes:

- 1. Marking will also include line matrix date code.
- 2. Bar indicates Pin 1 location.

Tape and Reel Specification



Ordering Information

Part Number	Qty per Reel	Reel Size
μClamp5501ZVTFT	15,000	7"



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