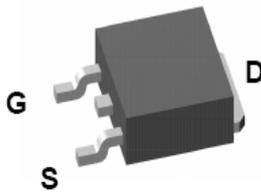


# P2206BD

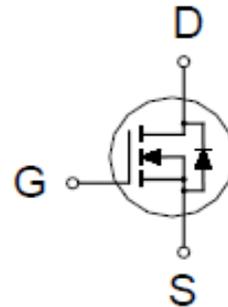
## N-Channel Enhancement Mode MOSFET

### PRODUCT SUMMARY

| $V_{(BR)DSS}$ | $R_{DS(ON)}$                    | $I_D$ |
|---------------|---------------------------------|-------|
| 60V           | 22.5m $\Omega$ @ $V_{GS} = 10V$ | 32A   |



TO-252



### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

| PARAMETERS/TEST CONDITIONS           |                                   | SYMBOL           | LIMITS     | UNITS            |    |
|--------------------------------------|-----------------------------------|------------------|------------|------------------|----|
| Drain-Source Voltage                 |                                   | $V_{DS}$         | 60         | V                |    |
| Gate-Source Voltage                  |                                   | $V_{GS}$         | $\pm 20$   |                  |    |
| Continuous Drain Current             | $T_C = 25\text{ }^\circ\text{C}$  | $I_D$            | 32         | A                |    |
|                                      | $T_C = 100\text{ }^\circ\text{C}$ |                  | 20         |                  |    |
| Pulsed Drain Current <sup>1</sup>    |                                   | $I_{DM}$         | 100        |                  |    |
| Avalanche Current                    |                                   | $I_{AS}$         | 26         |                  |    |
| Avalanche Energy                     |                                   | $L=0.1\text{mH}$ | $E_{AS}$   | 33.8             | mJ |
| Power Dissipation                    | $T_C = 25\text{ }^\circ\text{C}$  | $P_D$            | 50         | W                |    |
|                                      | $T_C = 100\text{ }^\circ\text{C}$ |                  | 20         |                  |    |
| Junction & Storage Temperature Range |                                   | $T_j, T_{stg}$   | -55 to 150 | $^\circ\text{C}$ |    |

### THERMAL RESISTANCE RATINGS

| THERMAL RESISTANCE  | SYMBOL          | TYPICAL | MAXIMUM | UNITS                       |
|---------------------|-----------------|---------|---------|-----------------------------|
| Junction-to-Case    | $R_{\theta JC}$ |         | 2.5     | $^\circ\text{C} / \text{W}$ |
| Junction-to-Ambient | $R_{\theta JA}$ |         | 62.5    |                             |

<sup>1</sup>Pulse width limited by maximum junction temperature.

## P2206BD

### N-Channel Enhancement Mode MOSFET

#### ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25 °C, Unless Otherwise Noted)

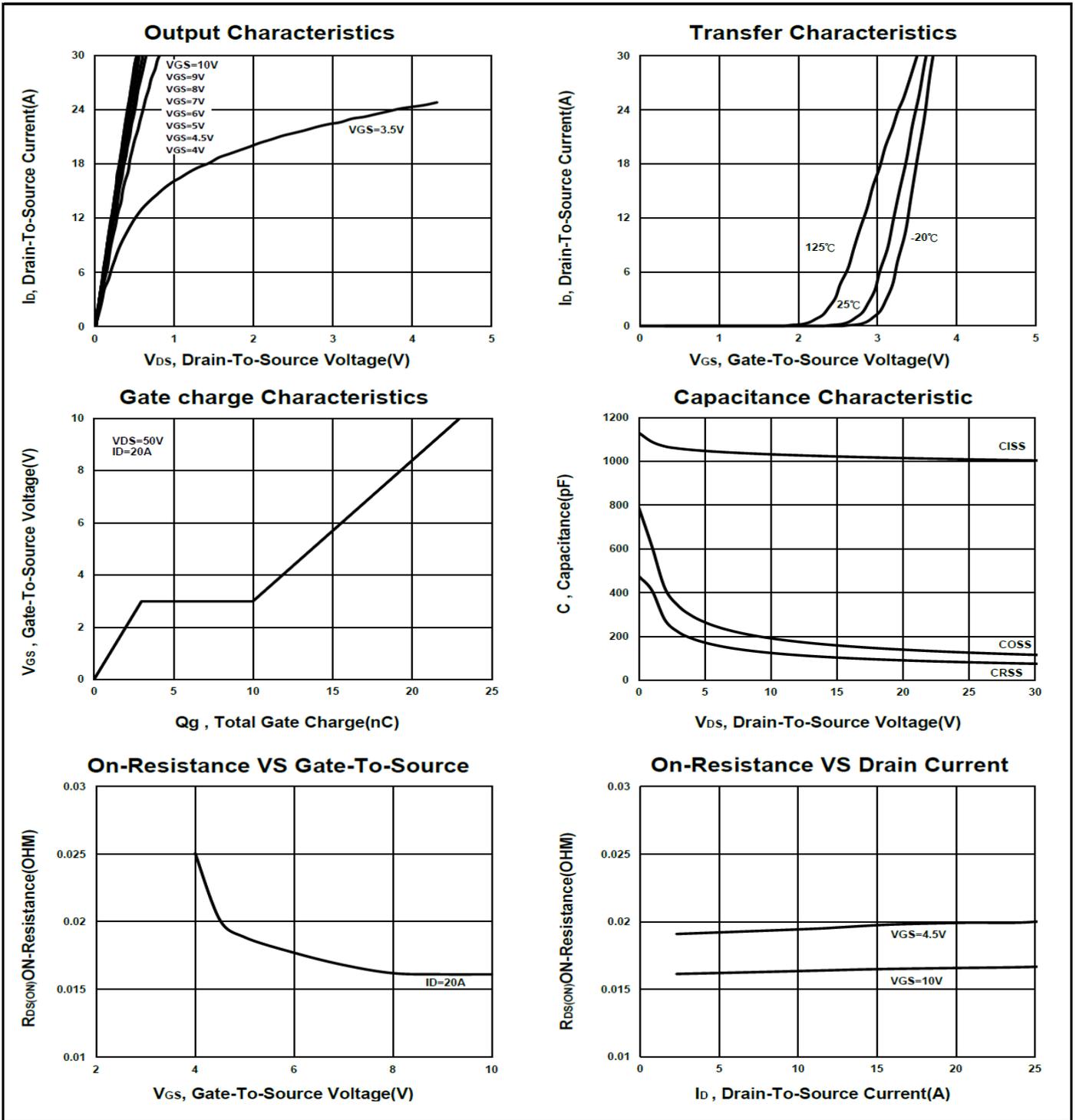
| PARAMETER   | SYMBOL               | TEST CONDITIONS   | LIMITS                                      |      |      | UNITS |    |
|---|----------------------|---|---|------|------|-------|----|
|   |                      |   | MIN   | TYP  | MAX  |       |    |
| <b>STATIC</b>   |                      |   |   |      |      |       |    |
| Drain-Source Breakdown Voltage  | V <sub>(BR)DSS</sub> | V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA  | 60  |      |      | V     |    |
| Gate Threshold Voltage  | V <sub>GS(th)</sub>  | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA                                    | 1.3   | 1.75 | 2.3  |       |    |
| Gate-Body Leakage   | I <sub>GSS</sub>     | V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V  |   |      | ±100 | nA    |    |
| Zero Gate Voltage Drain Current   | I <sub>DSS</sub>     | V <sub>DS</sub> = 48V, V <sub>GS</sub> = 0V   |   |      | 1    | μA    |    |
|   |                      | V <sub>DS</sub> = 40V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 125°C                           |   |      | 10   |       |    |
| Drain-Source On-State Resistance <sup>1</sup>                                 | R <sub>DS(ON)</sub>  | V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A   |   | 17   | 22.5 | mΩ    |    |
|   |                      | V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 12A  |   | 19   | 30   |       |    |
| Forward Transconductance <sup>1</sup>   | g <sub>fs</sub>      | V <sub>DS</sub> = 5V, I <sub>D</sub> = 20A  |   | 40   |      | S     |    |
| <b>DYNAMIC</b>  |                      |   |   |      |      |       |    |
| Input Capacitance   | C <sub>iss</sub>     | V <sub>GS</sub> = 0V, V <sub>DS</sub> = 25V, f = 1MHz   |   | 1016 |      | pF    |    |
| Output Capacitance  | C <sub>oss</sub>     |   |   | 125  |      |       |    |
| Reverse Transfer Capacitance  | C <sub>rss</sub>     |   |   | 81   |      |       |    |
| Gate Resistance   | R <sub>g</sub>       | V <sub>GS</sub> = 0V, V <sub>DS</sub> = 0V, f = 1MHz  |   | 1    |      | Ω     |    |
| Total Gate Charge <sup>2</sup>  | Q <sub>g</sub>       | V <sub>GS</sub> = 10V   | V <sub>DS</sub> = 30V, I <sub>D</sub> = 20V |      | 23.2 | nC    |    |
|   |                      | V <sub>GS</sub> = 4.5V  |   |      | 13   |       |    |
| Gate-Source Charge <sup>2</sup>   | Q <sub>gs</sub>      |   |   | 3.2  |      |       |    |
| Gate-Drain Charge <sup>2</sup>  | Q <sub>gd</sub>      |   |   | 7.4  |      |       |    |
| Turn-On Delay Time <sup>2</sup>   | t <sub>d(on)</sub>   | V <sub>DS</sub> = 30V ,<br>I <sub>D</sub> ≅ 20A, V <sub>GS</sub> = 10V, R <sub>GEN</sub> = 6Ω |   |      | 38   |       | nS |
| Rise Time <sup>2</sup>  | t <sub>r</sub>       |   |   |      | 24   |       |    |
| Turn-Off Delay Time <sup>2</sup>  | t <sub>d(off)</sub>  |   |   | 102  |      |       |    |
| Fall Time <sup>2</sup>  | t <sub>f</sub>       |   |   | 35   |      |       |    |
| <b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T<sub>J</sub> = 25 °C)</b> |                      |   |   |      |      |       |    |
| Continuous Current <sup>3</sup>   | I <sub>S</sub>       |   |   |      | 32   | A     |    |
| Forward Voltage <sup>1</sup>  | V <sub>SD</sub>      | I <sub>F</sub> = 20A, V <sub>GS</sub> = 0V  |   |      | 1.3  | V     |    |
| Reverse Recovery Time   | t <sub>rr</sub>      | I <sub>F</sub> = 20A, di <sub>F</sub> /dt = 100A /μs  |   | 22   |      | nS    |    |
| Reverse Recovery Charge   | Q <sub>rr</sub>      |   |   |      | 15   |       | nC |

<sup>1</sup>Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

<sup>2</sup>Independent of operating temperature.

# P2206BD

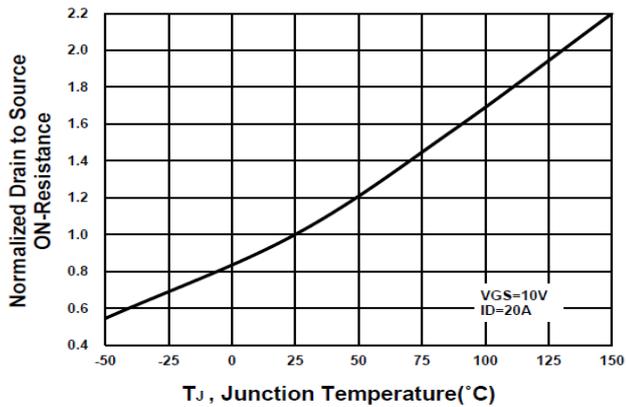
## N-Channel Enhancement Mode MOSFET



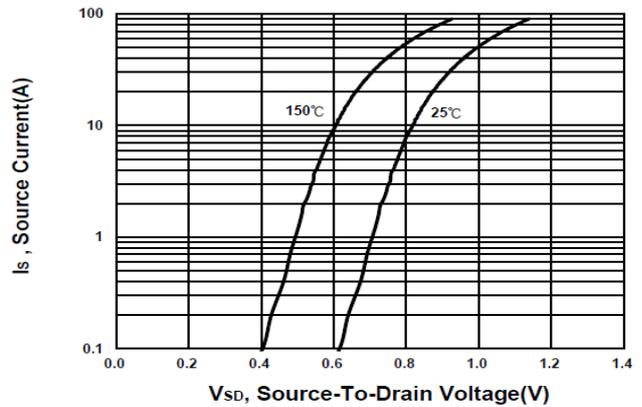
# P2206BD

## N-Channel Enhancement Mode MOSFET

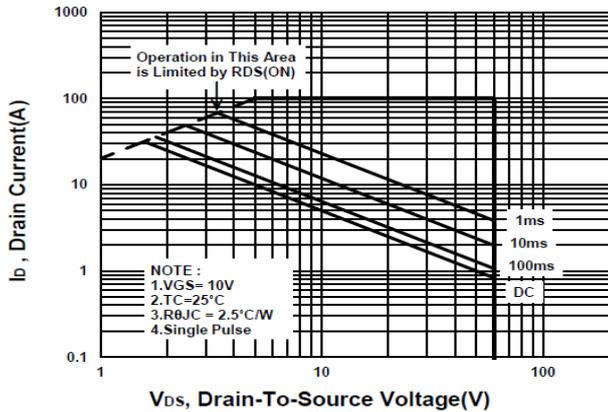
**On-Resistance VS Temperature**



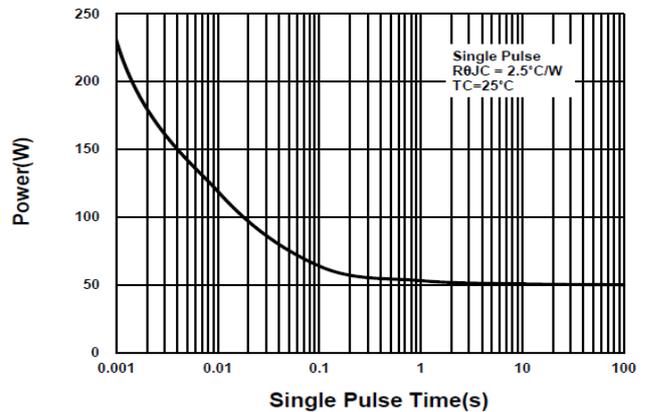
**Source-Drain Diode Forward Voltage**



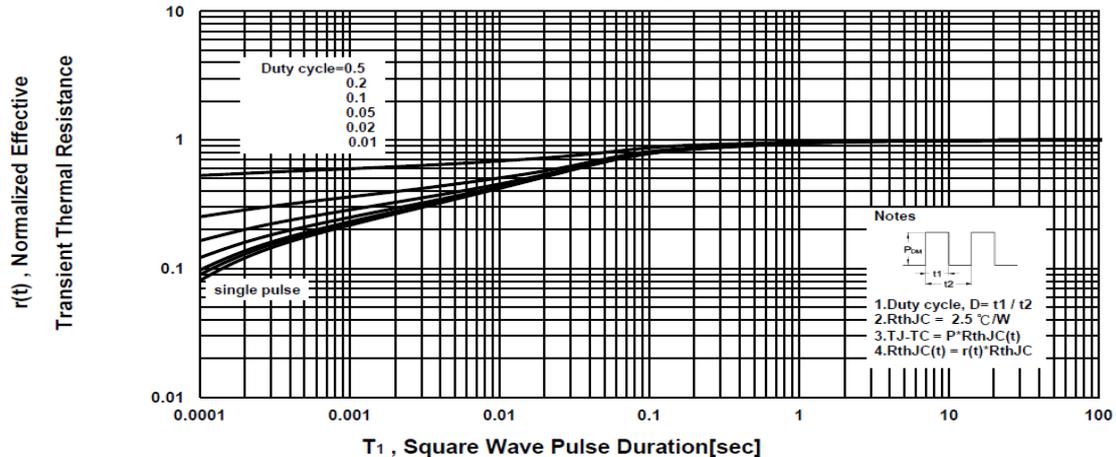
**Safe Operating Area**



**Single Pulse Maximum Power Dissipation**



**Transient Thermal Response Curve**



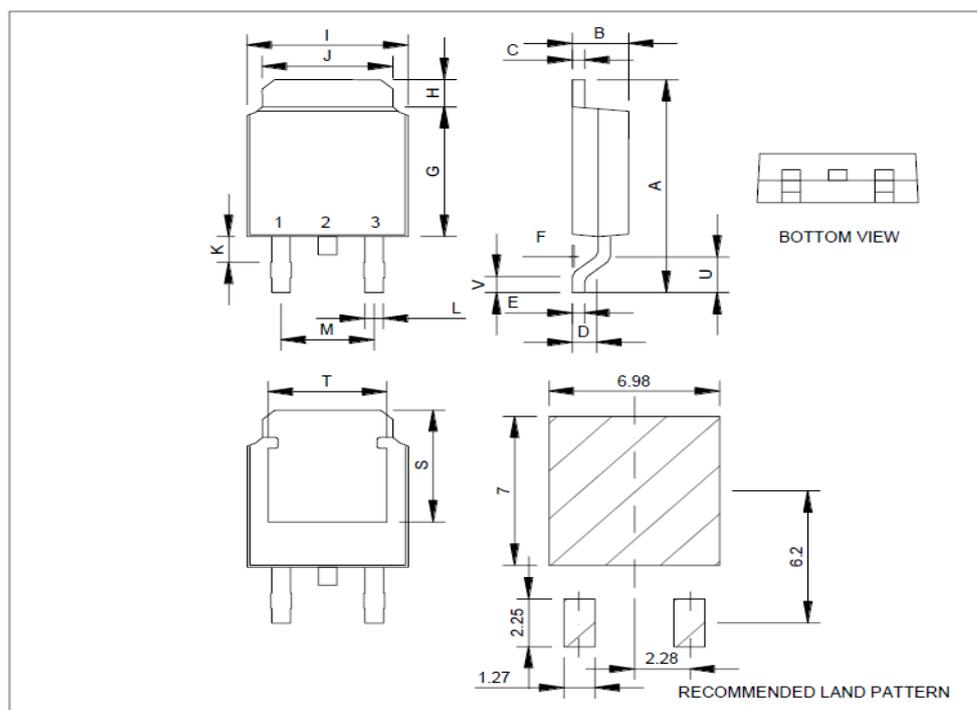
# P2206BD

## N-Channel Enhancement Mode MOSFET

### Package Dimension

### TO-252 (DPAK) MECHANICAL DATA

| Dimension | mm   |      |       | Dimension | mm   |      |      |
|-----------|------|------|-------|-----------|------|------|------|
|           | Min. | Typ. | Max.  |           | Min. | Typ. | Max. |
| A         | 8.9  | 10   | 10.41 | J         | 4.8  |      | 5.64 |
| B         | 2.1  | 2.2  | 2.4   | K         | 0.15 |      | 1.1  |
| C         | 0.4  | 0.5  | 0.61  | L         | 0.4  | 0.76 | 0.89 |
| D         | 0.82 | 1.2  | 1.5   | M         | 4.2  | 4.58 | 5    |
| E         | 0.4  | 0.5  | 0.61  | S         | 4.9  | 5.1  | 5.3  |
| F         | 0    |      | 0.2   | T         | 4.6  | 4.75 | 5.44 |
| G         | 5.3  | 6.1  | 6.3   | U         | 1.4  |      | 1.78 |
| H         | 0.9  |      | 1.7   | V         | 0.55 | 1.25 | 1.7  |
| I         | 6.3  | 6.5  | 6.8   |           |      |      |      |

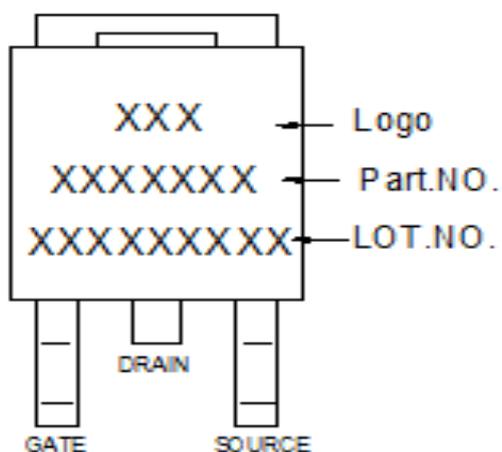


\*因为各家封装模具不同而外观略有所差异，不影响电性及Layout。

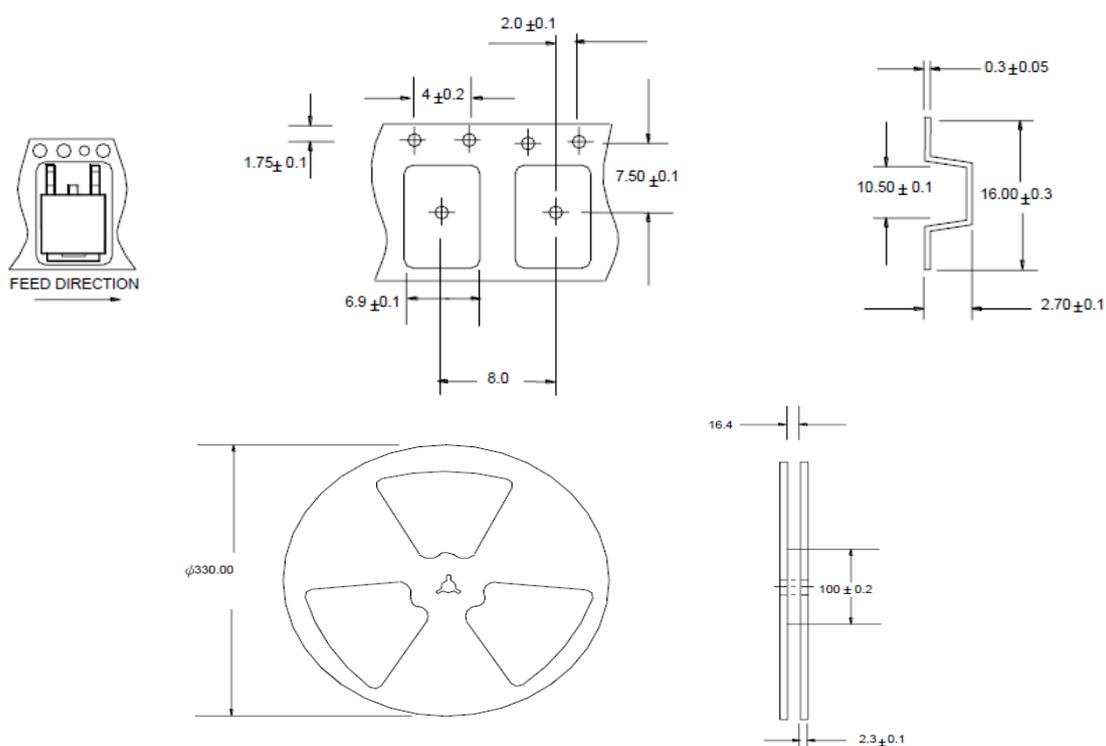
# P2206BD

## N-Channel Enhancement Mode MOSFET

### A. Marking Information



### B. Tape & Reel Information: 2500pcs/Reel



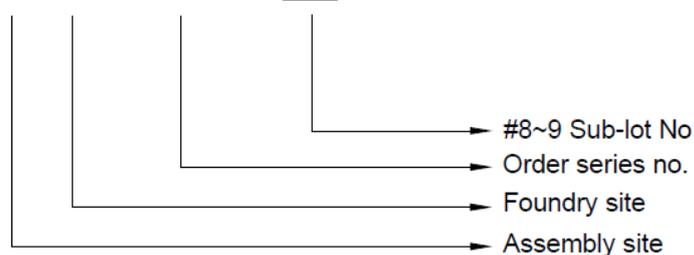
## **P2206BD**

### **N-Channel Enhancement Mode MOSFET**

#### C. Lot.No. & Date Code rule

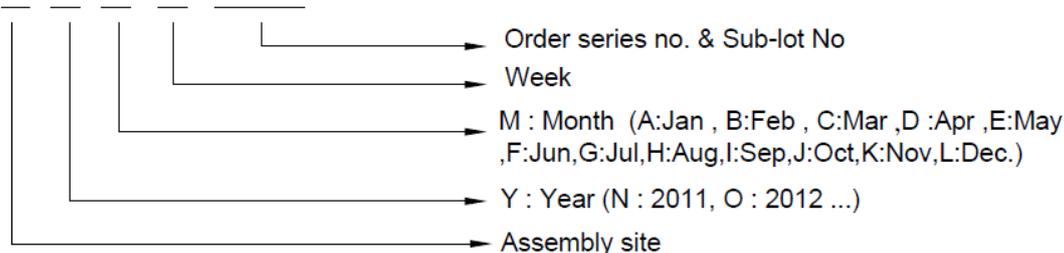
##### 1.LOT.NO.

M N 15M21 03



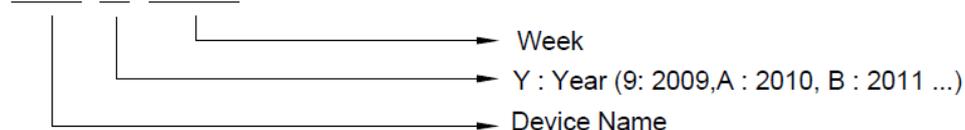
##### 2.Date Code

D Y M X XXX



##### 3.Date Code (for Small package)

XX Y WW



## P2206BD

### N-Channel Enhancement Mode MOSFET

#### D.Label rule

标签内容(Label content)



|    |                    |   |
|----|--------------------|---|
| 1  | Label Size         | 30 * 90 mm  |
| 2  | Font style         | Times New Roman or Arial<br>(或可区分英文”0”和数字”0”，”G和”Q”的字型即可)   |
| 3  | Great Power        | Height: 4 mm  |
| 4  | Package            | Height: 2 mm  |
| 5  | Date               | Height: 2 mm Shipping date: YYYY/MM/DD, ex. 2008/09/12  |
| 6  | Device             | Height: 3 mm (Max: 16 Digit)  |
| 7  | Lot                | Height: 3 mm (Max: 9 Digit) Sub lot   |
| 8  | D/C                | Height: 3 mm (Max: 7 Digit)   |
| 9  | QTY                | Height: 3 mm (Max: 6 Digit) Thousand mark is no needed  |
| 10 | Pb Free label      |  Diameter: 1 cm bottom color: Green<br>Font color: Black Font style: Arial                                 |
| 11 | Halogen Free label |  Diameter: 1 cm bottom color: Green<br>Font color: Black Font style: Arial                                 |
| 12 | Scan info          | Device / Lot / D/C / QTY , Insert “ / “ between every parts.<br>for example: P3055LDG/G12345601/GGG2301/2000<br>DPI (Dots per inch): Over 300 dpi<br>Code : Code 128<br>Height: 6 mm at least |