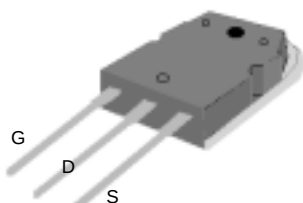


N-channel Enhancement-mode Power MOSFET

PRODUCT SUMMARY

BV_{DSS}	900V
$R_{DS(ON)}$	1.4 Ω
I_D	7.6A

 **Pb-free; RoHS-compliant TO-247**



TO-247 (suffix W)

DESCRIPTION

The SSM09N90CGW achieves fast switching performance with low gate charge without a complex drive circuit. It is suitable for high voltage applications such as AC/DC converters and offline power supplies.

The SSM09N90CGW is in a TO-247 (TO-3P) package, which is widely used for commercial and industrial applications, where the greater pin spacing is needed to meet safety specifications. The through-hole package is suitable for vertical mounting, where a small footprint is required on the board, and/or an external heatsink is to be attached.

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Units
V_{DS}	Drain-source voltage	900	V
V_{GS}	Gate-source voltage	± 30	V
I_D	Continuous drain current, $T_C = 25^\circ\text{C}$	7.6	A
	$T_C = 100^\circ\text{C}$	4.8	A
I_{DM}	Pulsed drain current ¹	25	A
P_D	Total power dissipation, $T_C = 25^\circ\text{C}$	208	W
	Linear derating factor	1.6	W/ $^\circ\text{C}$
E_{AS}	Single pulse avalanche energy ³	120	mJ
I_{AS}	Avalanche current	6	A
T_{STG}	Storage temperature range	-55 to 150	$^\circ\text{C}$
T_J	Operating junction temperature range	-55 to 150	$^\circ\text{C}$

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Units
$R_{\theta JC}$	Maximum thermal resistance, junction-case	0.6	$^\circ\text{C/W}$
$R_{\theta JA}$	Maximum thermal resistance, junction-ambient	40	$^\circ\text{C/W}$

Notes:

1. Pulse width must be limited to avoid exceeding the safe operating area.
2. Pulse width <300us, duty cycle <2%.
3. Starting $T_J=25^\circ\text{C}$, $V_{DD}=50\text{V}$, $L=6.8\text{mH}$, $R_G=25\Omega$, $I_{AS}=6\text{A}$.

ELECTRICAL CHARACTERISTICS (at $T_j = 25^\circ\text{C}$, unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-source breakdown voltage	$V_{GS}=0V, I_D=1mA$	900	-	-	V
$\Delta BV_{DSS}/\Delta T_j$	Breakdown voltage temperature coefficient	Reference to 25°C , $I_D=1mA$	-	0.74	-	V/ $^\circ\text{C}$
$R_{DS(ON)}$	Static drain-source on-resistance	$V_{GS}=10V, I_D=3.6A$	-	1.25	1.4	Ω
$V_{GS(th)}$	Gate threshold voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	2	-	4	V
g_{fs}	Forward transconductance	$V_{DS}=10V, I_D=3.6A$	-	3.6	-	S
I_{DSS}	Drain-source leakage current	$V_{DS}=900V, V_{GS}=0V$	-	-	10	μA
		$V_{DS}=720V, V_{GS}=0V, T_j = 125^\circ\text{C}$	-	-	100	μA
I_{GSS}	Gate-source leakage current	$V_{GS}=\pm 30V$	-	-	± 100	nA
Q_g	Total gate charge ²	$I_D=7.2A$	-	50.7	80	nC
Q_{gs}	Gate-source charge	$V_{DS}=540V$	-	12	-	nC
Q_{gd}	Gate-drain ("Miller") charge	$V_{GS}=10V$	-	16	-	nC
$t_{d(on)}$	Turn-on delay time ²	$V_{DS}=450V$	-	20	-	ns
t_r	Rise time	$I_D=7.2A$	-	16	-	ns
$t_{d(off)}$	Turn-off delay time	$R_G=6.8\Omega, V_{GS}=10V$	-	65	-	ns
t_f	Fall time	$R_D=62.5\Omega$	-	27	-	ns
C_{iss}	Input capacitance	$V_{GS}=0V$	-	3097	5000	pF
C_{oss}	Output capacitance	$V_{DS}=15V$	-	516	-	pF
C_{rss}	Reverse transfer capacitance	$f=1.0MHz$	-	19	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward voltage ²	$I_S=7.2A, V_{GS}=0V$	-	-	1.5	V
t_{rr}	Reverse-recovery time	$I_S=7.2A, V_{GS}=0V,$	-	673	-	ns
Q_{rr}	Reverse-recovery charge	$dI/dt=100A/\mu s$	-	9.6	-	μC

Notes:

1.Pulse width must be limited to avoid exceeding the maximum junction temperature of 150°C .

2.Pulse width $<300\mu s$, duty cycle $<2\%$.

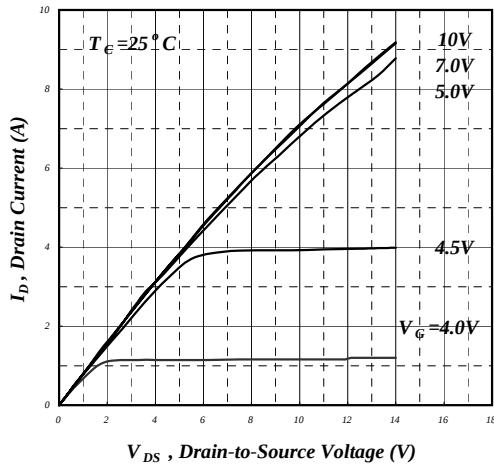


Fig 1. Typical Output Characteristics

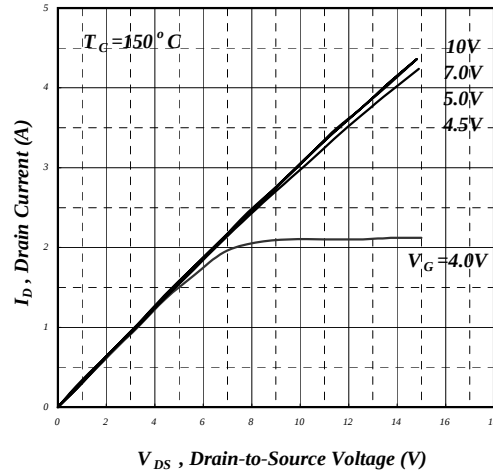


Fig 2. Typical Output Characteristics

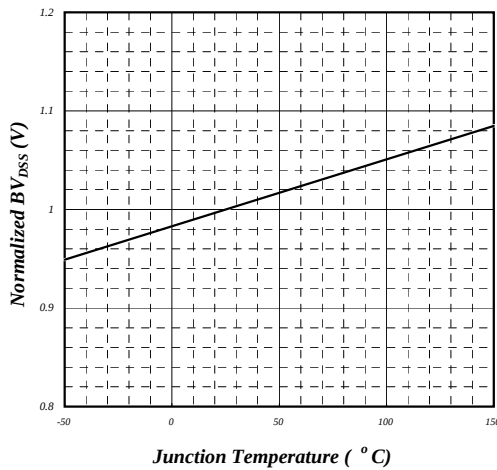
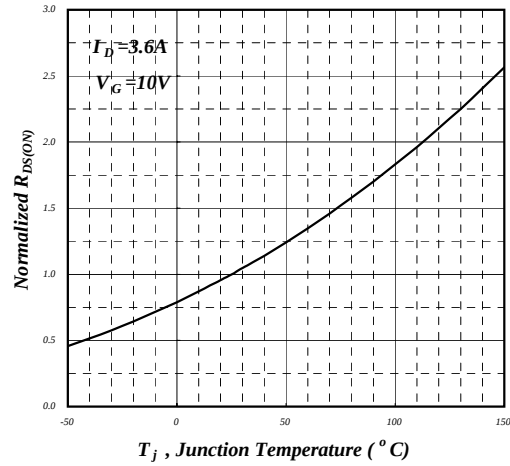

 Fig 3. Normalized BVD_{SS} vs. Junction Temperature


Fig 4. Normalized On-Resistance vs. Junction Temperature

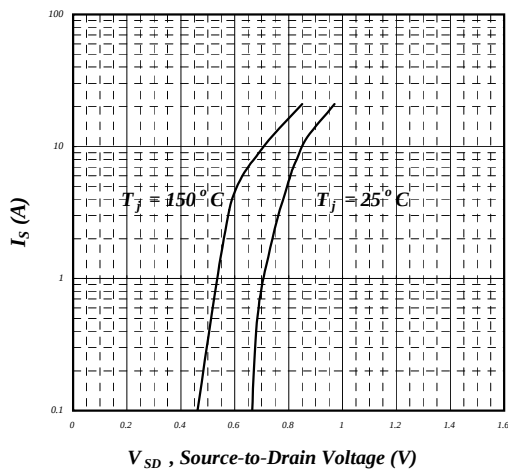


Fig 5. Forward Characteristic of Reverse Diode

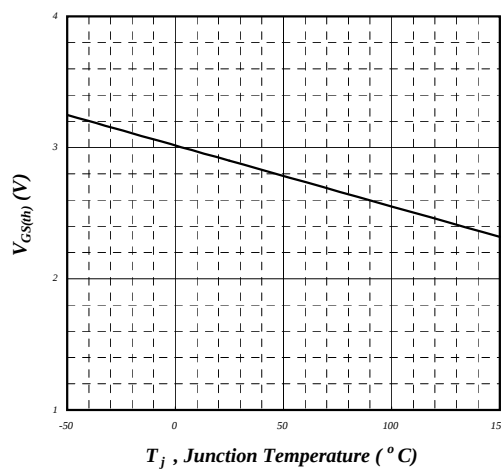


Fig 6. Gate Threshold Voltage vs. Junction Temperature

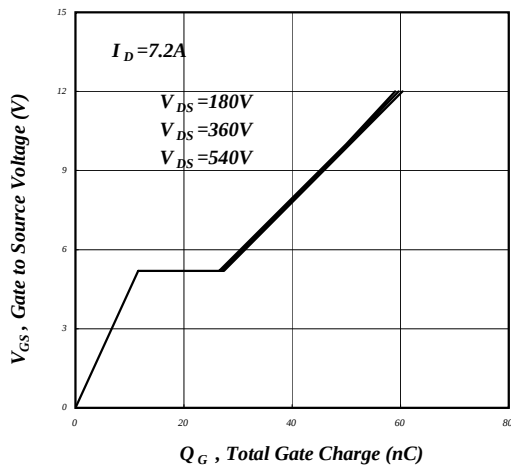


Fig 7. Gate Charge Characteristics

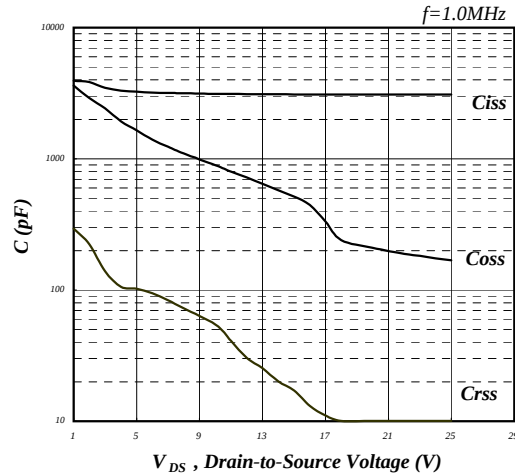


Fig 8. Typical Capacitance Characteristics

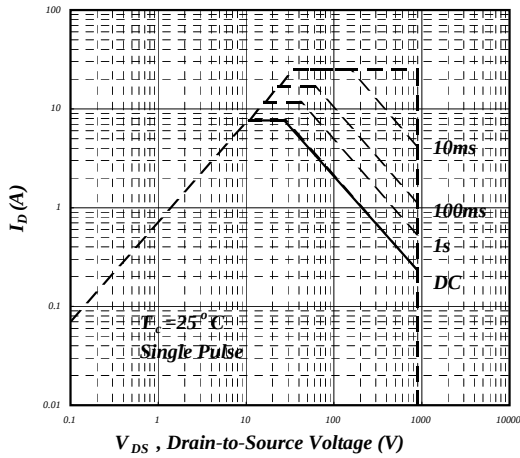


Fig 9. Maximum Safe Operating Area

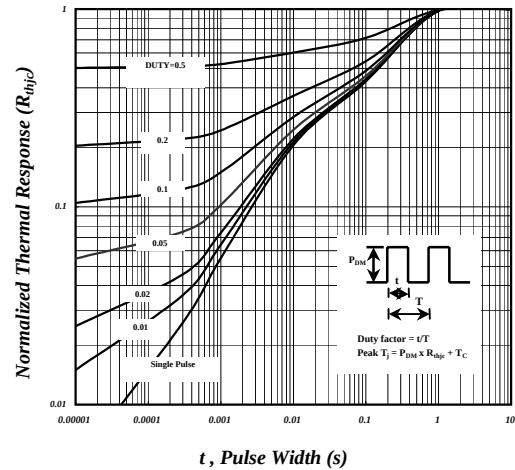


Fig 10. Effective Transient Thermal Impedance

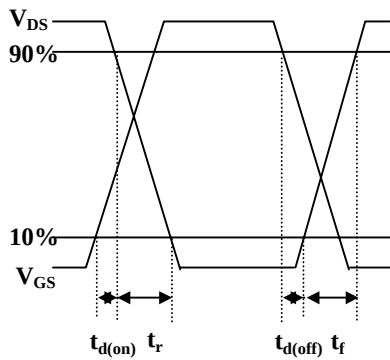


Fig 11. Switching Time Waveform

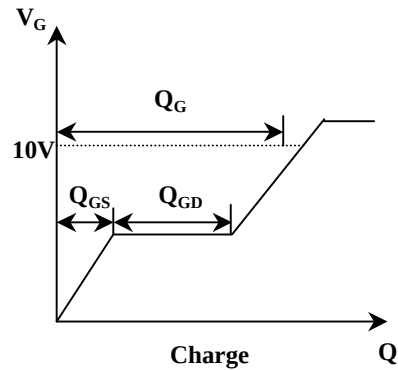
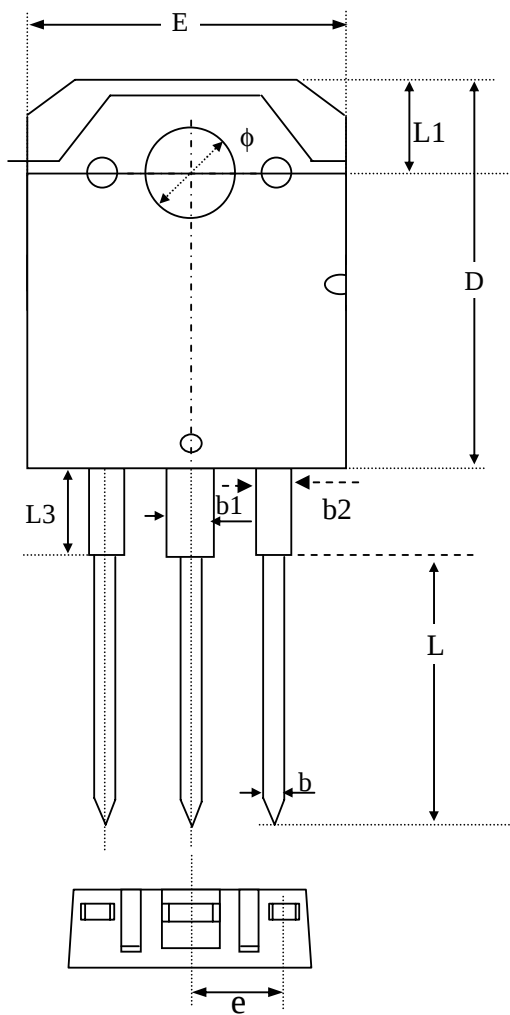


Fig 12. Gate Charge Waveform

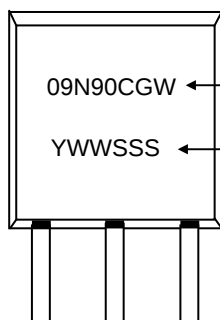
PHYSICAL DIMENSIONS - TO-247



SYMBOLS	Millimeters		
	MIN	NOM	MAX
A	4.60	4.80	5.00
A1	1.20	1.40	1.60
b	0.80	1.00	1.20
b1	2.80	3.00	3.20
b2	1.80	2.00	2.20
c	0.55	0.60	0.75
c1	1.45	1.50	1.65
D	19.70	19.90	20.10
E	15.40	15.60	15.80
e	5.15	5.45	5.75
L	16.20	16.50	16.80
L1	3.60	3.80	4.00
L3	3.30	3.50	3.70

1. All dimensions are in millimeters.
2. Dimensions do not include mold protrusions.

PART MARKING - TO-247



PART NUMBER: 09N90CGW = SSM09N90CGW

DATE/LOT CODE:

Y = last digit of the year
 WW = work week (01 -> 52)
 SSS = lot code sequence

PACKING: Moisture sensitivity level MSL3

1000pcs in tubes packed inside a moisture barrier bag (MBB).

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