

MMS 6508-1 MICROMODULE BOARD

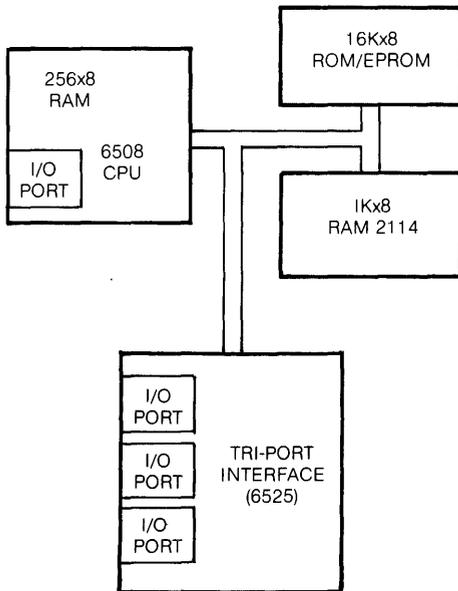
Description

The MMS 6508-1 is the first in a family of single board microcomputers, designed for easy system development. The heart of this micromodule is the new 6508 microprocessor. It has as enhancements, an on-chip eight-bit I/O port, 256 bytes of RAM, and a tri-state address bus.

The micromodule board has four sockets that allow the user to have from 8 to 16K bytes of program memory. The sockets are designed to accept ROMs, as well as 2716 or 2532 EPROMs, using +5v supply.

The micromodule board also features the new 6525 tri-port interface. This chip has three eight-bit I/O ports that are bit programmable as twenty-four I/O lines, or sixteen I/O lines, five priority interrupts and two handshake lines.

Block Diagram



Features

- Use +5v/2A power supply
- 1 MHz Operation
- 32 I/O lines or 24 I/O lines and 5 prioritized interrupts
- 16K ROM/EPROM Area
- 1K External RAM
- 256 bytes RAM on 6508

Edge Connector

Pin	Assignment	Pin	Assignment
1	P6	A	P7
2	P4	B	P5
3	P2	C	P3
4	P0	D	P1
5	01 In	E	IRQ
6	Reset	F	02 In
7	GND	H	GND
8	GND	J	GND
9	+5v	K	+5v
10	+5v	L	+5v
11	PC7	M	PA0
12	PC6	N	PA1
13	PC5	P	PA2
14	PC4	R	PA3
15	PC3	S	PA4
16	PB4	T	PA5
17	PC2	U	PA6
18	PB5	V	PA7
19	PC1	W	PB0
20	PB6	X	PB1
21	PC0	Y	PB2
22	PB7	Z	PB3



NIMOS

MMS
6508-1

6508 (MMS/1) LOGIC DIAGRAM

