N-Channel 30V(D-S) MOSFET

GENERAL DESCRIPTION

The ME7362 is the N-Channel logic enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology. This high density process is especially tailored to minimize on-state resistance. These devices are particularly suited for low voltage application such as notebook computer power management and other battery powered circuits where Low-side switching , and low in-line power loss are needed in a very small outline surface mount package.

FEATURES

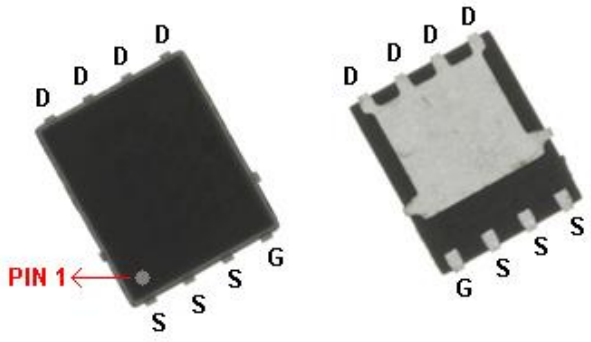
- $R_{DS(ON)} \leqq 2.0m\Omega @ V_{GS}=10V$
- $R_{DS(ON)} \leqq 3\ m\Omega @ V_{GS}=4.5V$
- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability

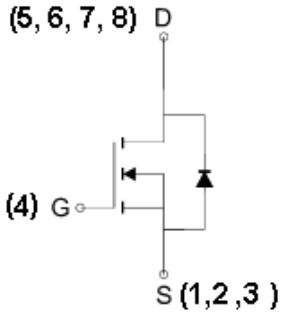
APPLICATIONS

- Power Management in Note book
- NB/MB Vcore Low side switching
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch

PIN CONFIGURATION

PowerDFN 5x6





N-Channel MOSFET

Ordering Information: ME7362 (Pb-free)

ME7362-G (Green product-Halogen free)

Absolute Maximum Ratings (T_C=25℃ Unless Otherwise Noted)

Parameter		Symbol	Maximum Ratings	Unit
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current*	T_C=25℃	I_D	115	A
	T_C=70℃		92	
Pulsed Drain Current		I_{DM}	459	A
Maximum Power Dissipation*	T_C=25℃	P_D	38	W
	T_C=70℃		24	
Operating Junction Temperature		T_J	-55 to 150	℃
Thermal Resistance-Junction to Case*		$R_{\theta JC}$	3.3	℃/W

*The device mounted on 1in^2 FR4 board with 2 oz copper

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Electrical Characteristics (T_C=25℃ Unless Otherwise Specified)

Symbol	Parameter	Limit	Min	Typ	Max	Unit
STATIC						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=250μA	30			V
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=250μA	1		2.2	V
I_{GSS}	Gate Leakage Current	V_{DS}=0V, V_{GS}=±20V			±100	nA
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=30V, V_{GS}=0V			1	μA
$R_{DS(ON)}$	Drain-Source On-State Resistance[a]	V_{GS}=10V, I_D=30A		1.4	2.0	mΩ
		V_{GS}=4.5V, I_D=28A		2.3	3	
V_{SD}	Diode Forward Voltage	I_S=2.8A, V_{GS}=0V		0.8	1.2	V
DYNAMIC						
Qg	Total Gate Charge	V_{DS}=15V, V_{GS}=10V, I_D=27A		148		nC
Qg	Total Gate Charge	V_{DS}=15V, V_{GS}=4.5V, I_D=27A		70		
Qgs	Gate-Source Charge			25		
Qgd	Gate-Drain Charge			30		
Ciss	Input Capacitance	V_{DS}=15V, V_{GS}=0V, F=1MHz		7430		pF
Coss	Output Capacitance			1150		
Crss	Reverse Transfer Capacitance			378		
Rg	Gate-Resistance	V_{DS}=0V, V_{GS}=0V, F=1MHz		0.9		Ω
$t_{d(on)}$	Turn-On Delay Time	V_{DD}=15V, R_L=15Ω I_D=1A, V_{GEN}=10V R_G=6Ω		39		Ns
t_r	Turn-On Rise Time			25		
$t_{d(off)}$	Turn-Off Delay Time			190		
t_f	Turn-Off Fall Time			60		

Notes: a. Pulse test: pulse width≦ 300us, duty cycle≦ 2%, Guaranteed by design, not subject to production testing.

b. Matsuki Electric/ Force mos reserves the right to improve product design, functions and reliability without notice.

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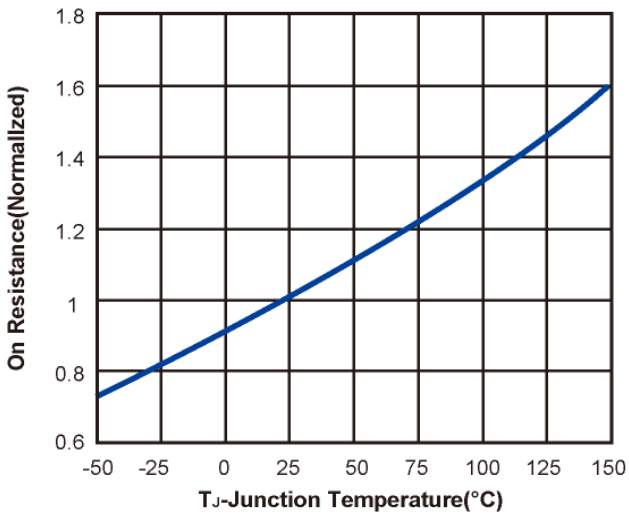
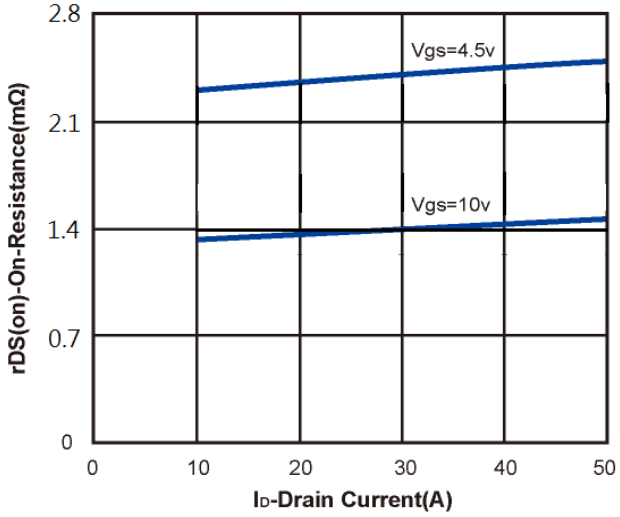
Typical Characteristics (T_J =25℃ Noted)

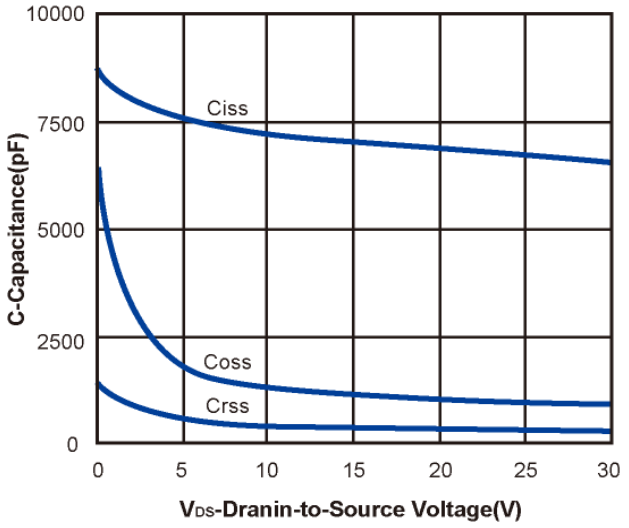
On Resistance vs. Junction Temperature



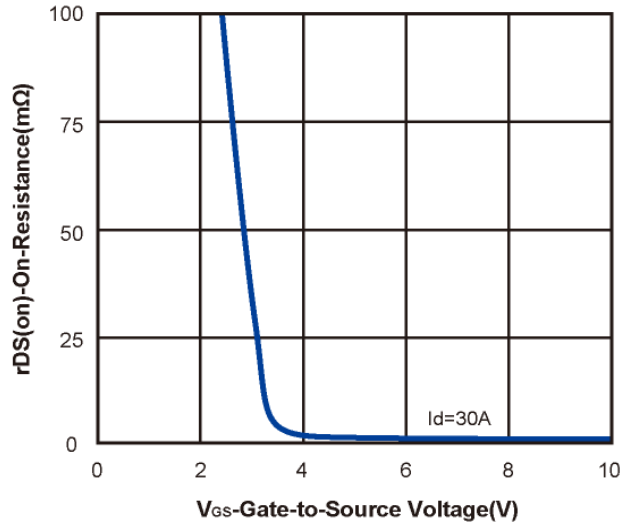
On Resistance vs. Drain Current



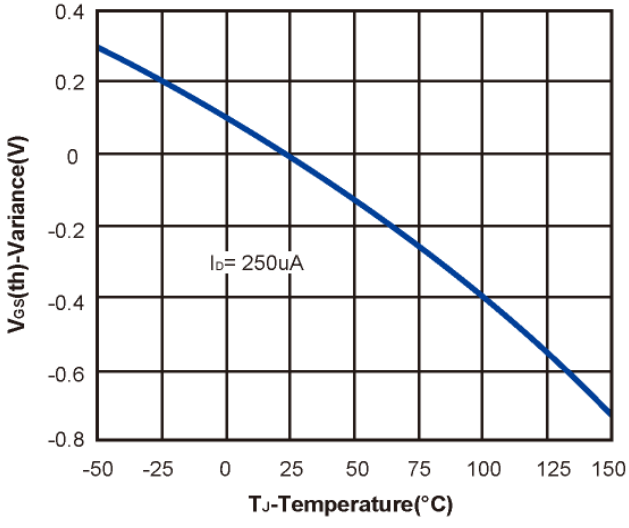
Capacitance



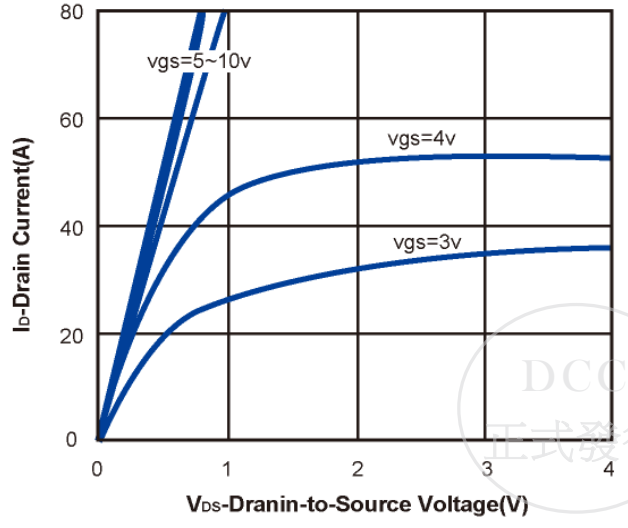
On Resistance vs. Gate-to-Source Voltage



Threshold Voltage



On-Region Characteristics



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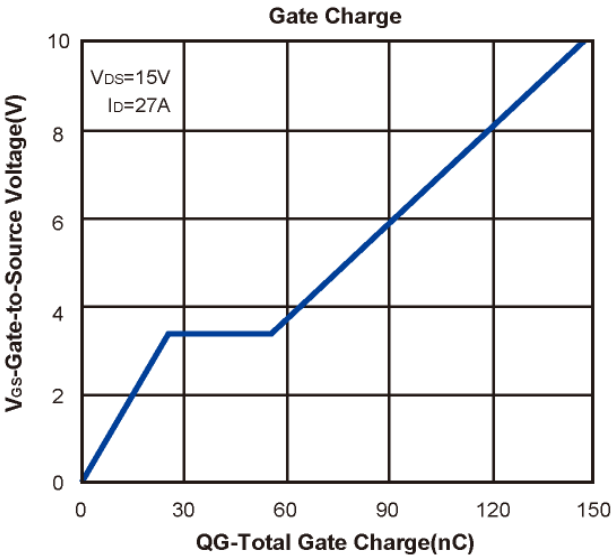
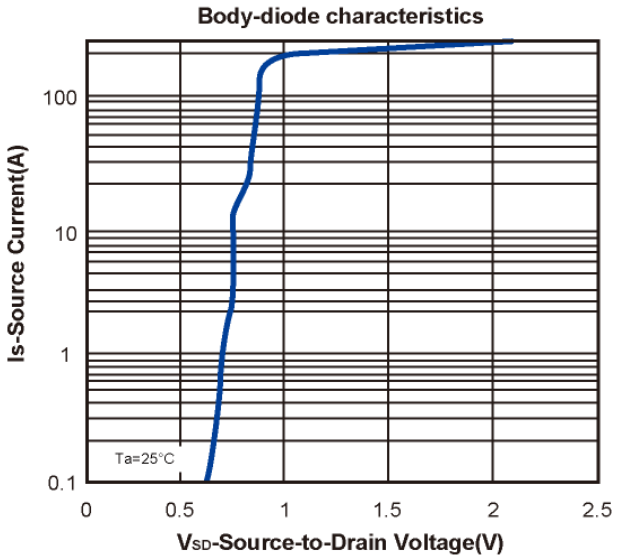
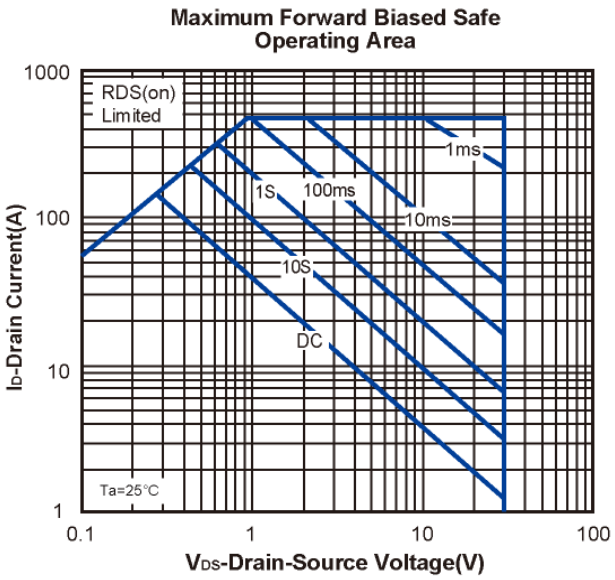
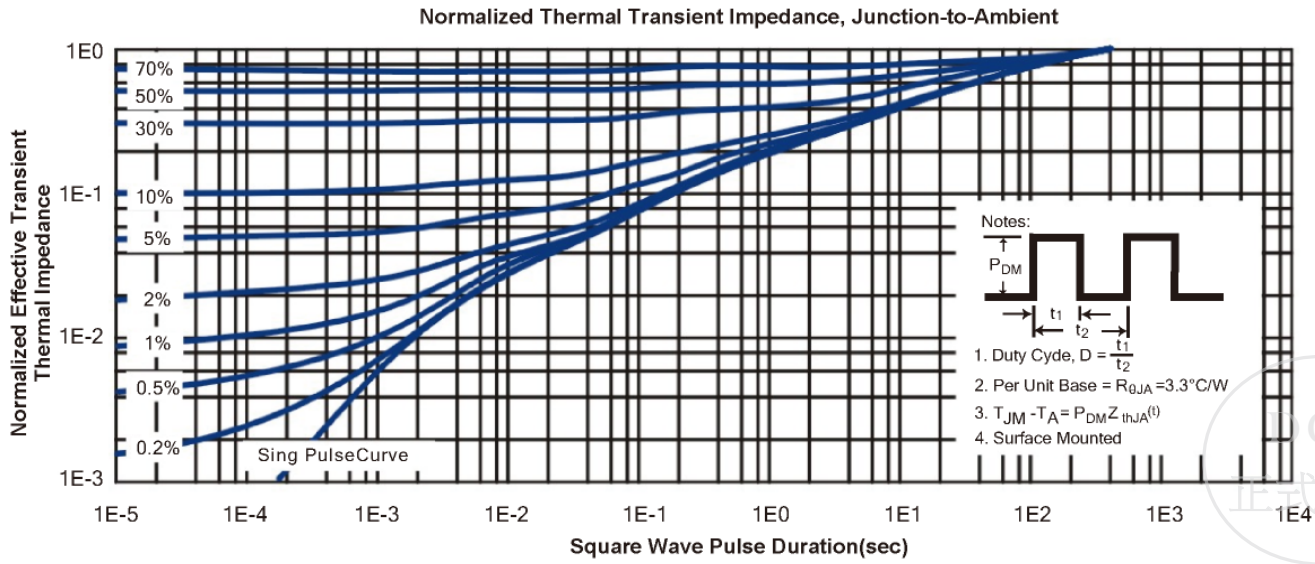
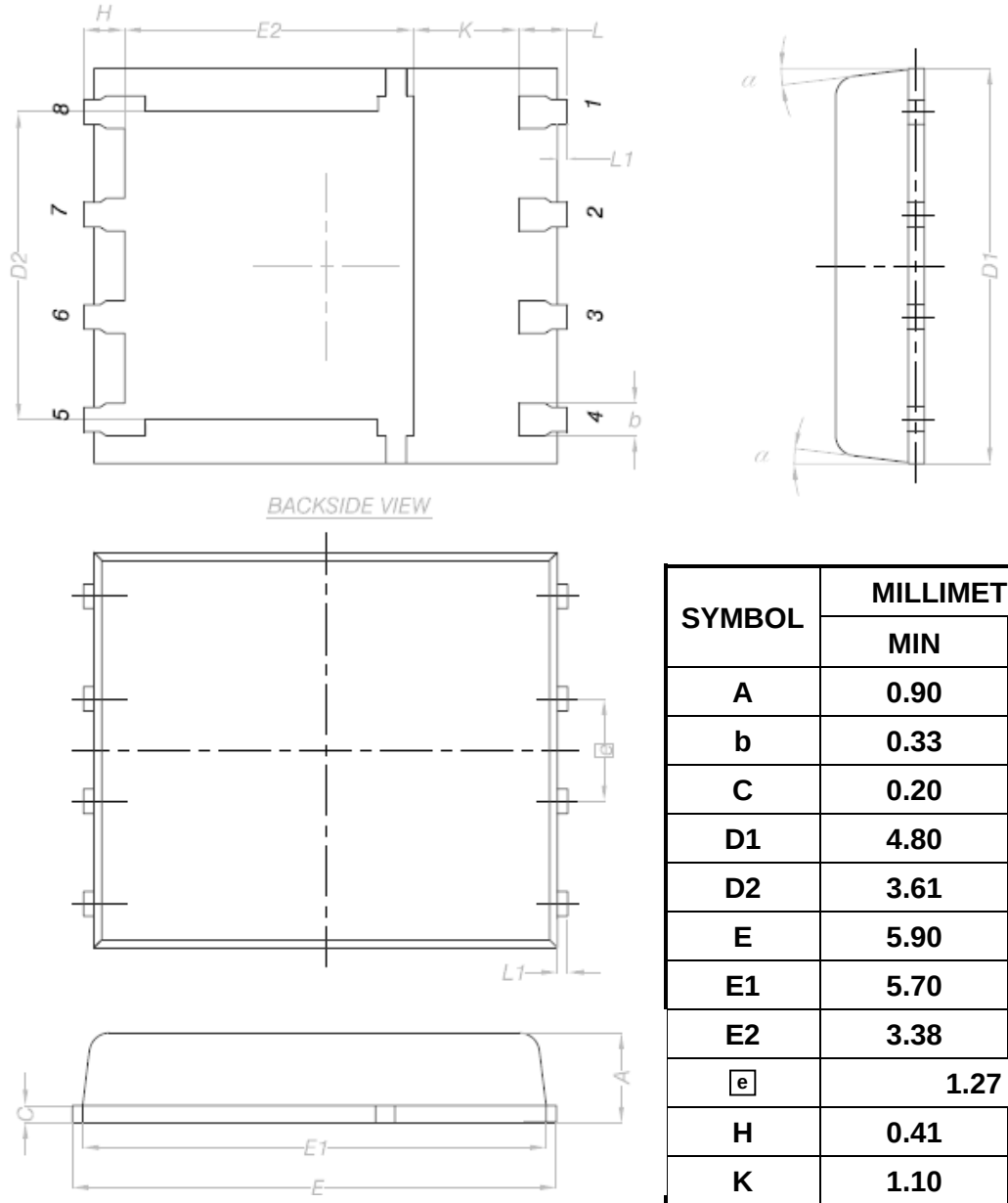








PowerDFN 5x6 Package Outline



SYMBOL	MILLIMETERS (mm)	
	MIN	MAX
A	0.90	1.10
b	0.33	0.51
C	0.20	0.30
D1	4.80	5.00
D2	3.61	3.96
E	5.90	6.10
E1	5.70	5.80
E2	3.38	3.78
e	1.27 BSC	
H	0.41	0.61
K	1.10	-
L	0.51	0.71
L1	0.06	0.20
α	0°	12°