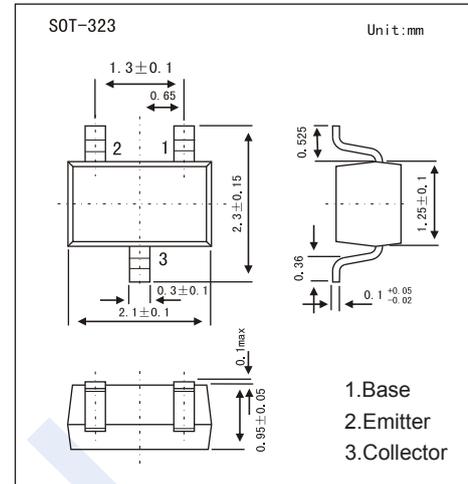


PNP Transistors

MMSTA56 (KMSTA56)

■ Features

- Small Surface Mount Package
- General Purpose for Amplification



■ Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

Parameter	Symbol	Rating	Unit
Collector - Base Voltage	V_{CB0}	-80	V
Collector - Emitter Voltage	V_{CE0}	-80	
Emitter - Base Voltage	V_{EB0}	-4	
Collector Current - Continuous	I_C	-500	mA
Collector Power Dissipation	P_C	200	mW
Thermal Resistance From Junction To Ambient	$R_{\theta JA}$	625	$^\circ\text{C}/\text{W}$
Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	-55 to 150	

■ Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Collector- base breakdown voltage	V_{CB0}	$I_C = -100 \mu\text{A}, I_E = 0$	-80			V
Collector- emitter breakdown voltage	V_{CE0}	$I_C = -1 \text{ mA}, I_B = 0$	-80			
Emitter - base breakdown voltage	V_{EB0}	$I_E = -100 \mu\text{A}, I_C = 0$	-4			
Collector-base cut-off current	I_{CB0}	$V_{CB} = -80 \text{ V}, I_E = 0$			-100	nA
Emitter cut-off current	I_{EB0}	$V_{EB} = -4 \text{ V}, I_C = 0$			-100	
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C = -100 \text{ mA}, I_B = -10 \text{ mA}$			-0.25	V
Base - emitter saturation voltage	$V_{BE(sat)}$	$I_C = -100 \text{ mA}, I_B = -10 \text{ mA}$			-1.2	
Base - emitter voltage	V_{BE}	$V_{CE} = -1 \text{ V}, I_C = -100 \text{ mA}$			-1.2	
DC current gain	$h_{FE(1)}$	$V_{CE} = -1 \text{ V}, I_C = -10 \text{ mA}$	50			
	$h_{FE(2)}$	$V_{CE} = -1 \text{ V}, I_C = -100 \text{ mA}$	50			
Transition frequency	f_T	$V_{CE} = -1 \text{ V}, I_C = -100 \text{ mA}, f = 100 \text{ MHz}$	50			MHz

■ Marking

Marking	K2G
---------	-----