

P-Channel Enhancement Mode MOSFET

Features

- 40V/-36A,
 $R_{DS(ON)} = 17m\Omega$ (max.) @ $V_{GS} = -10V$
 $R_{DS(ON)} = 25m\Omega$ (max.) @ $V_{GS} = -4.5V$
- 100% UIS + R_g Tested
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)

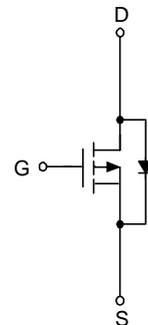
Applications

- Power Management in LCD TV Inverter.

Pin Description



Top View of TO-220FP



P-Channel MOSFET

Ordering and Marking Information

<p>SM4025PS □□□-□□□</p> <div style="margin-left: 20px;"> <p>└─ Assembly Material</p> <p>└─ Handling Code</p> <p>└─ Temperature Range</p> <p>└─ Package Code</p> </div>	<p>Package Code FP : TO-220FP</p> <p>Operating Junction Temperature Range C : -55 to 150 °C</p> <p>Handling Code TU : Tube</p> <p>Assembly Material G: Halogen and Lead Free Device</p>
<p>SM4025PS FP: </p>	<p>XXXXX - Lot Code</p>

Note : SINOPOWER lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. SINOPOWER lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020D for MSL classification at lead-free peak reflow temperature. SINOPOWER defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

SINOPOWER reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit	
V_{DSS}	Drain-Source Voltage	-40	V	
V_{GSS}	Gate-Source Voltage	± 25		
I_D^c	Continuous Drain Current ($V_{GS}=-10V$)	$T_C=25^\circ\text{C}$	-36	A
		$T_C=100^\circ\text{C}$	-23	
I_{DP}^c	300 μs Pulsed Drain Current Tested	$T_C=25^\circ\text{C}$	-145	
I_S^c	Diode Continuous Forward Current		-18	
I_{AS}^b	Avalanche Current, Single pulse	L=0.1mH	-30	
		L=1mH	-13	
E_{AS}^b	Avalanche Energy, Single pulse	L=0.1mH	45	mJ
		L=1mH	84	
T_J	Maximum Junction Temperature	150	$^\circ\text{C}$	
T_{STG}	Storage Temperature Range	-55 to 150		
P_D^a	Maximum Power Dissipation	$T_A=25^\circ\text{C}$	6.3	W
		$T_A=70^\circ\text{C}$	4.0	
P_D^c	Maximum Power Dissipation	$T_C=25^\circ\text{C}$	38	
		$T_C=100^\circ\text{C}$	15	
$R_{\theta JA}^a$	Thermal Resistance-Junction to Ambient	$t \leq 10\text{s}$	20	$^\circ\text{C/W}$
		Steady State	62.5	
$R_{\theta JC}^c$	Thermal Resistance-Junction to Case	Steady State	3.3	

Note a : Surface Mounted on 1in² pad area, $t \leq 10\text{sec}$. $R_{\theta JA}$ steady state $t = 100\text{s}$.

Note b : UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature $T_j=25^\circ\text{C}$).

Note c : The power dissipation P_D is based on $T_{J(MAX)} = 150^\circ\text{C}$, and it is useful for reducing junction-to-case thermal resistance ($R_{\theta JC}$) when additional heat sink is used.

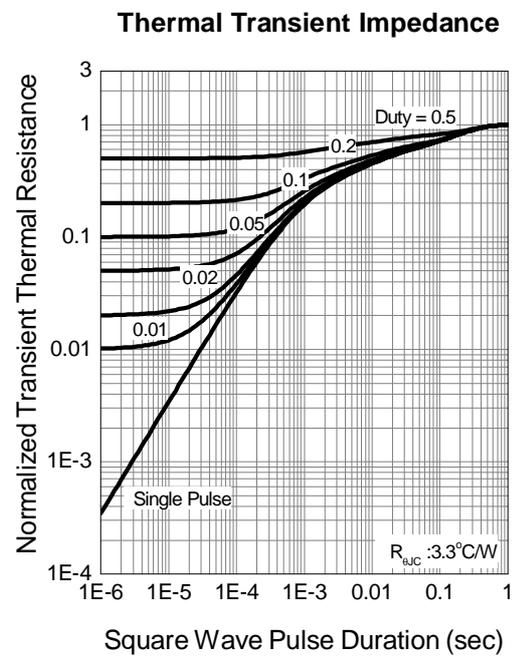
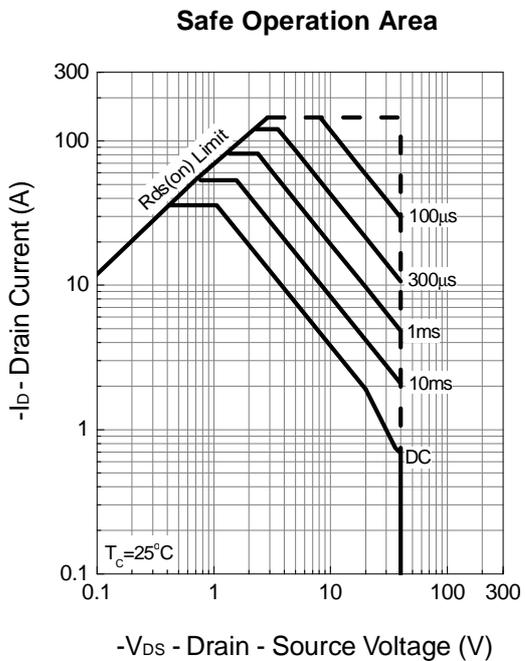
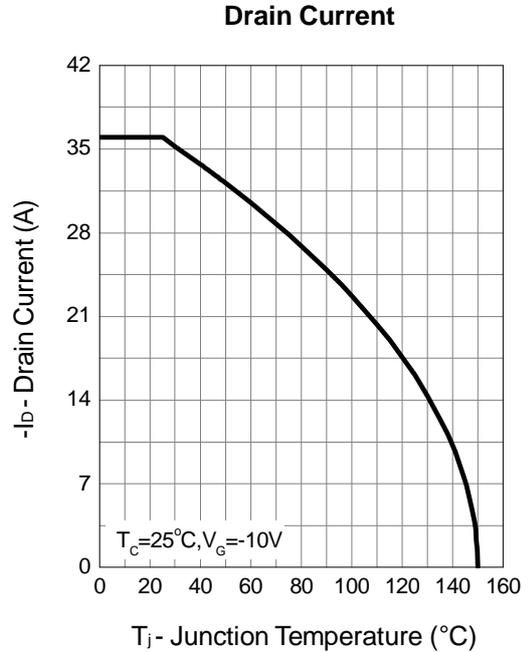
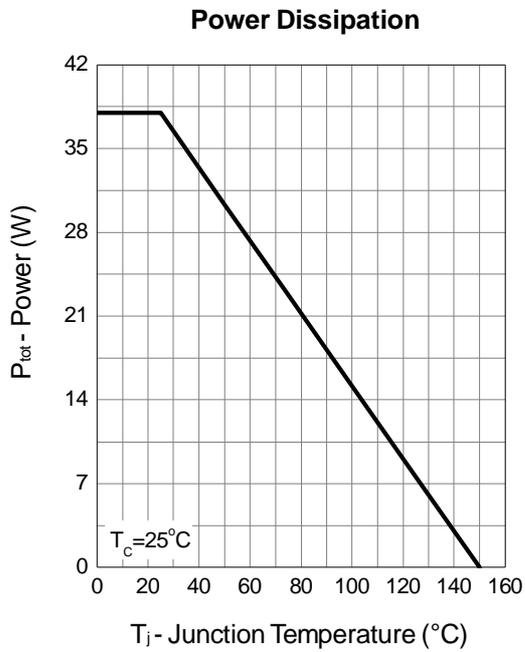
Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_{DS}=-250\mu A$	-40	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=-32V, V_{GS}=0V$ $T_J=85^\circ\text{C}$	-	-	-1	μA
			-	-	-30	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_{DS}=-250\mu A$	-1.4	-1.9	-2.4	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 25V, V_{DS}=0V$	-	-	± 100	nA
$R_{DS(ON)}^d$	Drain-Source On-state Resistance	$V_{GS}=-10V, I_{DS}=-15A$	-	13	17	m Ω
		$V_{GS}=-4.5V, I_{DS}=-10A$	-	19	25	
Diode Characteristics						
V_{SD}^d	Diode Forward Voltage	$I_{SD}=-1A, V_{GS}=0V$	-	-0.75	-1	V
t_{rr}	Reverse Recovery Time	$I_{SD}=-15A,$	-	24	-	ns
Q_{rr}	Reverse Recovery Charge	$dI_{SD}/dt=100A/\mu s$	-	18	-	nC
Dynamic Characteristics^e						
R_G	Gate Resistance	$V_{GS}=0V, V_{DS}=0V, F=1\text{MHz}$	-	2.5	5	Ω
C_{iss}	Input Capacitance	$V_{GS}=0V,$ $V_{DS}=-20V,$ Frequency=1.0MHz	-	1500	1950	pF
C_{oss}	Output Capacitance		-	220	-	
C_{riss}	Reverse Transfer Capacitance		-	175	-	
$t_{d(ON)}$	Turn-on Delay Time	$V_{DD}=-20V, R_L=20\Omega,$ $I_{DS}=-1A, V_{GEN}=-10V,$ $R_G=6\Omega$	-	14	25	ns
t_r	Turn-on Rise Time		-	12	22	
$t_{d(OFF)}$	Turn-off Delay Time		-	41	74	
t_f	Turn-off Fall Time		-	22	40	
Gate Charge Characteristics^e						
Q_g	Total Gate Charge	$V_{DS}=-20V, V_{GS}=-10V,$ $I_{DS}=-15A$	-	33	46	nC
Q_{gs}	Gate-Source Charge		-	5	-	
Q_{gd}	Gate-Drain Charge		-	9	-	

Note d : Pulse test; pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

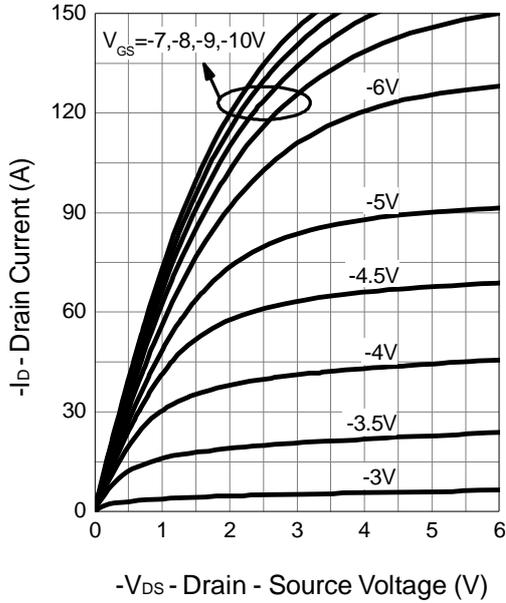
Note e : Guaranteed by design, not subject to production testing.

Typical Characteristics

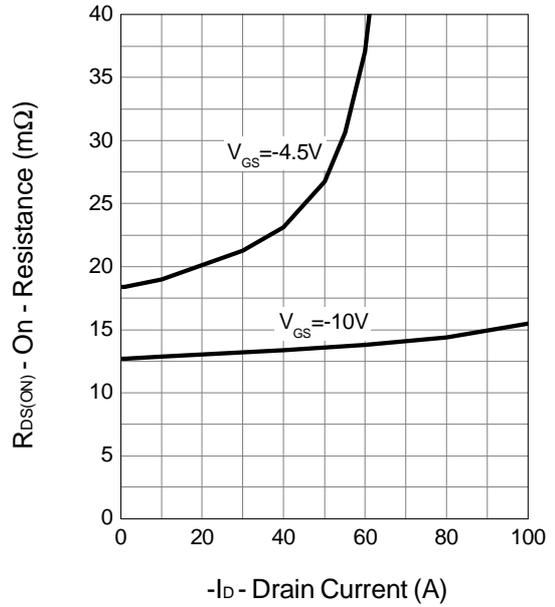


Typical Characteristics (Cont.)

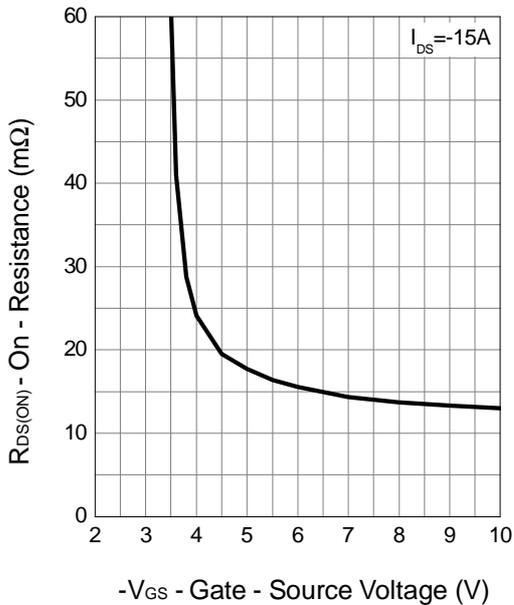
Output Characteristics



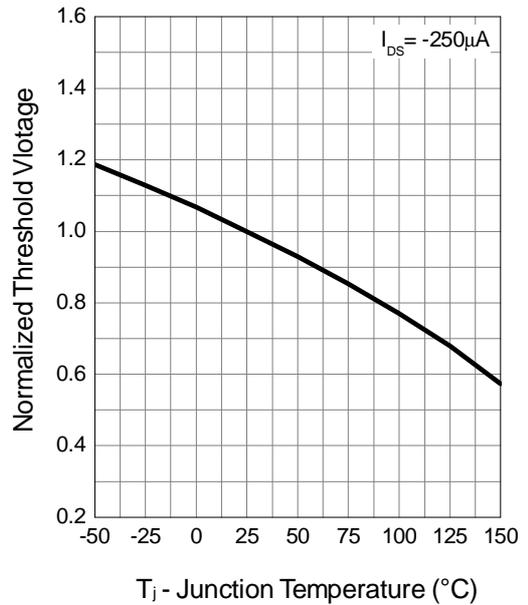
Drain-Source On Resistance



Gate-Source On Resistance

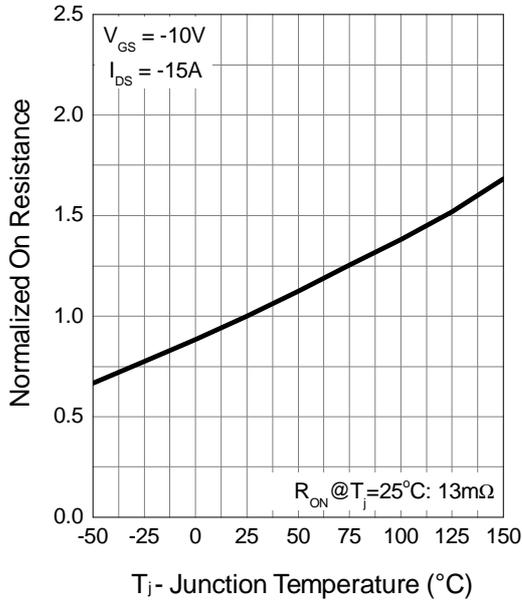


Gate Threshold Voltage

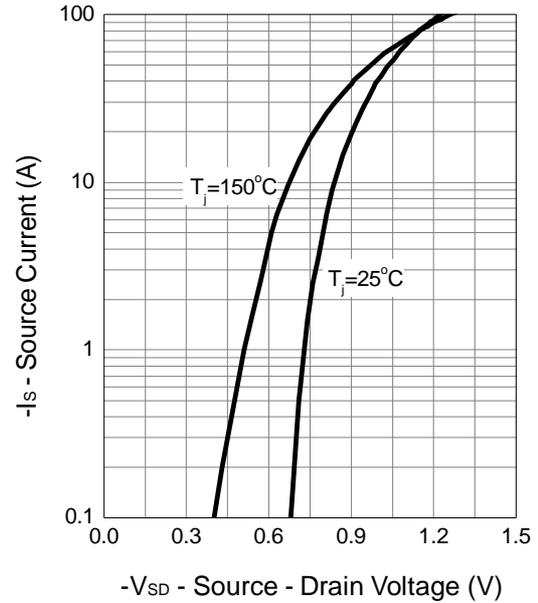


Typical Characteristics (Cont.)

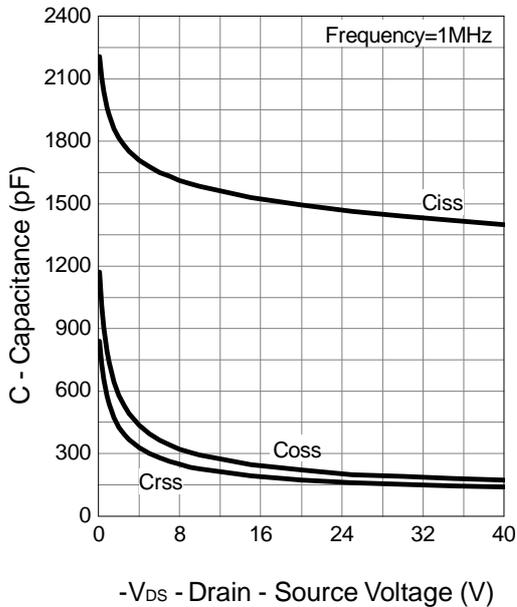
Drain-Source On Resistance



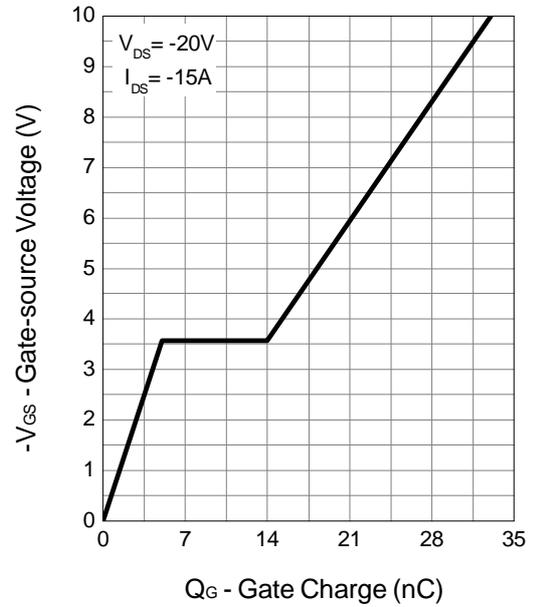
Source-Drain Diode Forward



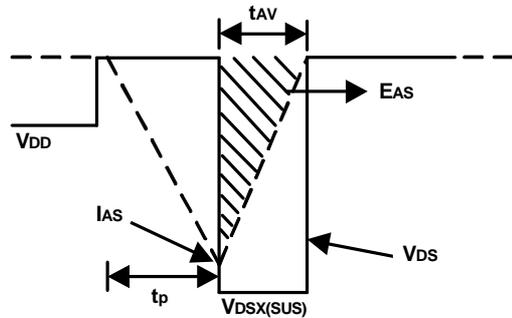
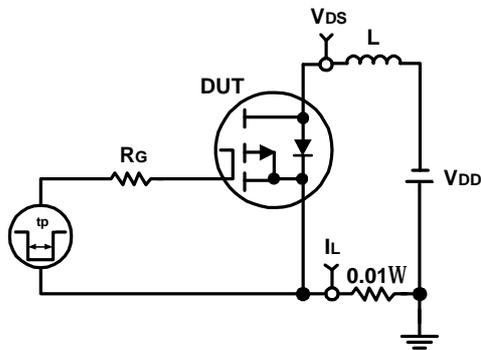
Capacitance



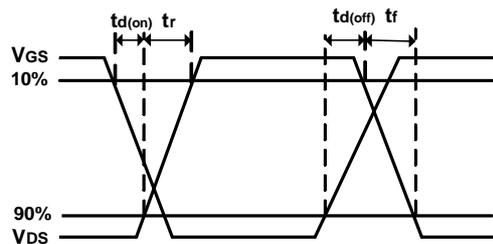
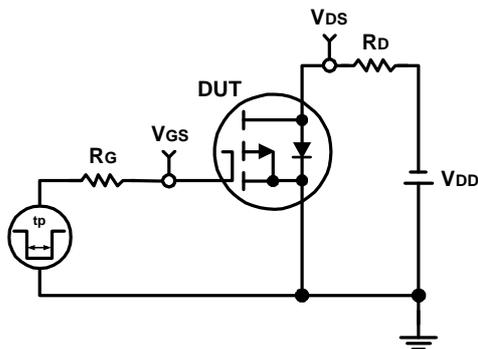
Gate Charge



Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms



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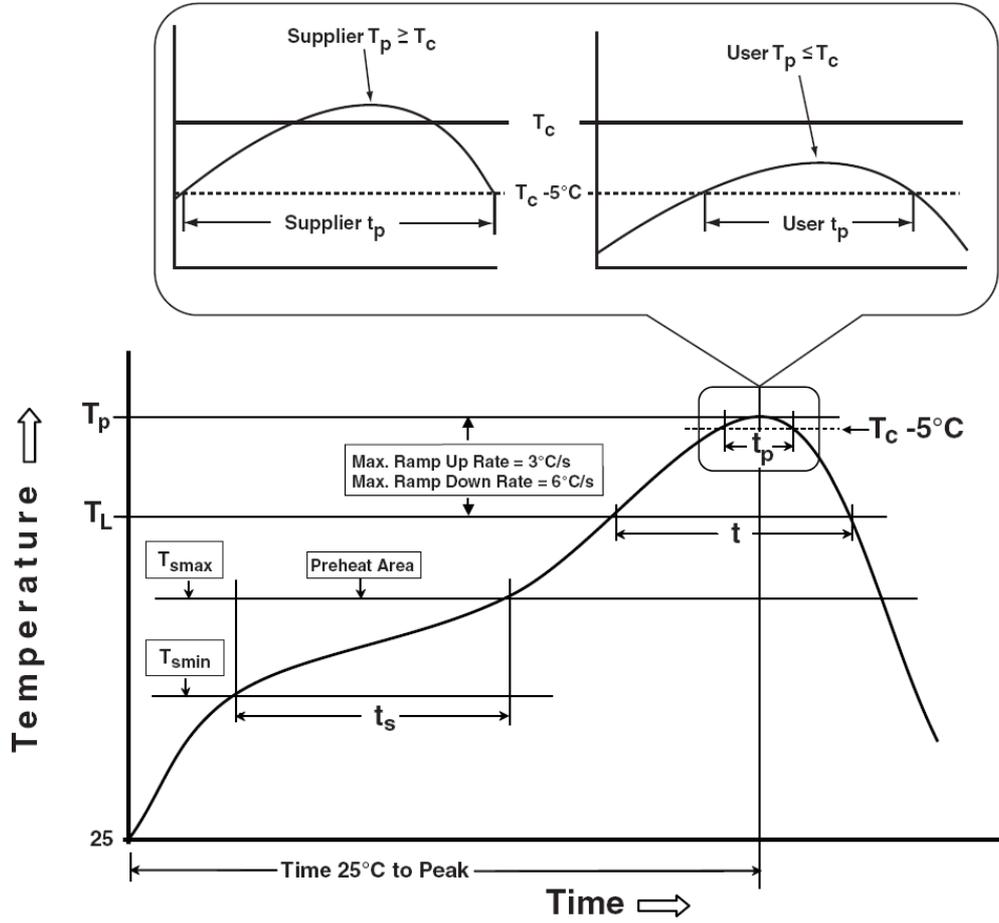
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Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak		
Temperature min (T_{smin})	100 °C	150 °C
Temperature max (T_{smax})	150 °C	200 °C
Time (T_{smin} to T_{smax}) (t_s)	60-120 seconds	60-120 seconds
Average ramp-up rate (T_{smax} to T_p)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L)	183 °C	217 °C
Time at liquidous (t_L)	60-150 seconds	60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_p)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum.		
** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1. SnPb Eutectic Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	1000 Hrs, 80% of VDS max @ T_{jmax}
HTGB	JESD-22, A108	1000 Hrs, 100% of VGS max @ T_{jmax}
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -65°C~150°C

Customer Service

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