

N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (mΩ)(Typ.)	I _D (A) ^a	Q _g (Typ.)
30	7.3 at V _{GS} = 10 V	45	23 nC
	10.2 at V _{GS} = 4.5 V		

FEATURES

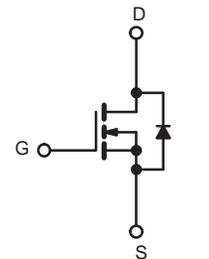
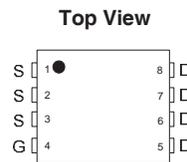
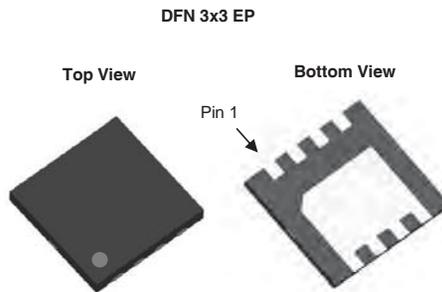
- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested



RoHS
COMPLIANT

APPLICATIONS

- Notebook PC Core
- VRM/POL



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	± 20	V
Continuous Drain Current (T _J = 150 °C)	T _C = 25 °C	I _D	45
	T _C = 70 °C		37
Pulsed Drain Current	I _{DM}	135	A
Single-Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	49
Maximum Power Dissipation	T _C = 25 °C	P _D	28.5
	T _C = 70 °C		19.95
	T _A = 25 °C		3.15 ^{b,c}
	T _A = 70 °C		2.2 ^{b,c}
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b,d}	R _{thJA}	40	48	°C/W
Maximum Junction-to-Case	R _{thJC}	4.5	5.3	

Notes:

- Based on T_C = 25 °C.
- Surface mounted on 1" x 1" FR4 board.
- t = 10 s.
- Maximum under steady state conditions is 50 °C/W.

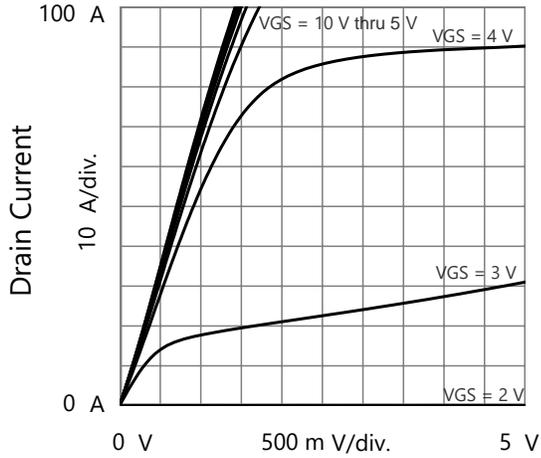
SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted)							
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0, I_D = 250\text{ }\mu\text{A}$	30			V	
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	1		3	V	
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}$			1	μA	
		$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$			10		
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$	45			A	
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 12\text{ A}$		7.3	8.1	m Ω	
		$V_{GS} = 4.5\text{ V}, I_D = 9\text{ A}$		10.2	12		
Forward Transconductance ^a	g_{fs}	$V_{DS} = 10\text{ V}, I_D = 12\text{ A}$		90		S	
Dynamic^b							
Input Capacitance	C_{iss}	$V_{DS} = 15\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		1170		pF	
Output Capacitance	C_{oss}			184			
Reverse Transfer Capacitance	C_{rss}			140			
Total Gate Charge	Q_g	$V_{DS} = 15\text{ V}, V_{GS} = 10\text{ V}, I_D = 12\text{ A}$		23		nC	
Gate-Source Charge	Q_{gs}			2.3			
Gate-Drain Charge	Q_{gd}			4.3			
Gate Resistance	R_g	$f = 1\text{ MHz}$		3		Ω	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 15\text{ V}, R_L = 0.5\text{ }\Omega$ $I_D \cong 12\text{ A}, V_{GEN} = 10\text{ V}, R_g = 1\text{ }\Omega$		17		ns	
Rise Time	t_r			13			
Turn-Off Delay Time	$t_{d(off)}$			72			
Fall Time	t_f			11			
Drain-Source Body Diode Characteristics							
Continuous Source-Drain Diode Current	I_S	$T_C = 25\text{ }^\circ\text{C}$			45	A	
Pulse Diode Forward Current (100 μs)	I_{SM}				135		
Body Diode Voltage	V_{SD}	$I_S = 1\text{ A}$			1.2	V	
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 12\text{ A}, di/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$		55	82	ns	
Body Diode Reverse Recovery Charge	Q_{rr}				75	105	nC
Reverse Recovery Fall Time	t_a				28		ns
Reverse Recovery Rise Time	t_b				25		

Notes:

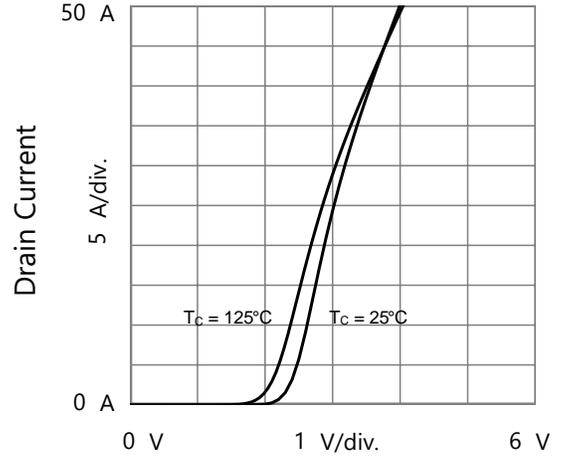
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
 b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

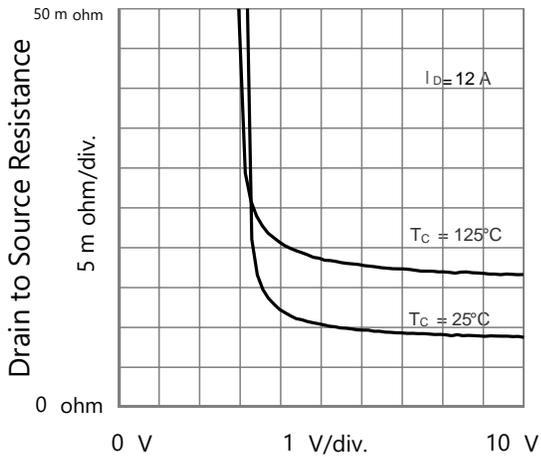
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



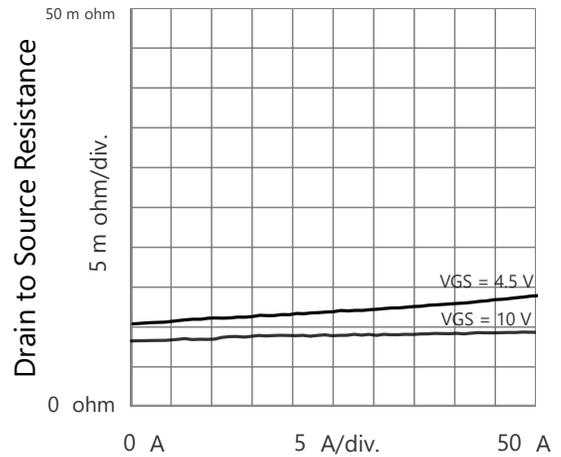
Drain to Source Voltage
Output Characteristics



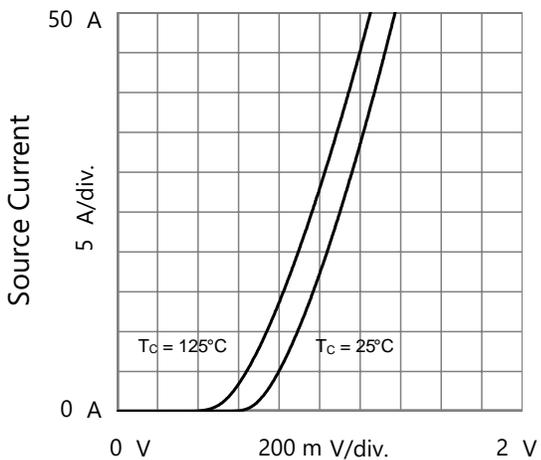
Gate to Source Voltage
Transfer Characteristics



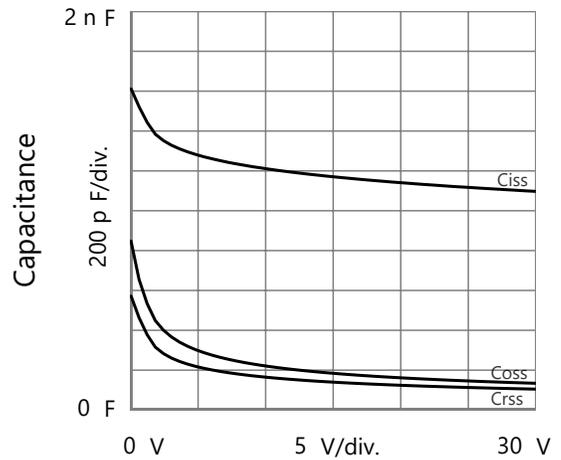
Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage



Drain Current
Drain to Source Resistance vs. Drain Current

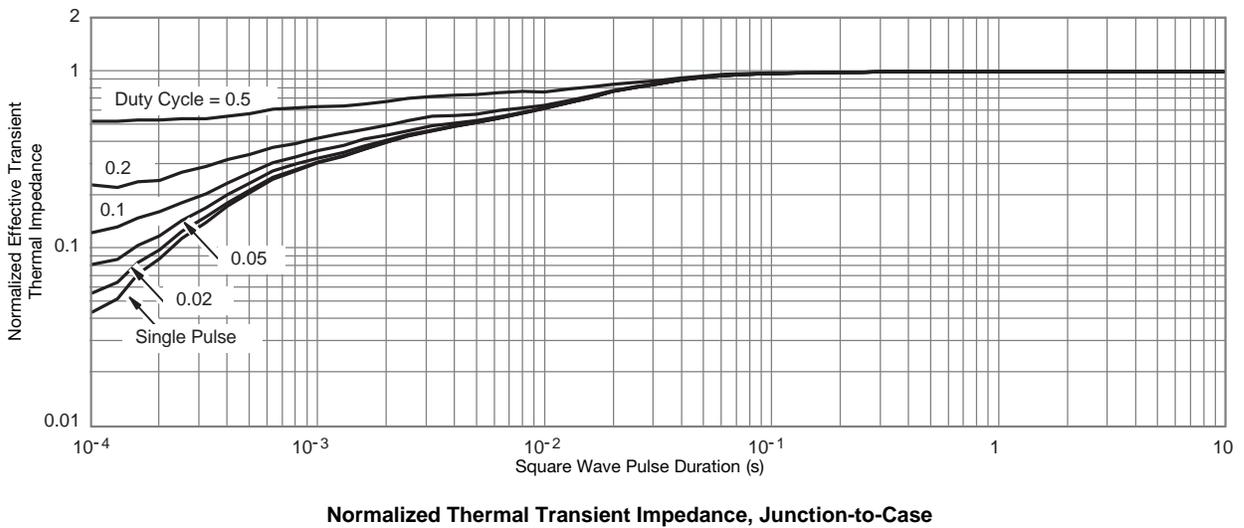
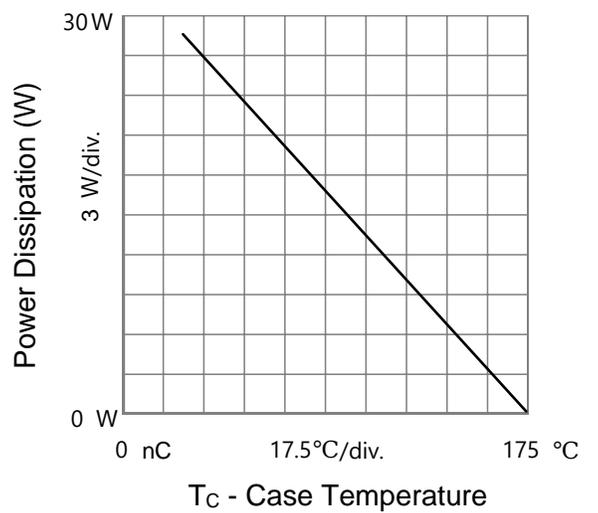
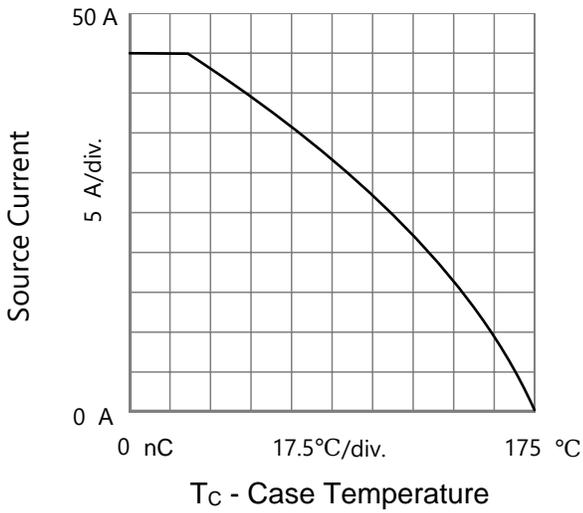
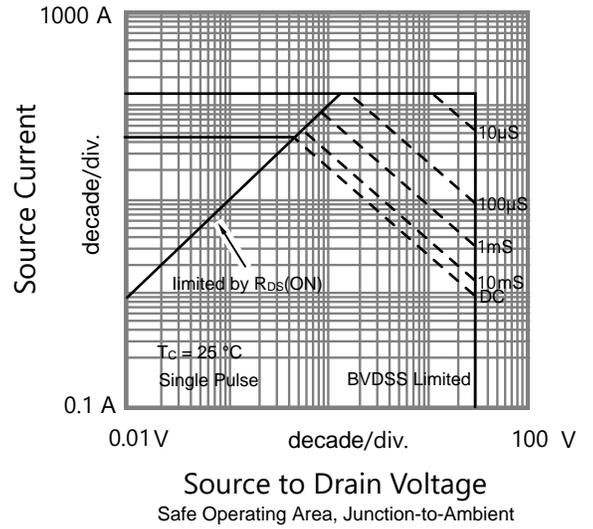
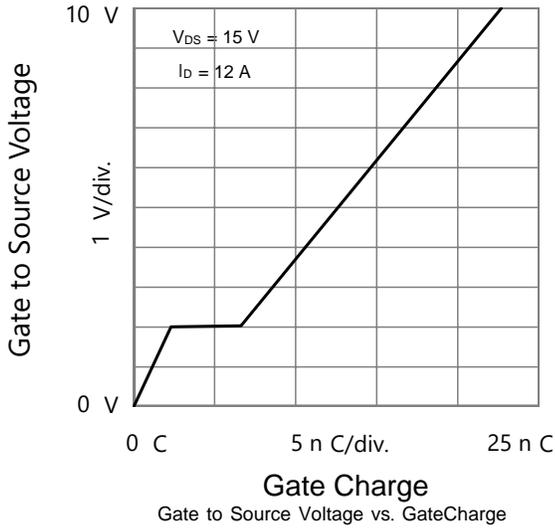


Source to Drain Voltage
Body Diode Forward Characteristics



Drain to Source Voltage
Capacitances

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



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