

N-Channel 40 V (D-S) MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (mΩ)(Typ.)	I _D (A) ^{a, e}	Q _g (Typ.)
40	4.7 at V _{GS} = 10 V	70	70 nC
	6 at V _{GS} = 4.5 V	65	

FEATURES

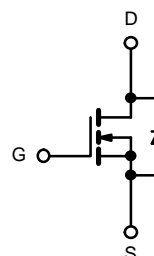
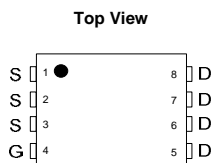
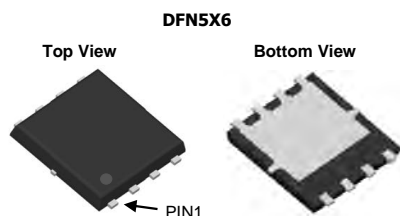
- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested



RoHS
COMPLIANT

APPLICATIONS

- Notebook PC Core
- VRM/POL



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	40	V
Gate-Source Voltage	V _{GS}	± 20	
Continuous Drain Current (T _J = 175 °C)	I _D	T _C = 25 °C	70 ^{a, e}
		T _C = 70 °C	60 ^e
		T _A = 25 °C	19 ^{b, c}
		T _A = 70 °C	18.6 ^{b, c}
Pulsed Drain Current	I _{DM}	280	A
Avalanche Current Pulse	L = 0.1 mH I _{AS}	59	
Single Pulse Avalanche Energy	E _{AS}	210	
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C	70 ^{a, e}
		T _A = 25 °C	3.5 ^{b, c}
Maximum Power Dissipation	P _D	T _C = 25 °C	105 ^a
		T _C = 70 °C	55
		T _A = 25 °C	6.15 ^{b, c}
		T _A = 70 °C	3.05 ^{b, c}
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, d}	R _{thJA}	15	25	°C/W
Maximum Junction-to-Case	R _{thJC}	1.0	1.5	

Notes:

- a. Based on T_C = 25 °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 10 s.
- d. Maximum under steady state conditions is 90 °C/W.
- e. Calculated based on maximum junction temperature.

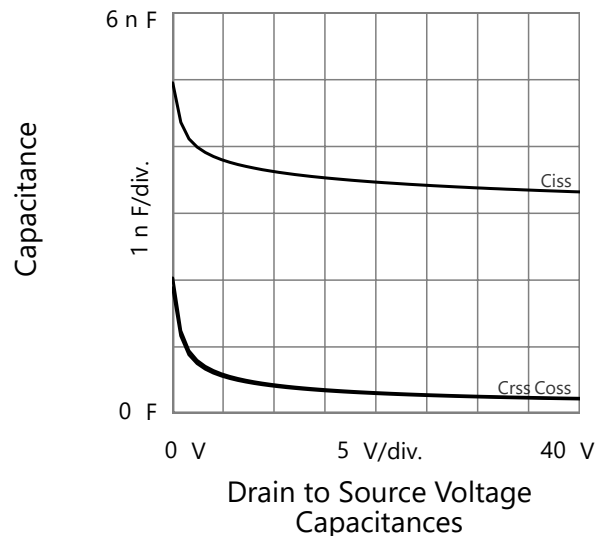
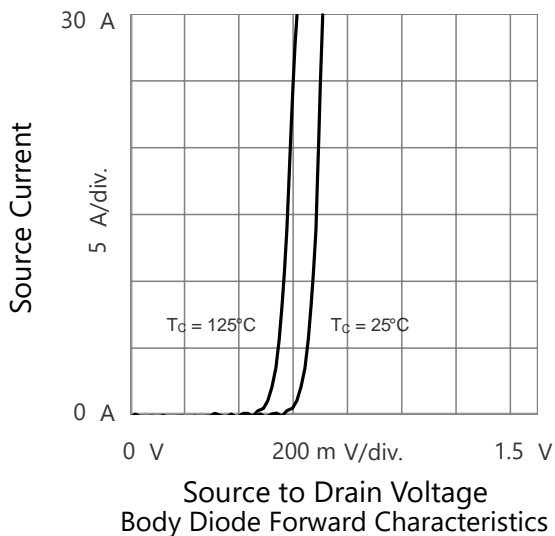
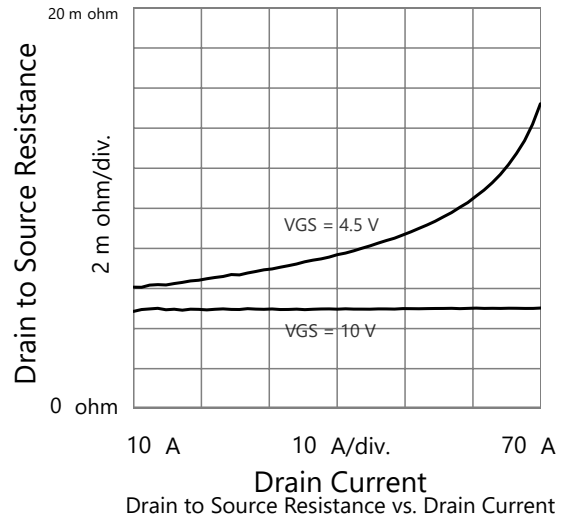
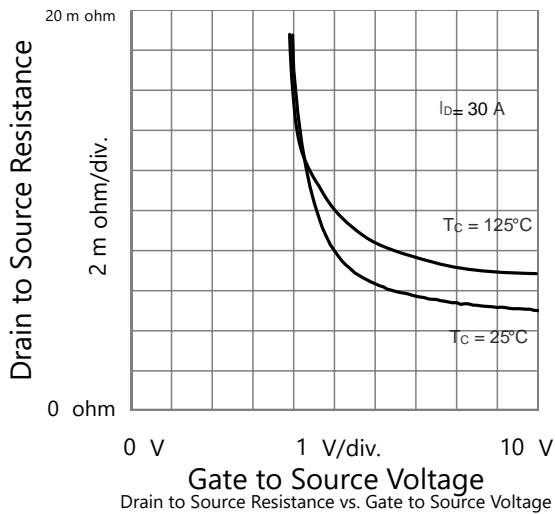
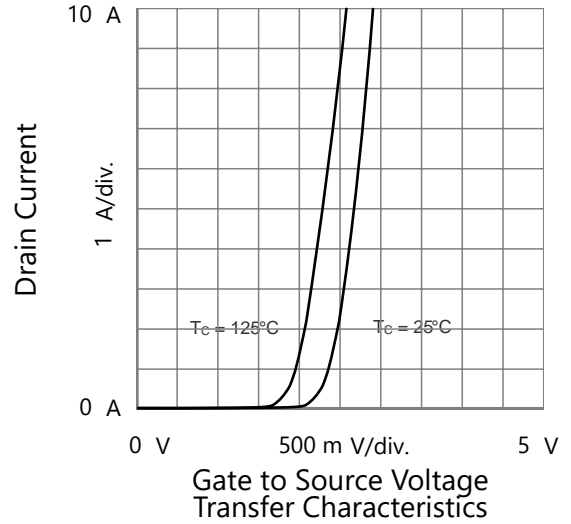
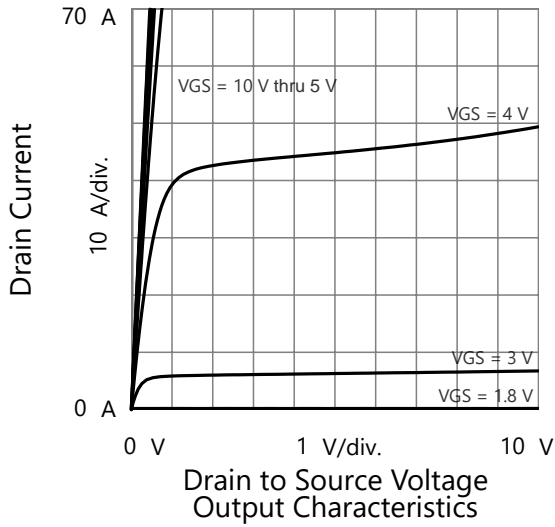
SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min .	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	40			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250\text{ }\mu\text{A}$		35		mV/ $^\circ\text{C}$
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			-5.5		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	1.2		2.5	V
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 40\text{ V}, V_{GS} = 0\text{ V}$			1	μA
		$V_{DS} = 40\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$			10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$	70			A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 30\text{ A}$		4.7	5.2	m Ω
		$V_{GS} = 4.5\text{ V}, I_D = 20\text{ A}$		6	7	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 15\text{ V}, I_D = 30\text{ A}$		110		S
Dynamic^b						
Input Capacitance	C_{iss}	$V_{DS} = 20\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		3470		pF
Output Capacitance	C_{oss}			315		
Reverse Transfer Capacitance	C_{rss}			300		
Total Gate Charge	Q_g	$V_{DS} = 20\text{ V}, V_{GS} = 10\text{ V}, I_D = 30\text{ A}$		70		nC
Gate-Source Charge	Q_{gs}			32		
Gate-Drain Charge	Q_{gd}			27		
Gate Resistance	R_g	$f = 1\text{ MHz}$		2.2		Ω
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 15\text{ V}, R_L = 0.555\text{ }\Omega$ $I_D \cong 30\text{ A}, V_{GEN} = 10\text{ V}, R_g = 1\text{ }\Omega$		18		ns
Rise Time	t_r			11		
Turn-Off Delay Time	$t_{d(off)}$			70		
Fall Time	t_f			10		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 15\text{ V}, R_L = 0.625\text{ }\Omega$ $I_D \cong 20\text{ A}, V_{GEN} = 4.5\text{ V}, R_g = 1\text{ }\Omega$		55		
Rise Time	t_r			180		
Turn-Off Delay Time	$t_{d(off)}$			55		
Fall Time	t_f			12		
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	$T_C = 25\text{ }^\circ\text{C}$			70	A
Pulse Diode Forward Current ^a	I_{SM}				280	
Body Diode Voltage	V_{SD}	$I_S = 1\text{ A}$		0.6	1	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 30\text{ A}, di/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$		52	78	ns
Body Diode Reverse Recovery Charge	Q_{rr}			70.2	105	nC
Reverse Recovery Fall Time	t_a			27		ns
Reverse Recovery Rise Time	t_b			25		

Notes:

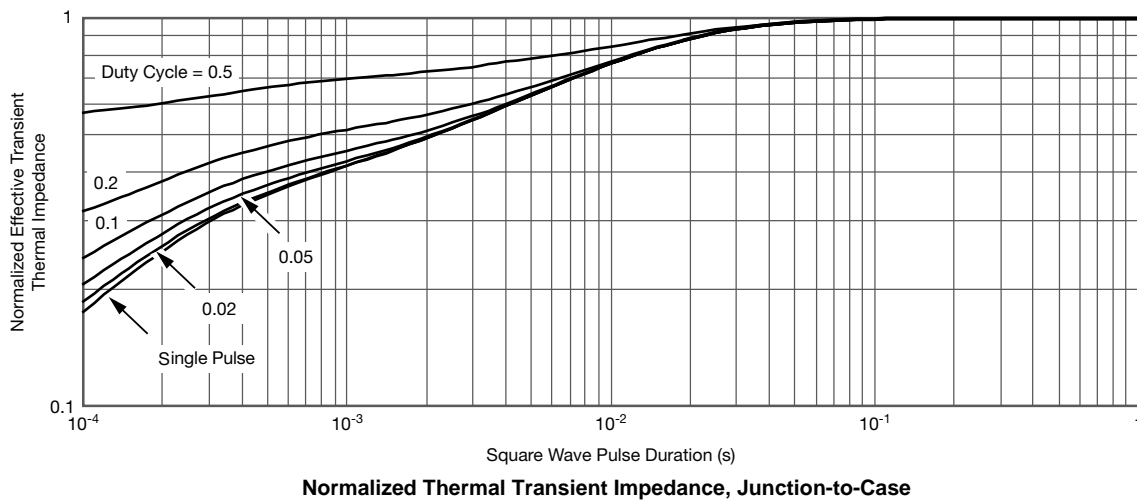
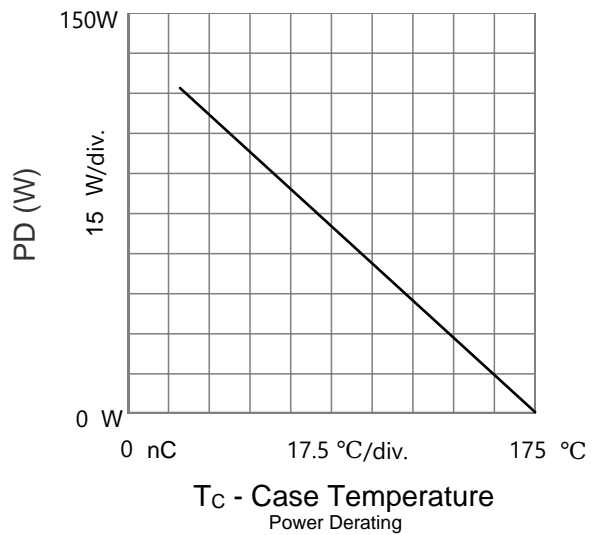
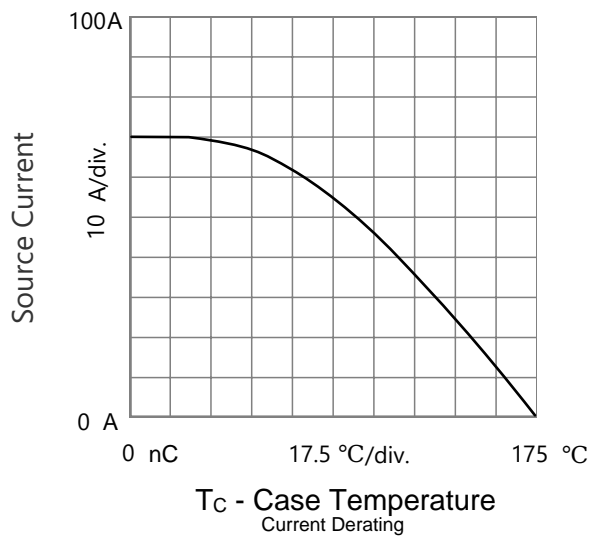
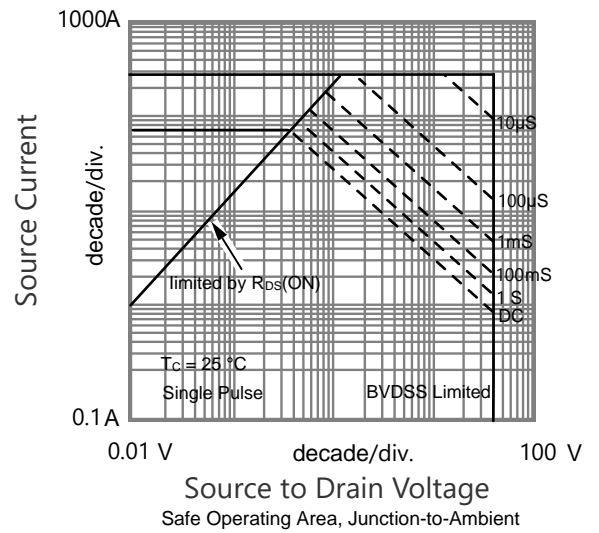
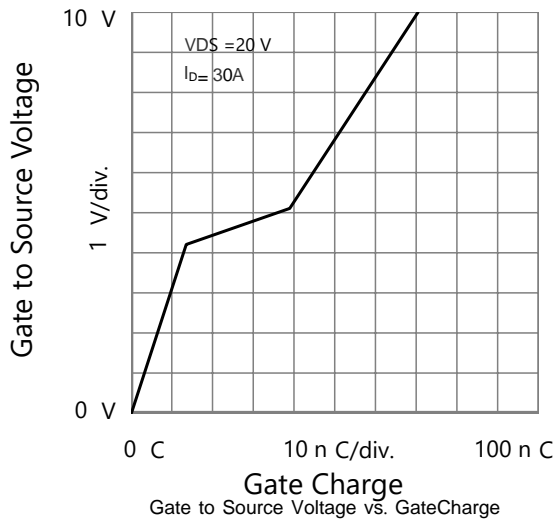
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
 b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum ratings conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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