

RKR0303BKJ

Silicon Schottky Barrier Diode for Rectifying

REJ03G1739-0100
Rev.1.00
Nov 17, 2008

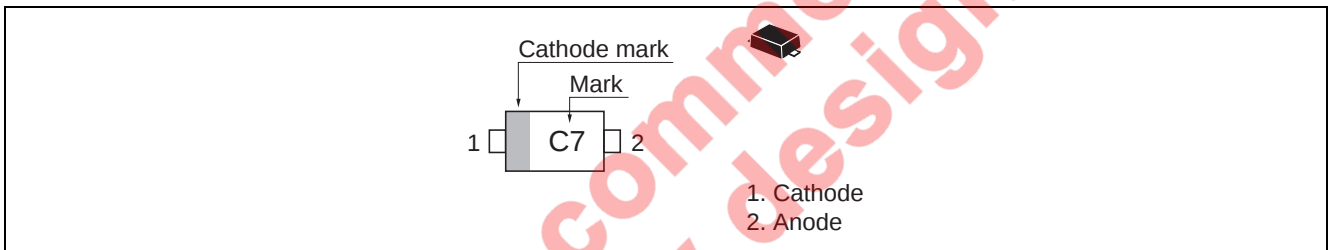
Features

- Low reverse current drop and suitable for high efficiency rectifying.
- Ultra small Flat Lead Package (UFP) is suitable for compact and high-density surface mount design.

Ordering Information

Part No.	Laser Mark	Package Name	Package Code
RKR0303BKJ	C7	UFP	PWSF0002ZA-A

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	V_{RRM}	30	V
Reverse voltage	V_R	30	V
forward current	I_F * ¹	0.3	A
Non-Repetitive peak forward surge current	I_{FSM} * ²	1	A
Junction temperature	T_J	150	°C
Storage temperature	T_{stg}	-55 to +150	°C

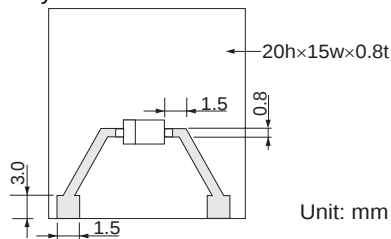
Notes: 1. See from Fig.4 to Fig.6.
 2. 10 ms sine wave 1 pulse.

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_F	—	—	0.50	V	$I_F = 300$ mA
Reverse current	I_R	—	—	50	μA	$V_R = 30$ V
Thermal resistance	$R_{th(j-a)}$	—	600	—	°C/W	Polyimide board * ¹

Note: 1. Polyimide board



Note: In the UFP package, some lead is exposed because the tip of the lead is used as the cutting plane. Therefore, the solderability of the lead tip has been ignored. Please test and confirm before use.

Main Characteristic

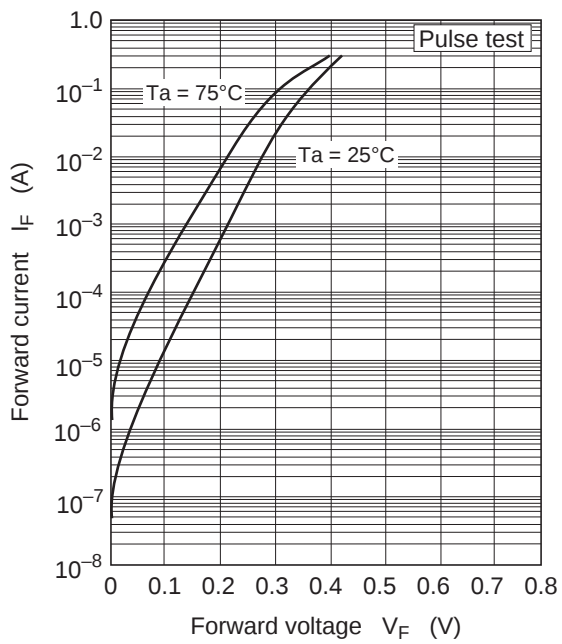


Fig.1 Forward current vs. Forward voltage

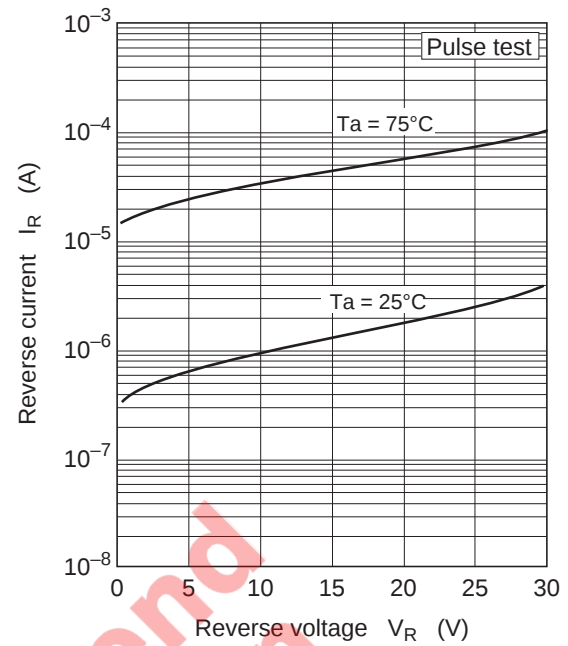


Fig.2 Reverse current vs. Reverse voltage

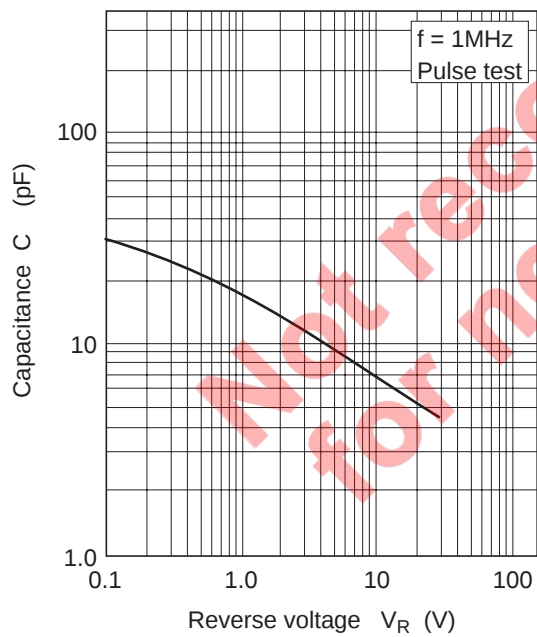


Fig.3 Capacitance vs. Reverse voltage

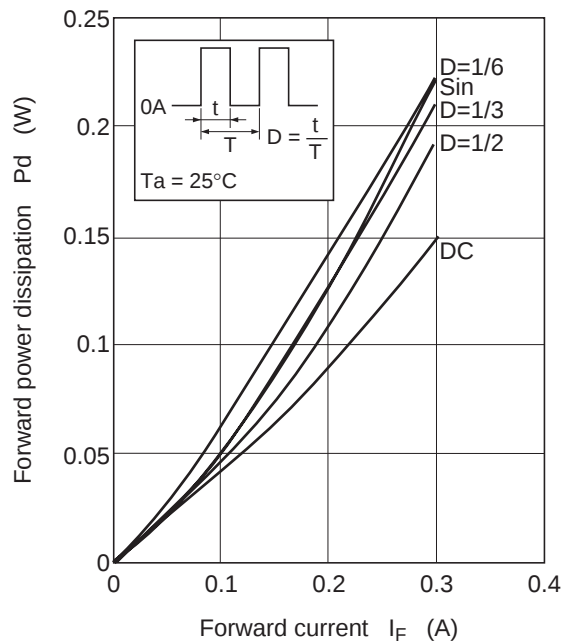


Fig.4 Forward power dissipation vs. Forward current

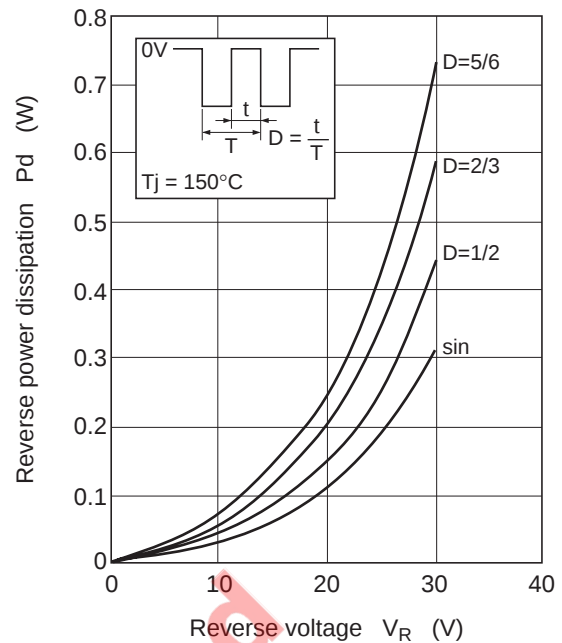


Fig.5 Reverse power dissipation vs. Reverse voltage

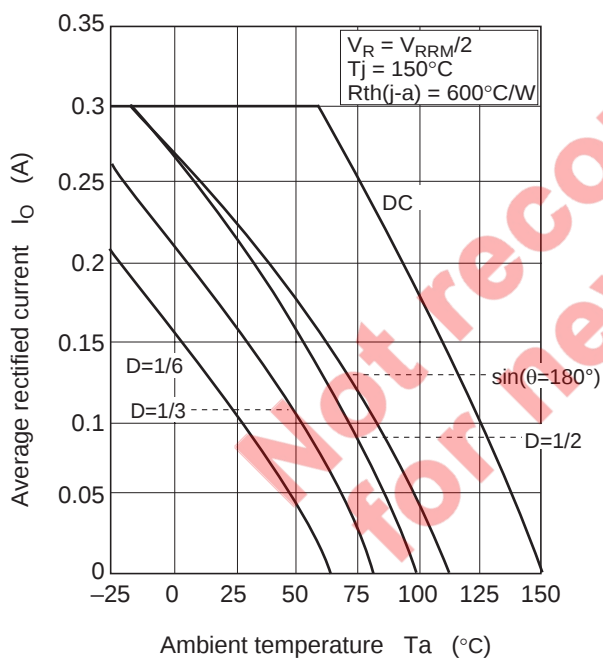
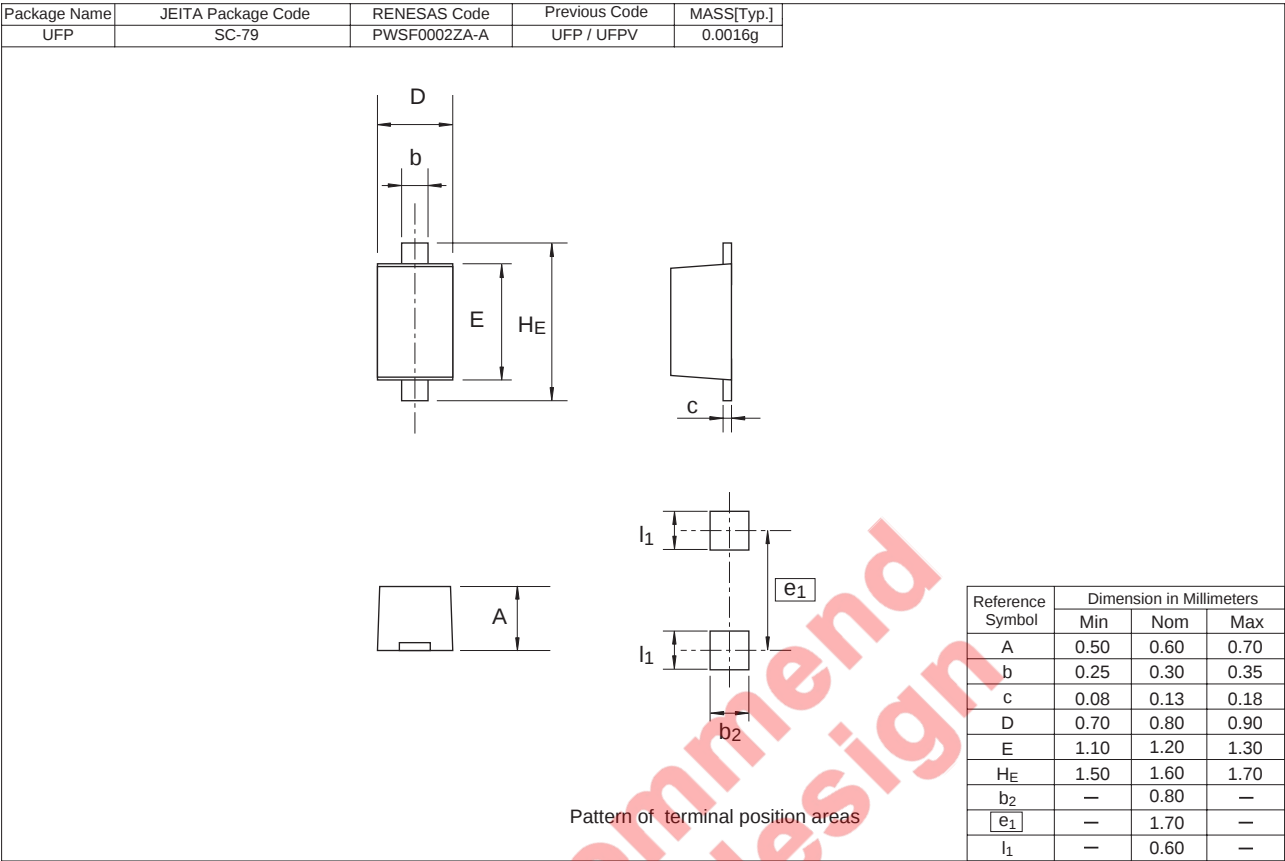


Fig.6 Average rectified current vs. Ambient temperature

Package Dimensions



Notes:

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