

Silicon Zener Diodes



Glass Axial Leaded

Low Level, Very Low Voltage, Low Leakage

Model	Zener Voltage Vdc	Dynamic Impedance Ω		Reverse Current	
				I _r μ A _{dc}	V _r Vdc
				MAX	
K120	1.2	20	125	.05	0.4
K150	1.5	20	125	.05	0.5
K180	1.8	20	125	.05	0.6
K210	2.1	20	125	.05	0.9
K240	2.4	20	125	.05	1.2
K270	2.7	20	125	.05	1.7
K300	3.0	20	125	.05	1.9
K330	3.3	20	125	.05	2.2
K360	3.6	20	125	.05	2.5
K390	3.9	20	125	.05	2.7
K430	4.3	25	135	.05	3.1
K470	4.7	25	135	.05	3.5
K510	5.1	25	135	.05	3.8
Test Conditions	V _z @ I _z = 10 mA	Z _z @ I _z = 10 mA	Z _K @ I _z = 1 mA	@25° C	
Maximum Rating	Parameters	Value		Rating	
	DC Power Dissipation	@ T _a = 50° C		250 mW	
	Operating Temperature (T _{OPR})	-65 to + 175° C			
	Storage Temperature (T _{STG})	-65 to + 200° C			
	Voltage Tolerance:	Standard Device		+10%	
	Other package styles available				