

### General Description

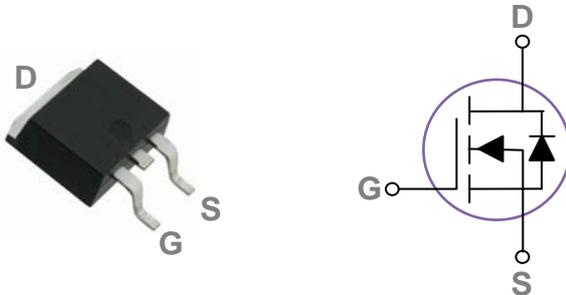
These N-Channel enhancement mode power field effect transistors are planar stripe, DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switch mode power supply

BVDSS	RDSON	ID
200V	130mΩ	20A

### Features

- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### TO252 Pin Configuration



### Applications

- High efficient switched mode power supplies
- TV Power
- Adapter/charger
- Networking
- PV Inverter / UPS

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	200	V
$V_{GS}$	Gate-Source Voltage	$\pm 30$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )	20	A
	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ )	13	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	80	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	420	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	29	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	60	W
	Power Dissipation – Derate above $25^\circ\text{C}$	0.48	W/ $^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	2.1	$^\circ\text{C}/\text{W}$

**Electrical Characteristics ( $T_J=25\text{ }^\circ\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	200	---	---	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=200V, V_{GS}=0V, T_J=25^\circ C$	---	---	1	$\mu A$
		$V_{DS}=160V, V_{GS}=0V, T_J=125^\circ C$	---	---	10	$\mu A$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 30V, V_{DS}=0V$	---	---	$\pm 100$	nA

**On Characteristics**

$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=8A$	---	100	130	m $\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	3	4	5	V
gfs	Forward Transconductance	$V_{DS}=30V, I_D=5A$	---	6.5	---	S

**Dynamic and switching Characteristics**

$Q_g$	Total Gate Charge <sup>3,4</sup>	$V_{DS}=160V, V_{GS}=10V, I_D=10A$	---	30	60	nC
$Q_{gs}$	Gate-Source Charge <sup>3,4</sup>		---	7.5	115	
$Q_{gd}$	Gate-Drain Charge <sup>3,4</sup>		---	11	22	
$T_{d(on)}$	Turn-On Delay Time <sup>3,4</sup>	$V_{DD}=100V, V_{GS}=10V, R_G=6\Omega, I_D=10A$	---	22	44	ns
$T_r$	Rise Time <sup>3,4</sup>		---	4	8	
$T_{d(off)}$	Turn-Off Delay Time <sup>3,4</sup>		---	44	88	
$T_f$	Fall Time <sup>3,4</sup>		---	6	12	
$C_{iss}$	Input Capacitance	$V_{DS}=25V, V_{GS}=0V, F=1MHz$	---	1330	2600	pF
$C_{oss}$	Output Capacitance		---	195	380	
$C_{riss}$	Reverse Transfer Capacitance		---	21	40	
$R_g$	Gate resistance	$V_{GS}=0V, V_{DS}=0V, F=1MHz$	---	2.3	4.6	$\Omega$

**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_S$	Continuous Source Current	$V_G=V_D=0V, \text{ Force Current}$	---	---	20	A
$I_{SM}$	Pulsed Source Current		---	---	40	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0V, I_S=1A, T_J=25^\circ C$	---	---	1	V
$t_{rr}$	Reverse Recovery Time <sup>3</sup>	$V_{GS}=0V, I_S=10A, di/dt=100A/\mu s$	---	125	---	ns
$Q_{rr}$	Reverse Recovery Charge <sup>3</sup>	$T_J=25^\circ C$	---	610	---	nC

**Note :**

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{DD}=50V, V_{GS}=10V, L=1mH, I_{AS}=29A, R_G=25\Omega, \text{ Starting } T_J=25^\circ C$ .
3. The data tested by pulsed , pulse width  $\leq 300\mu s$  , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.

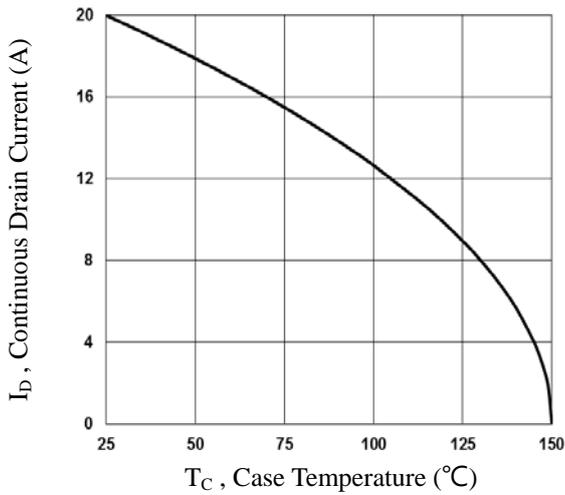


Fig.1 Continuous Drain Current vs.  $T_c$

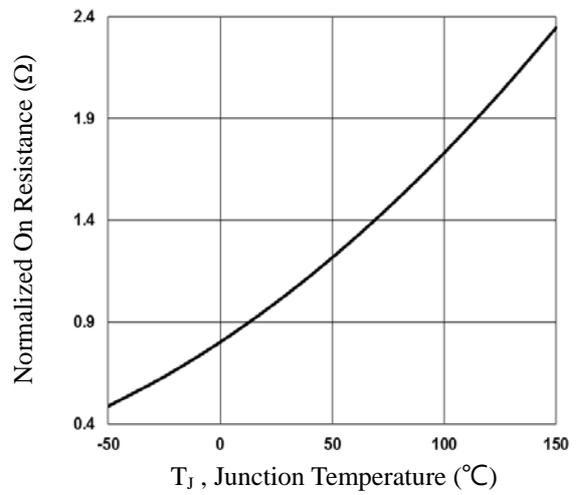


Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_j$

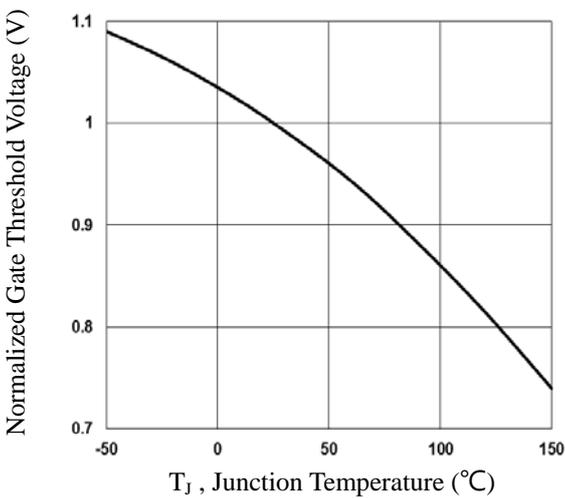


Fig.3 Normalized  $V_{th}$  vs.  $T_j$

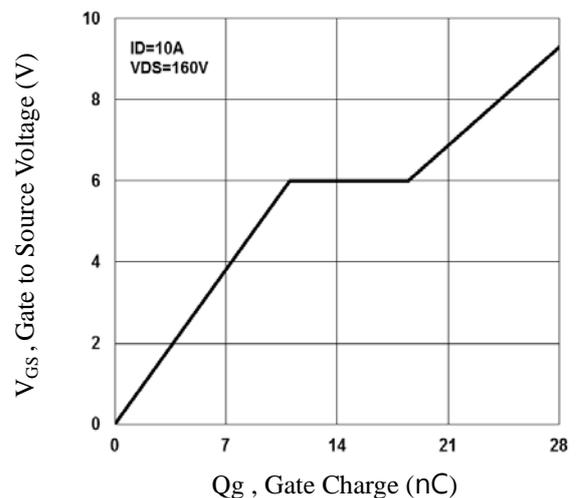


Fig.4 Gate Charge Waveform

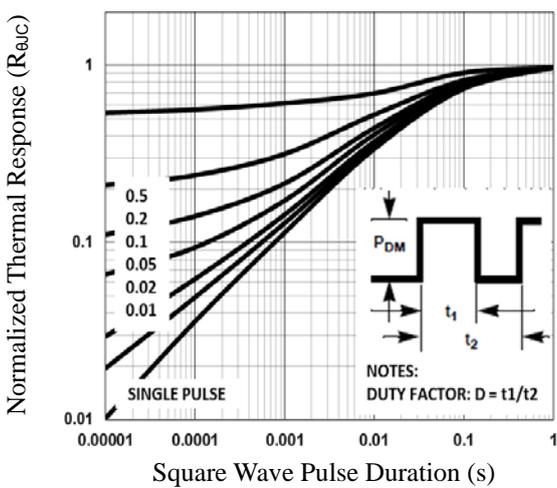


Fig.5 Normalized Transient Impedance

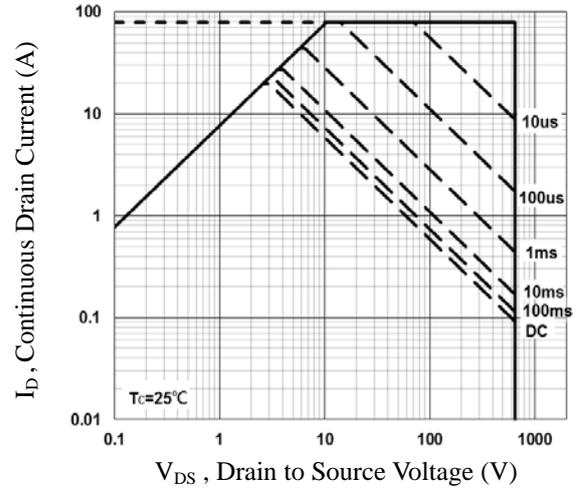


Fig.6 Maximum Safe Operation Area

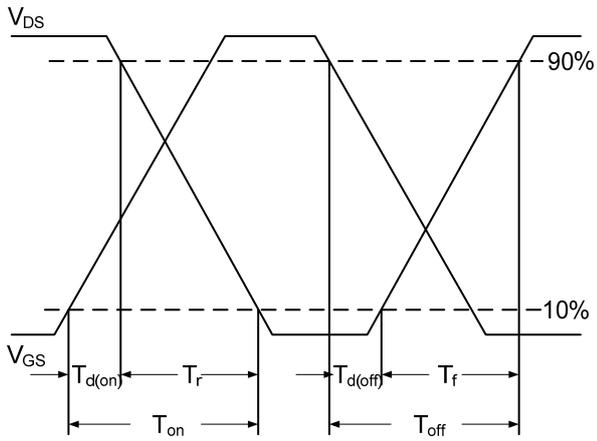


Fig.7 Switching Time Waveform

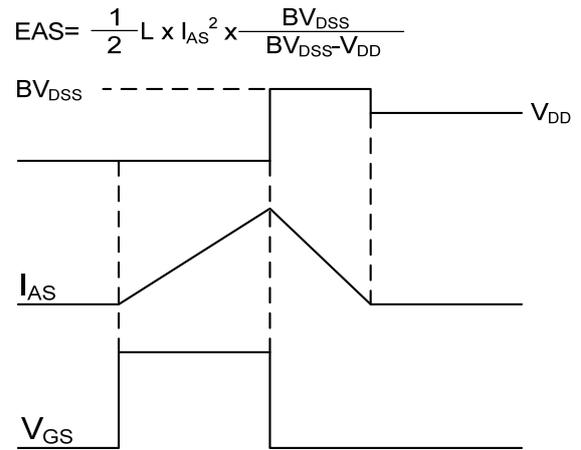
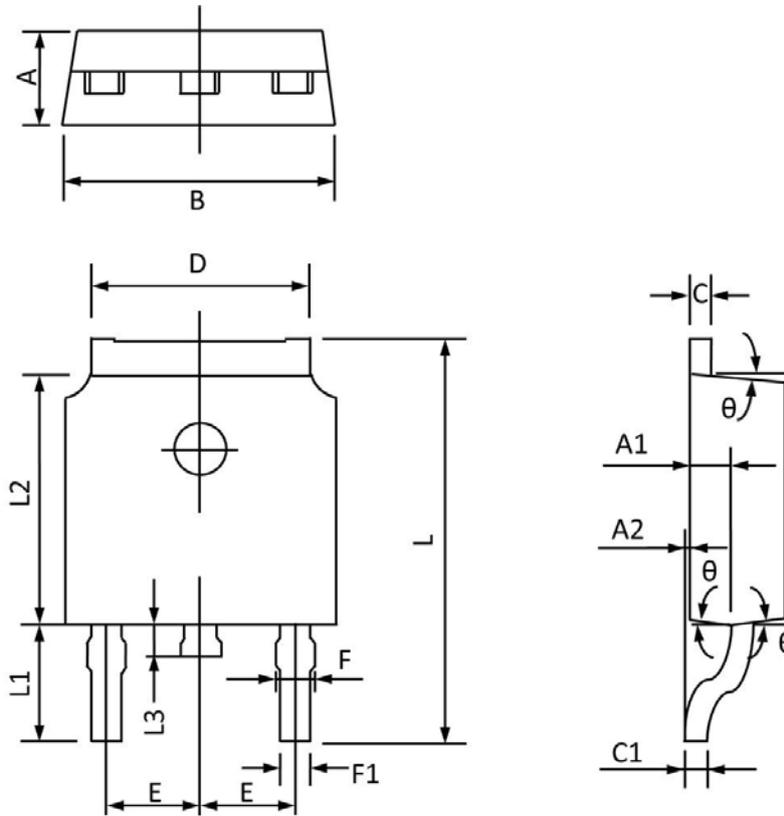


Fig.8 EAS Waveform

### TO252 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	2.20	2.40	0.087	0.094
A1	0.91	1.11	0.036	0.044
A2	0.00	0.15	0.000	0.006
B	6.50	6.70	0.256	0.264
C	0.46	0.580	0.018	0.230
C1	0.46	0.580	0.018	0.030
D	5.10	5.46	0.201	0.215
E	2.186	2.386	0.086	0.094
F	0.74	0.94	0.029	0.037
F1	0.660	0.860	0.026	0.034
L	9.80	10.40	0.386	0.409
L1	2.9REF		0.114REF	
L2	6.00	6.20	0.236	0.244
L3	0.60	1.00	0.024	0.039
$\theta$	3°	9°	3°	9°