

2N2192, A, B thru 2N2195, A, B (SILICON)



NPN silicon annular transistors for high-current switching and amplifier applications.

CASE 31 (TO-5)

Collector connected to case

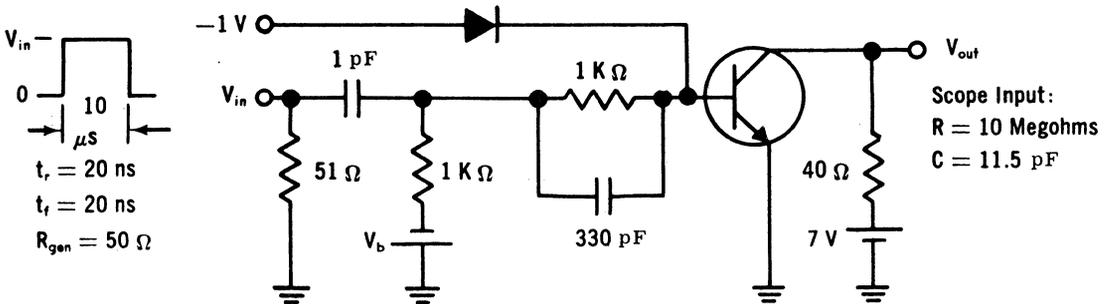
MAXIMUM RATINGS

Rating	Symbol	2N2192 2N2192A 2N2192B 2N2194 2N2194A 2N2194B	2N2193 2N2193A 2N2193B	2N2195 2N2195A 2N2195B	Unit
Collector-Base Voltage	V_{CB}	60	80	45	Vdc
Collector-Emitter Voltage	V_{CEO}	40	50	25	Vdc
Emitter-Base Voltage	V_{EB}	5.0	8.0	5.0	Vdc
Collector Current	I_C	1.0	1.0	1.0	Adc
Total Device Dissipation @ 25°C Ambient Temperature Derating Factor Above 25°C	P_D	0.8 4.56	0.8 4.56	0.6 3.43	Watt mW/°C
Total Device Dissipation @ 25°C Case Temperature Derating Factor Above 25°C	P_D	\longleftrightarrow 2.8 \longleftrightarrow \longleftrightarrow 16 \longleftrightarrow			Watts mW/°C
Junction Temperature, Operating	T_J	-65 to +200			°C
Storage Temperature Range	T_{stg}	-65 to +200			°C

FIGURE 1

2N2193, A, B } $V_{in} = 15\text{ V}, V_b = 15\text{ V}$
 2N2194, A, B }

2N2192, A, B - $V_{in} = 7.5\text{ V}, V_b = 7.5\text{ V}$



2N2192,A,B thru 2N2195,A,B (continued)

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
Collector-Base Breakdown Voltage ($I_C = 100 \mu\text{A}$, $I_E = 0$)	BV_{CBO}	60 80 45	-	Vdc
Collector Emitter-Open Base Sustain Voltage ⁽¹⁾ ($I_C = 25 \text{ mA}$ pulsed, $I_B = 0$)	$V_{CEO(sus)}$	40 50 25	-	Vdc
Emitter-Base Breakdown Voltage ($I_E = 100 \mu\text{A}$, $I_C = 0$)	BV_{EBO}	5.0 5.0 8.0	-	Vdc
Collector Cutoff Current ($V_{CB} = 30 \text{ Vdc}$, $I_E = 0$)	I_{CBO}	-	0.010 0.100	μA
($V_{CB} = 30 \text{ Vdc}$, $I_E = 0$, $T_A = 150^\circ\text{C}$)		-	15 25 50	
($V_{CB} = 60 \text{ Vdc}$, $I_E = 0$)		-	0.010	
($V_{CB} = 60 \text{ Vdc}$, $I_E = 0$, $T_A = 150^\circ\text{C}$)		-	25	
Emitter Cutoff Current ($V_{EB} = 3 \text{ Vdc}$, $I_C = 0$)	I_{EBO}	-	0.050 0.100	μA
($V_{EB} = 5 \text{ Vdc}$, $I_C = 0$)		-	0.050	
Collector-Emitter Saturation Voltage ($I_C = 150 \text{ mA}$, $I_B = 15 \text{ mA}$)	$V_{CE(sat)}$	-	0.35 0.25 0.18	Vdc
Base-Emitter Saturation Voltage ($I_C = 150 \text{ mA}$, $I_B = 15 \text{ mA}$)	$V_{BE(sat)}$	-	1.3	Vdc
DC Current Gain ⁽¹⁾ ($I_C = 0.1 \text{ mA}$, $V_{CE} = 10 \text{ Vdc}$)	h_{FE}	15	-	-
($I_C = 10 \text{ mA}$, $V_{CE} = 10 \text{ Vdc}$)		75 30 15	-	
($I_C = 10 \text{ mA}$, $V_{CE} = 10 \text{ Vdc}$, $T_A = -55^\circ\text{C}$)		35 20	-	
($I_C = 150 \text{ mA}$, $V_{CE} = 10 \text{ Vdc}$)		100 40 20 20	300 120 60	
($I_C = 150 \text{ mA}$, $V_{CE} = 1.0 \text{ Vdc}$)		70 30 15 10	-	
($I_C = 500 \text{ mA}$, $V_{CE} = 10 \text{ Vdc}$)		35 20 12	-	
($I_C = 1.0 \text{ A}$, $V_{CE} = 10 \text{ Vdc}$)		15	-	
Output Capacitance ($V_{CB} = 10 \text{ Vdc}$, $I_E = 0$, $f = 1.0 \text{ MHz}$)	C_{ob}	-	20	pF
Small Signal Current Gain ($I_C = 50 \text{ mA}$, $V_{CE} = 10 \text{ V}$, $f = 20 \text{ MHz}$)	h_{fe}	2.5	-	-
Rise Time	t_r	-	70	ns
Storage Time	t_s	-	150	ns
Fall Time	t_f	-	50	ns

⁽¹⁾ Pulse Test: $PW \leq 300 \mu\text{s}$ Duty Cycle $\leq 2\%$