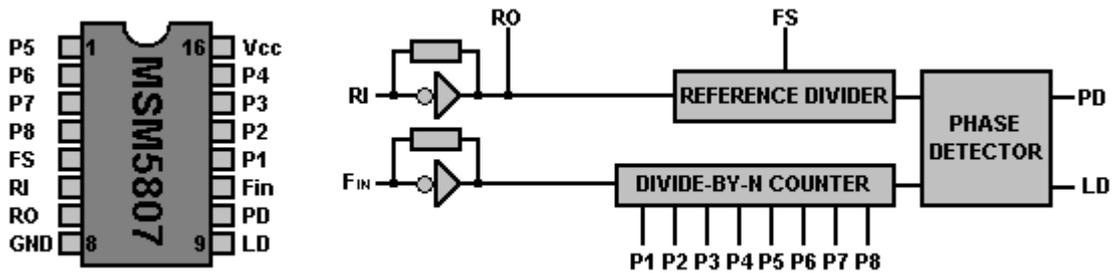
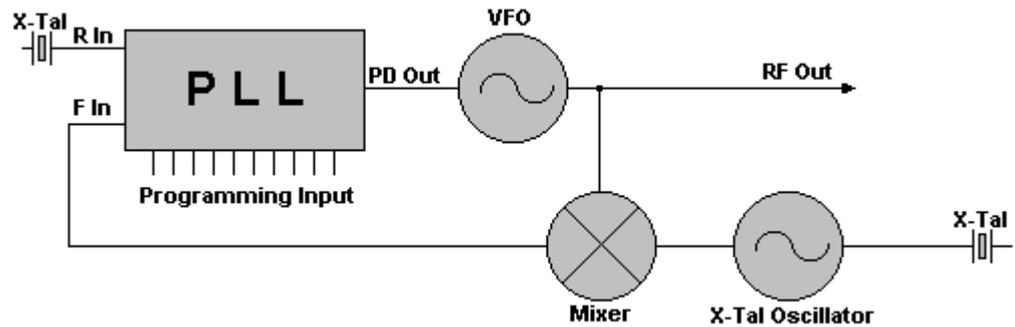


MSM5807 PLL Frequency Synthesizer



Overview - This PLL-circuit use a 8 bit BCD binary programmable divide-by-N counter.

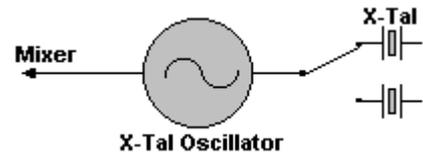
Down-converting of the frequency to the divider



This PLL Circuit use a Mixer and a X-Tal Oscillator to convert the output frequency f_{OUT} to the f_{IN} to the PLL Circuit.

The X-Tal frequency is $f_{XTAL} = f_{OUT} - f_{IN}$

The output frequency can be changed by changing the mixing-xtal or add a new mixing-xtal to the oscillator.



PINOUT Description

Pin	Name	Description
1	P5	Programmable input 5
2	P6	Programmable input 6
3	P7	Programmable input 7
4	P8	Programmable input 8
5	FS	Function Select - HIGH=Divided by 512 LOW=Divided by 1024
6	RI	Reference Oscillator Input
7	RO	Reference Oscillator Output
8	GND	Ground
9	PD	Phase Detector Output
10	LD	Loop Detect - Unlocked=HIGH Locked=HIGH
11	F in	VCO Frequency Input
12	P1	Programmable input 1
13	P2	Programmable input 2
14	P3	Programmable input 3
15	P4	Programmable input 4
16	Vcc	Positive Supply Voltage