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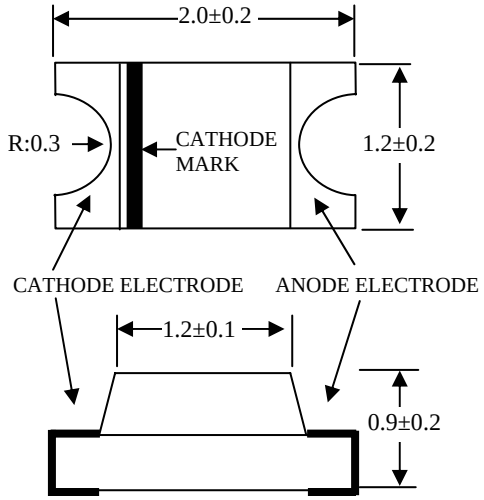
SUPER SWITCHING CHIP DIODE

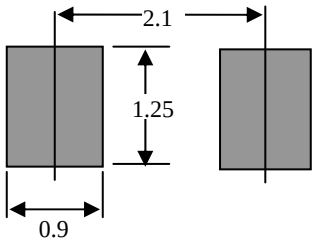
SSD355

FEATURES
- SUPER SWITCHING
- CHIP DIODE SIZE
- LOW STORED CHARGE

MECHANICAL DATA
- CASE: SOD-323, MINI MELF, DIMENSIONS IN MILLIMETERS
- TERMINALS: SOLDERABLE PER MIL-STD 750 METHOD 2026
- POLARITY: CATHODE INDICATED BY COLOR BAND



Recommended Land Pattern Dimensions



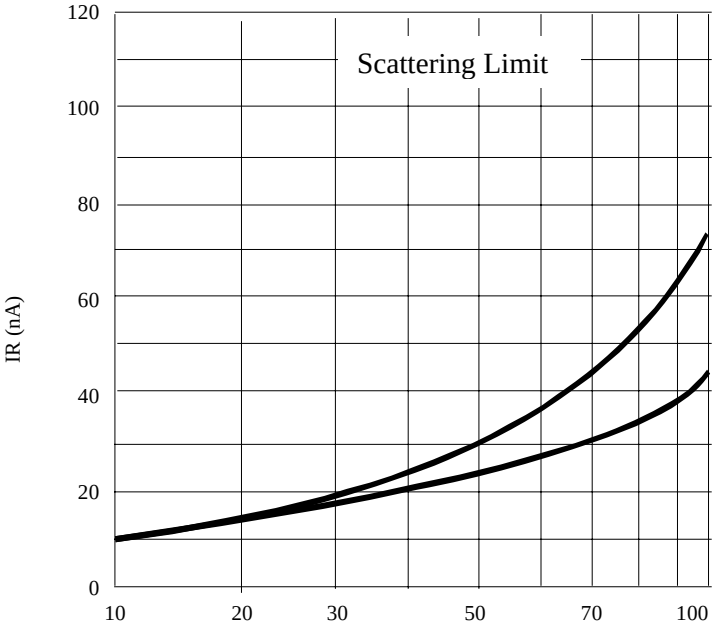
RATINGS	SYMBOL	SSD355	UNITS
CONTINUOUS REVERSE VOLTAGE	V_R	100	V
RECTIFIED CURRENT (AVERAGE) HALF WAVE RECTIFICATION WITH RESIST LOAD AT T_{amb} = 25℃ AND f ≧ 50HZ (NOTE 1)	I_O	100	mA
SURGE FORWARD CURRENT AT T < 1 s AND T_J = 25℃	I_{FSM}	500	mA
POWER DISSIPATION AT T_{amb}=25℃	P_{TOT}	350	mW
JUNCTION TEMPERATURE	T_J	175	℃
STORAGE TEMPERATURE RANGE	T_S	- 55 TO + 175	℃

CHARACTERISTICS T_J=25℃	SYMBOL	MIN	TYP	MAX	UNITS
FORWARD VOLTAGE AT I_F=100mA	V_F	-	-	1	V
LEAKAGE CURRENT					
AT V_R=25V	I_R	-	-	30	nA
AT V_R=80V	I_R	-	-	0.1	μA
REVERSE BREAKDOWN VOLTAGE TESTED WITH 100μA PULSES	V_R	100	-	-	V
CAPACITANCE AT V_F=V_R=0 , f = 1.0 MHz	C_D	-	-	1.5	PF
REVERSE RECOVERY TIME FROM I_F=10mA TO I_R=1mA V_R=6V R_L=100Ω	T_{RR}	-	-	4	nS
THERMAL RESISTANCE JUNCTION TO AMBIENT AIR (NOTE 1)	R_{THA}	-	-	0.35	K / mW

NOTE: 1. LEADS KEPT AT AMBIENT TEMP.

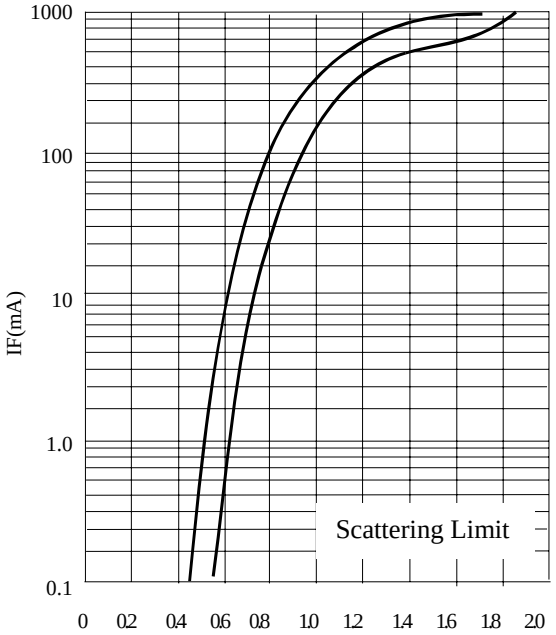
RATINGS AND CHARACTERISTIC CURVE SSD355

Fig. 1-REVERSE CHARACTERISTICS



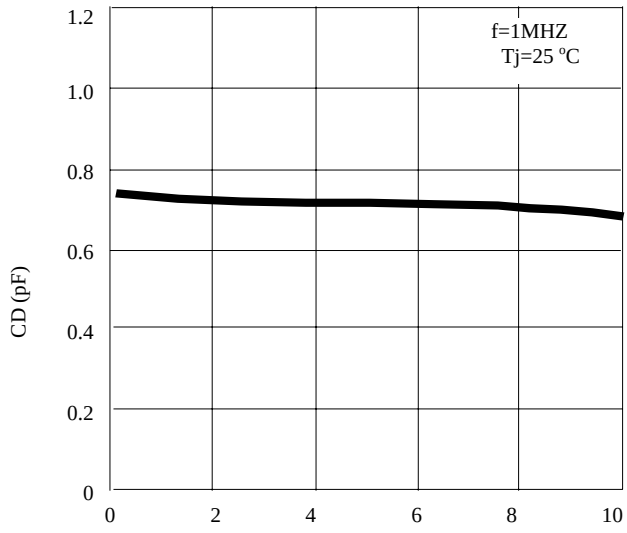
Typical Leakage Current Versus Reverse Voltage , VR (V)

Fig. 2-FORWARD CHARACTERISTICS



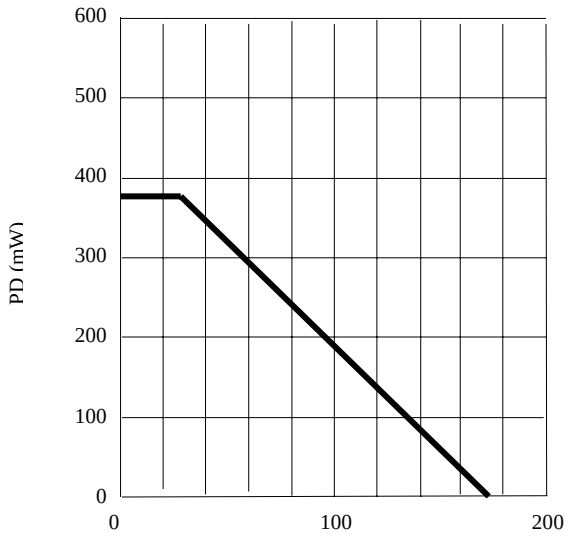
Typical Forward Current Versus Forward Voltage , VF (V)

ODES CAPACITANCE



Typical Diodes Capacitance Versus Reverse Voltage , VR(V)

Fig. 4-DERATING CURVE



Power Dissipation Versus Ambient Temperature
Ta (ºC)