

SS34L THRU SS36L

SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

Reverse Voltage - 40 to 60 Volts Forward Current - 3.0 Ampere

FEATURES

- ◆ Low profile surface mount package
- ◆ Built-in strain relief
- ◆ High switching speed, low V_F
- ◆ Low voltage drop, high efficiency
- ◆ For use in low voltage high frequency inverters,

Free willing, and polarity protection applications

- ◆ Guarding for over voltage protection

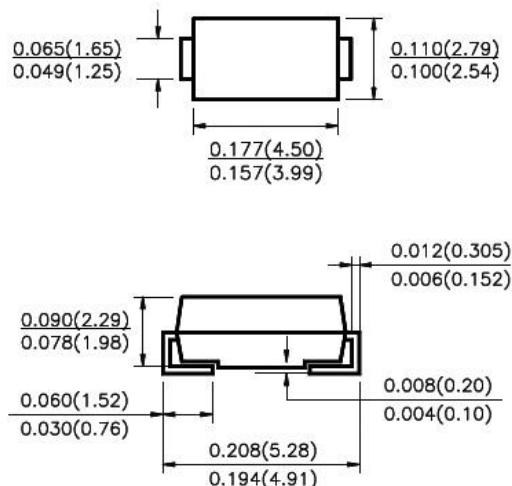
Mechanical Data

- ◆ Case: Transfer molded plastic
- ◆ Epoxy :UL 94V-0 rate flame retardant
- ◆ Lead: Solder plated, solderable per MIL-STD-750

method 2026

- ◆ Polarity: Color band denotes cathode end

DO-214AC (SMA)



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%

		SYMBOLS	SS34L	SS36L	UNIT
Maximum Repetitive Peak Reverse Voltage		V _{RRM}	40	60	VOLTS
Maximum RMS Voltage		V _{RMS}	28	42	VOLTS
Maximum DC Blocking Voltage		V _{DC}	40	60	VOLTS
Maximum Average Forward Rectified Current at T _L =75℃		I _(AV)	3		Amps
Peak Forward Surge Current, 8.3 ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)		I _{FSM}	70		Amps
Maximum Instantaneous Forward Voltage @ 3.0A(Note 1)		V _F	0.45	0.65	Volts
Maximum DC Reverse Current at rated DC Blocking voltage per element	T _A = 25℃	I _R	0.5		mA
	T _A = 100℃		20		
Typical Thermal Resistance (Note 2)		R _{θJA}	75		℃/W
		R _{θJL}	17		
Operating Junction Temperature		T _J	-55 to +125		℃
Storage Temperature Range		T _{STG}	-55 to +150		℃

Note: 1. Pulse test:300 μ S pulse width,1% duty cycle.2. PCB mounted with 0.2" $\times$ 0.2"(5.0 $\times$ 5.0mm)copper pads.

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RATING AND CHARACTERISTIC CURVES SS34L THRU SS36L

FIG.1-TYPICAL FORWARD CURRENT
DERATING CURVE

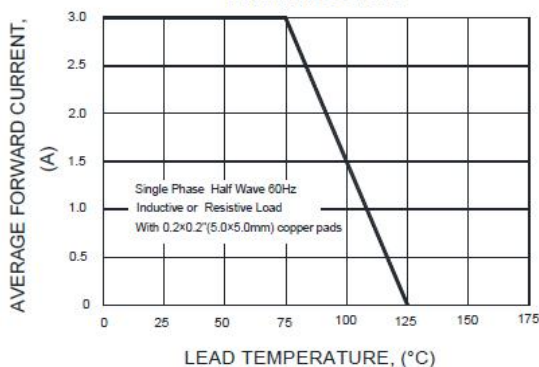


FIG.2-MAXIMUM NON-REPETITIVE PEAK
FORWARD SURGE CURRENT

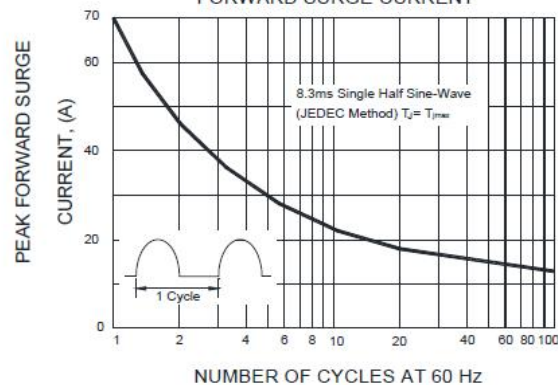


FIG.3-TYPICAL INSTANTANEOUS
FORWARD CHARACTERISTICS

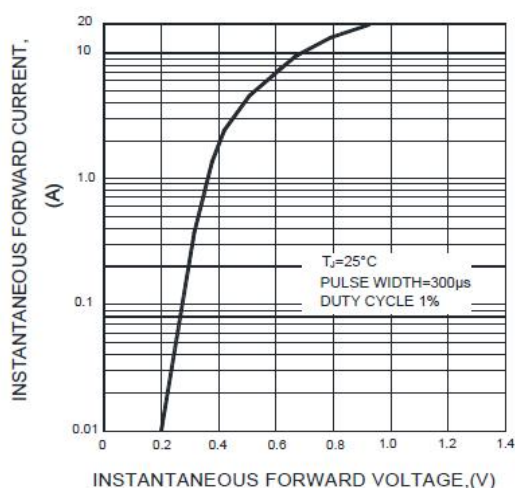


FIG.4-TYPICAL REVERSE
CHARACTERISTICS

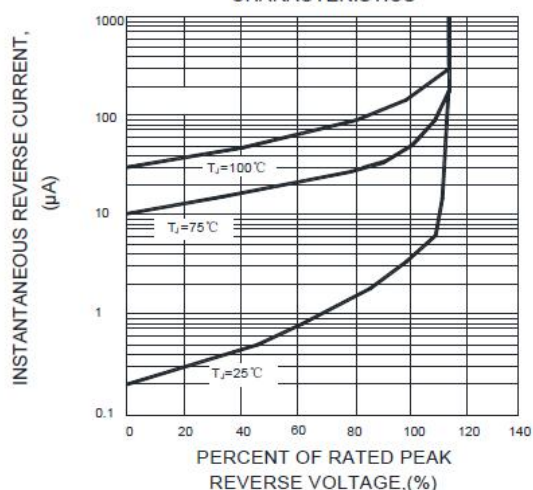
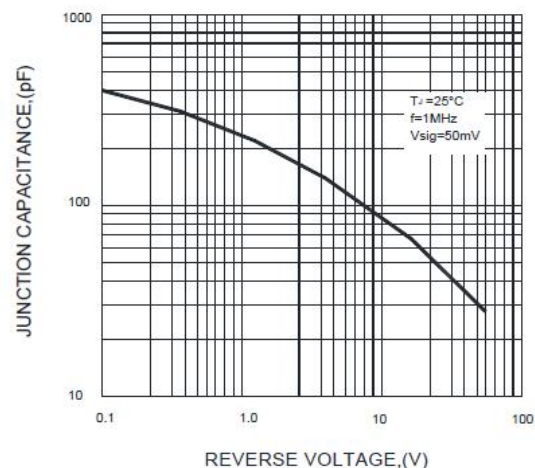


FIG.5-TYPICAL JUNCTION CAPACITANCE



Note: Specifications are subject to change without notice. For more detail and update, please visit our website.