

ANALOG PWM IC

1. General Description

G1132A is a high performance offline PWM Power switch for low power AC/DC charger and adaptor applications.

It operates in primary-side sensing and regulation. Consequently, Opto-coupler and TL431 could be eliminated. Proprietary Constant Voltage (CV) and Constant

Current (CC) control is integrated as shown in the Fig. 1. In CC control, the current and output power setting can be adjusted externally by the sense resistor R_S at CS pin. In CV control, multi-mode operations are utilized to achieve high performance and high efficiency.

In addition, good load regulation is achieved by the built-in cable drop compensation. Device operates in PFM in CC mode as well at large load condition and it operates in PWM with frequency reduction at light/medium load.

G1132A offers power on soft start control and protection coverage with auto-recovery features including Cycle-by-Cycle current limiting, VDD OVP, VDD clamp and UVLO. Excellent EMI performance is achieved with frequency jitter technique.

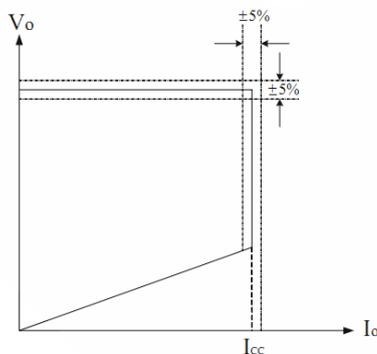


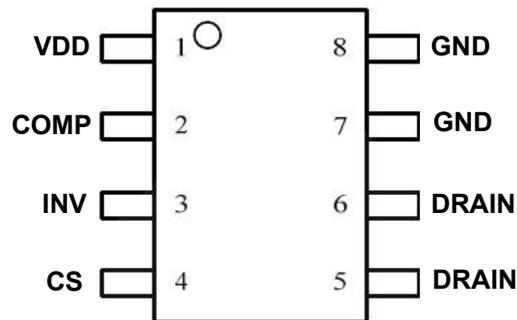
Fig.1. Typical CC/CV Curve

Features

- ◆ 5% Constant Voltage Regulation, 5% Constant Current Regulation at Universal AC input
- ◆ Primary-side Sensing and Regulation Without TL431 and Opto-coupler
- ◆ Built-in High-Voltage Power MOS
- ◆ Power on Soft-start
- ◆ Internal Cable Compensation
- ◆ Built-in Leading Edge Blanking (LEB)
- ◆ Cycle-by-Cycle Current Limiting
- ◆ VDD Under Voltage Lockout with Hysteresis (UVLO)
- ◆ Programmable CV and CC Regulation
- ◆ Adjustable Constant Current and Output Power Setting
- ◆ Built-in Secondary Constant Current Control with Primary Side Feedback
- ◆ Built-in adaptive current peak regulation
- ◆ Built-in Primary winding inductance compensation
- ◆ Program cable drop compensation
- ◆ VDD OVP and VDD Clamp
- ◆ 5000V HBM ESD
- ◆ Pb-free SOP-8L

Applications

- Cell Phone /Digital Cameras Charger
- Small Power Adaptor
- Auxiliary Power for PC, TV etc
- Linear Regulator/RCC Replacement

2. Products Information**2.1 Pin configuration**

Pin Configuration: G1132A

Pin Name	I/O	Description
VDD	P	Power Supply
COMP	I	Loop Compensation for CV Stability
INV	I	The Voltage feedback from auxiliary winding. Connected to resistor divider from auxiliary winding reflecting output voltage. PWM duty cycle is determined by EA output and current sense signal at pin 3
CS	I	Current sense input. Connected to MOSFET current sensing resistor node
DRAIN	O	HV MOSFET Drain Pin. The Drain pin is connected to the primary lead of the transformer
GND	P	Ground

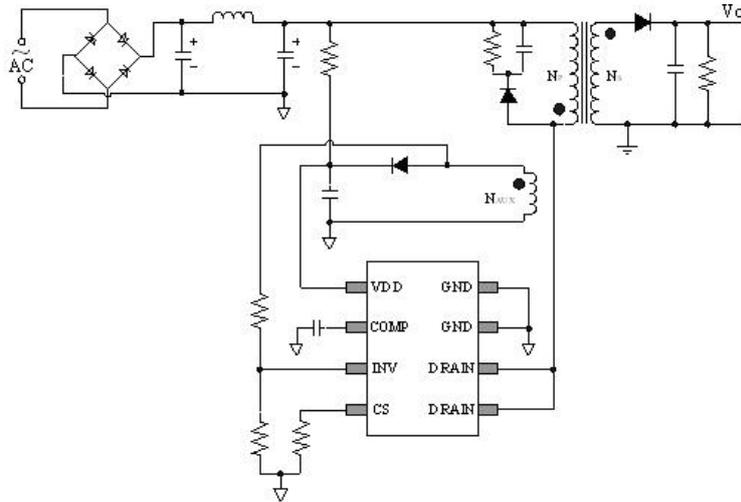
3. Absolute Maximum Ratings

Description	Absolute Maximum Ratings
VDD Voltage	-0.3 to VDD_clamp
HV MOSFET Drain Voltage	-0.3 to BVdss
VDD Zener Clamp Continuous Current	10 mA
COMP Voltage	-0.3 to 7V
CS Input Voltage	-0.3 to 7V
INV Input Voltage	-0.3 to 7V
HBM ESD	3000V
MM ESD	500V
Min/Max Operating Junction Temperature T _J	-20 to 150°C
Min/Max Storage Temperature T _{stg}	-55 to 150°C
Lead Temperature (Soldering, 10secs)	220°C
Operating Junction Temperature Range	-55 to + 150°C

Note: Stress beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. Exposure to absolute maximum-rated conditions for extended periods may affect device reliability



4. Typical Application



5. Electrical Characteristics

(TA = 25°C, VDD=16V, unless otherwise noted)

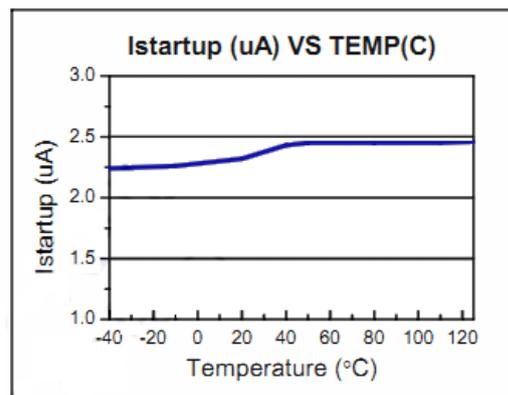
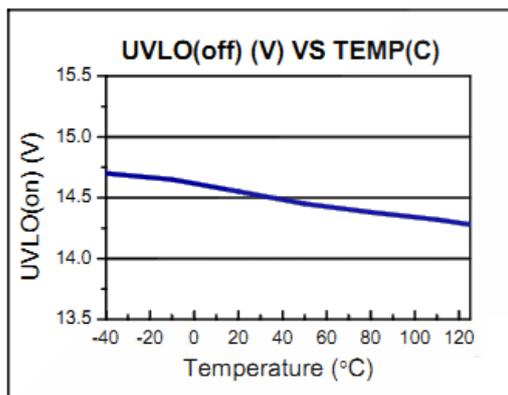
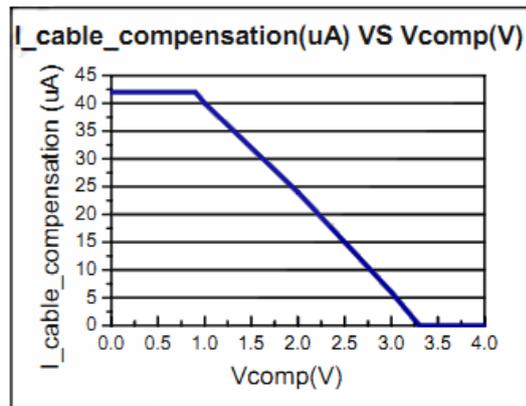
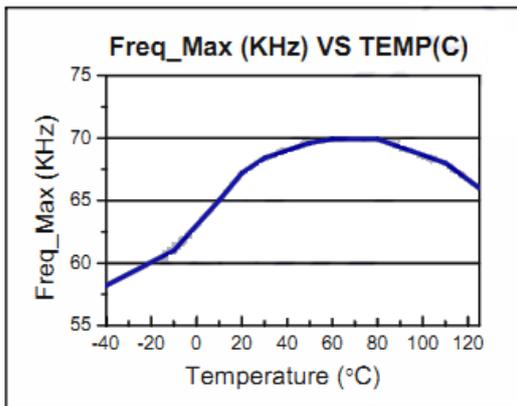
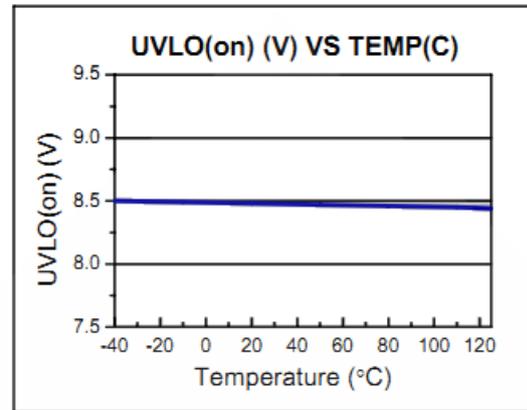
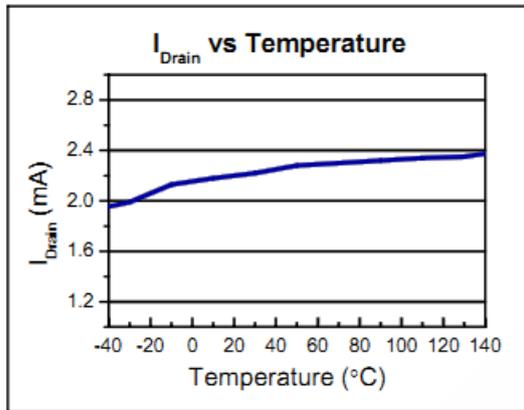
Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Supply Voltage						
IDD_ST	Standby current	VDD=13V		5	10	uA
IDD_OP	Operation Current	INV=2V,CS=0V, VDD =20V		2.5	3.5	mA
UVLO(ON)	VDD Under Voltage Lockout Enter	VDD falling	7.5	8.5	10	V
UVLO(OFF)	VDD Under Voltage Lockout Exit	VDD rising	13.5	14.5	16.0	V
OVP	Over voltage protection voltage	Ramp up VDD until gate clock is off	27.5	29.5	31	V
V_clamp	Maximum VDD operation voltage	IDD=10mA	30.5	32.5	34.5	V
Current Sense						
TLEB	LEB time			540		ns
Vth_oc	Over current threshold		880	910	940	mV
Td_oc	OCP Propagation delay			150		nS
ZSENSE_IN	Input Impedance			50		KΩ
T_ss	Soft start time			10		mS
CV Section						
Freq_Nom	System Nominal switch frequency			60		KHz
Freq_startup		INV=0V,Comp=5V		14		KHz
Δf/Freq	Frequency jitter range			+/-4		%
Error Amplifier Section						

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High Precision CC/CV Primary Side PWM Controller

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Supply Voltage						
Vref_EA	Reference voltage for EA		1.97	2	2.03	V
Gdc	DC gain of the EA			60		dB
I_COMP_M AX	Max. Cable compensation current	INV=2V,COMP=0V		40		uA
Power MOS Section						
BVdss	MOSFET Drain Breakdown Voltage	VGS=0V, IDS=250uA	600			V
R	Static Drain-Source On-Resistance			8.6	9	Ω
OTP Section						
T	Over Temperate Protection			160		$^{\circ}\text{C}$
Frequency Section						
Δf_{OSC}	Frequency Jittering			+/-4		%

6. CHARACTERIZATION



7. OPERATION DESCRIPTION

G1132A is a cost effective PWM power switch optimized for off-line low power AC/DC applications including battery chargers and adaptors. It operates in primary side sensing and regulation, thus opto-coupler and TL431 are not required. Proprietary built-in CV and CC control can achieve high precision CC/CV control meeting most adaptor and charger application requirements.

• Startup Current and Start up Control

Startup current of G1132A is designed to be very low so that VDD could be charged up above UVLO threshold and starts up quickly. A large value startup resistor can therefore be used to minimize the power loss in application.

• Operating Current

The Operating current of G1132A is as low as 2.5mA. Good efficiency is achieved with the low operating current together with Multi-mode control features.

• Soft Start

G1132A features an internal soft start to minimize the component electrical over-stress during power on startup. As soon as VDD reaches UVLO (OFF), the control algorithm will ramp peak current voltage threshold gradually from nearly zero to normal setting of 0.91V. Every restart is a soft start.

• CC/CV Operation

G1132A is designed to produce good CC/CV control characteristic as shown in the Fig.1.

In charger applications, a discharged battery charging starts in the CC portion of the curve until it is nearly full charged and smoothly switches to operate in CV portion of the curve.

In an AC/DC adapter, the normal operation occurs only on the CV portion of the curve. The CC portion provides output current limiting. In CV operation, the output voltage is regulated through the primary side control. In CC operation mode, G1132A will regulate the output current constant regardless of the output voltage drop.

• Principle of Operation

To support G1132A proprietary CC/CV control, system needs to be designed in DCM mode for flyback system (Refer to Typical Application Diagram).

In the DCM flyback converter, the output voltage can be sensed via the auxiliary winding. During MOSFET turn-on time, the load current is supplied from the output filter capacitor CO. The current in the primary winding ramps up. When MOSFET turns off, the primary current transfers to the secondary at the amplitude of

$$I_S = \frac{N_P}{N_S} \cdot I_P \quad (1)$$

The auxiliary voltage reflects the output voltage as shown in fig.2 and it is given by

$$V_{AUX} = \frac{N_{AUX}}{N_S} (V_O + \Delta V) \quad (2)$$

Where ΔV indicates the drop voltage of the output Diode.

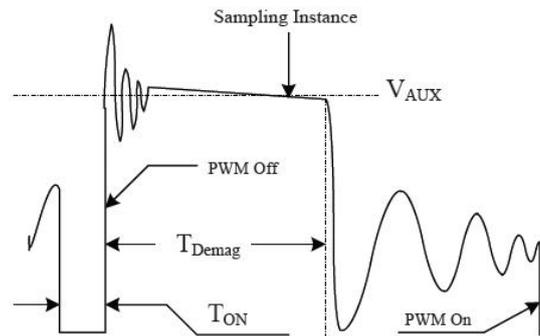


Fig.2. Auxiliary voltage waveform

Via a resistor divider connected between the auxiliary winding and INV (pin 3), the auxiliary voltage is sampled at the end of the demagnetization and it is hold until the next sampling.

The sampled voltage is compared with V_{ref} (2.0V) and the error is amplified. The error amplifier output COMP reflects the load condition and controls the PWM switching frequency to regulate the output voltage, thus constant output voltage can be achieved.

When sampled voltage is below V_{ref} and the error amplifier output COMP reaches its maximum, the switching frequency is controlled by the sampled voltage thus the output voltage to regulate the output current, thus the constant output current can be achieved.

• Adjustable CC point and Output Power

In G1132A, the CC point and maximum output power can be externally adjusted by external current sense resistor R_S at CS pin as illustrated in Typical Application Diagram. The output power is adjusted through CC point change. The larger R_S , the smaller CC point is, and the smaller output power becomes, and vice versa as shown in Fig.3.

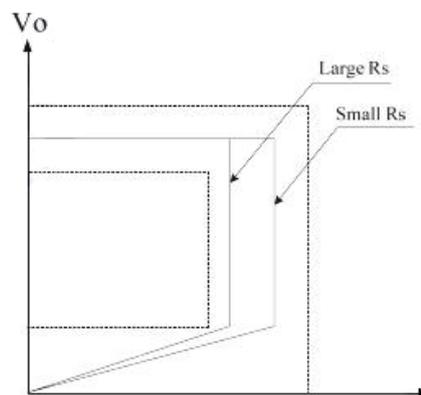


Fig.3. Adjustable output power by changing R_S

• Operation switching frequency

The switching frequency of G1132A is adaptively controlled according to the load conditions and the operation modes. No external frequency setting components are required. The operation switching frequency at maximum output power is set to 60KHz internally.

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For flyback operating in DCM, The maximum output power is given by

$$P_{O_{MAX}} = \frac{1}{2} L_p F_{SW} I_p^2 \quad (3)$$

Where L_p indicate the inductance of primary winding and I_p is the peak current of primary winding. Refer to the equation 3, the change of the primary winding inductance results in the change of the maximum output power and the constant output current in CC mode. To compensate the change from variations of primary winding inductance, the switching frequency is locked by an internal loop such that the switching frequency is

$$F_{SW} = \frac{1}{2T_{demag}} \quad (4)$$

Since T_{demag} is inversely proportional to the inductance, as a result, the product L_p and F_{sw} is constant, thus the maximum output power and constant current in CC mode will not change as primary winding inductance changes. Up to +/-10% variation of the primary winding inductance can be compensated.

• Frequency jitter for EMI improvement

The frequency jitter (switching frequency modulation) is implemented in G1132A. The oscillation frequency is modulated so that the tone energy is spread out. The spread spectrum minimizes the conduction band EMI and therefore eases the system design.

• Current Sensing and Leading Edge Blanking

Cycle-by-Cycle current limiting is offered in G1132A current mode PWM control. The switch current is detected by a sense resistor into the CS pin. An internal leading edge blanking circuit chops off the sensed voltage spike at initial internal power MOSFET on state so that the external RC filtering on sense input is no longer needed. The PWM duty cycle is determined by the current sense input voltage and the EA output voltage.

• Programmable Cable drop Compensation

In G1132A, cable drop compensation is implemented to achieve good load regulation. An offset voltage is generated at INV by an internal current flowing into the resistor divider. The current is inversely proportional to the voltage across pin COMP, as a result, it is inversely proportional to the output load current, thus the drop due to the cable loss can be compensated. As the load current decreases from full-load to no-load, the offset voltage at INV will increase. It can also be programmed by adjusting the resistance of the divider to compensate the drop for various cable lines used.

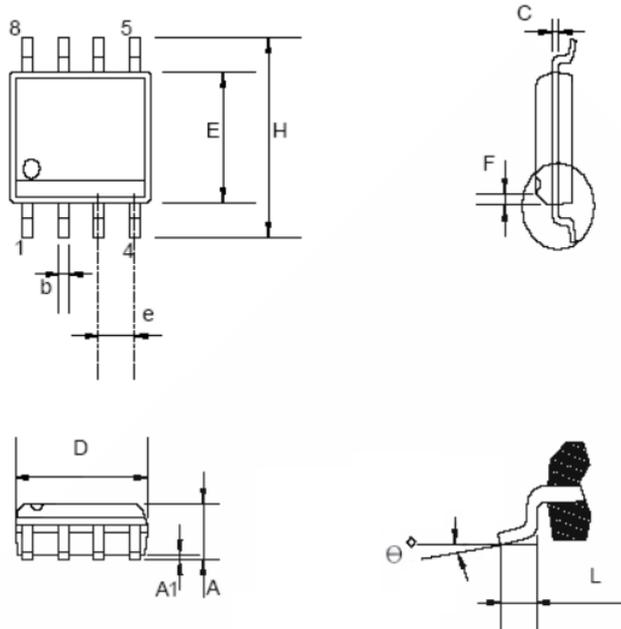
• Protection Control

Good power supply system reliability is achieved with its rich protection features including Cycle-by-Cycle current limiting (OCP), VDD clamp, Power on Soft Start, and Under Voltage Lockout on VDD(UVLO). VDD is supplied by transformer auxiliary winding output. The output of G1132A is shut down when VDD drops below UVLO (ON) limit and Switcher enters power on start-up sequence thereafter.



8. Package Information

SOP-8L



Symbol	Dimension in Millimeters			Dimensions in Inches		
	Min	Type	Max	Min	Type	Max
A	1.346		1.752	0.053		0.069
A1	0.101		0.254	0.004		0.010
b	0.380		0.510	0.015		0.020
c	0.170		0.230	0.007	0.008	0.009
D	4.648		4.978	0.183		0.196
E	3.810		3.987	0.150		0.157
e	1.016	1.270	1.524	0.040	0.050	0.060
F		0.381X45°			0.015X45°	
H	5.791		6.197	0.228		0.244
L	0.406		1.270	0.016		0.050
θ	0°		8°	0°		8°

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 This product has been designed and qualified for Industrial Level and Lead-Free.
 Qualification Standards can be found on GS's Web site.
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