Dual P-Channel MOSFET

BSS84S6R

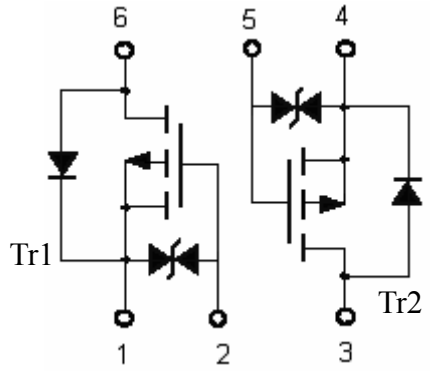
BV_{DSS}	-50V
I_D	-170mA
R_{DSON}@V_{GS}=-10V, I_D=-100mA	5Ω (typ)
R_{DSON}@V_{GS}=-5V, I_D=-100mA	6Ω (typ)
R_{DSON}@V_{GS}=-3V, I_D=-30mA	8Ω (typ)

Features

- Low on-resistance
- High ESD capability
- High speed switching
- Low-voltage drive(-2.5V)
- Pb-free package

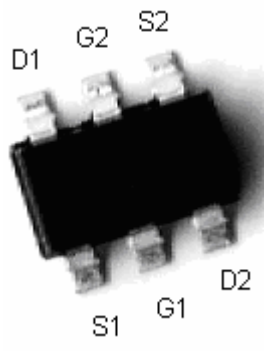
Equivalent Circuit

BSS84S6R



Outline

SOT-363R



The following characteristics apply to both Tr1 and Tr2

Absolute Maximum Ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit
Drain-Source Voltage		V_{DS}	-50	V
Gate-Source Voltage		V_{GS}	±20	
Continuous Drain Current @ T_A=25°C, V_{GS}=-5V (Note 3)		I_D	-170	mA
Continuous Drain Current @ T_A=85°C, V_{GS}=-5V (Note 3)			-120	
Pulsed Drain Current (Notes 1, 2)		I_{DM}	-800	mA
Maximum Power Dissipation (Note 3)	T_A=25℃	P_D	300	mW
	T_A=85℃		160	
Operating Junction and Storage Temperature		Tj, Tstg	-55~+150	°C

Note : 1. Pulse width limited by maximum junction temperature.
2. Pulse width≤ 300μs, duty cycle≤2%.
3.Surface mounted on 1 in² copper pad of FR-4 board, t≤5s.

Thermal Performance

Parameter	Symbol	Limit	Unit
Thermal Resistance, Junction-to-Ambient(PCB mounted) (Note)	Rth,ja	415	°C/W

Note : Surface mounted on 1 in² copper pad of FR-4 board, t≤5s.

Electrical Characteristics (Tj=25°C, unless otherwise noted)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV_{DSS}	-50	-	-	V	V_{GS}=0, I_D=-250μA
$V_{GS(th)}$	-1	-1.4	-2	V	V_{DS}=V_{GS}, I_D=-250μA
I_{GSS}	-	-	±8	μA	V_{GS}=±20V, V_{DS}=0
I_{DSS}	-	-	1	μA	V_{DS}=50V, V_{GS}=0
	-	-	10	μA	V_{DS}=40V, V_{GS}=0 (Tj=70°C)
*$R_{DS(ON)}$	-	5	7	Ω	V_{GS}=-10V, I_D=-100mA
	-	6	8.5	Ω	V_{GS}=-5V, I_D=-100mA
	-	8	12	Ω	V_{GS}=-3V, I_D=-30mA
*G_{FS}	80	-	-	mS	V_{DS}=-10V, I_D=-100mA
Dynamic					
Ciss	-	24	-	pF	V_{DS}=-25V, V_{GS}=0, f=1MHz
Coss	-	4.6	-	pF	
Crss	-	1.5	-	pF	
$t_{d(ON)}$	-	2.7	-	ns	V_{DS}=-25V, I_D=-100mA, V_{GS}=-5V, R_G=3.3Ω
t_r	-	3.3	-	ns	
$t_{d(OFF)}$	-	7.4	-	ns	
t_f	-	5	-	ns	
Qg	-	1.4	-	nC	V_{DS}=-40V, I_D=-170mA, V_{GS}=-5V
Qgs	-	0.36	-	nC	
Qgd	-	0.29	-	nC	
Source-Drain Diode					
*V_{SD}	-	-0.85	-1.2	V	V_{GS}=0V, I_S=-130mA

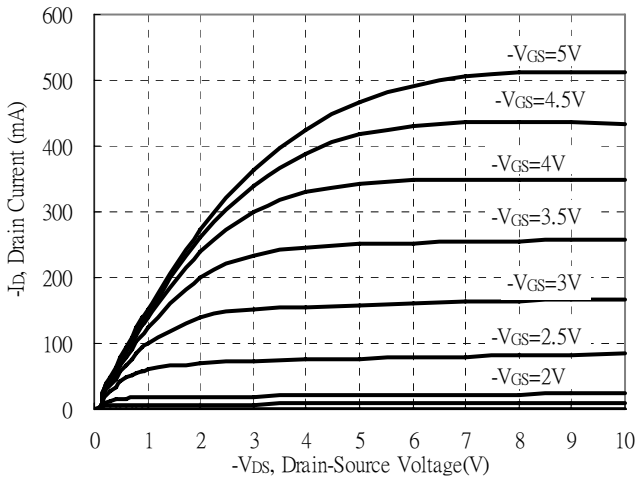
*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

Ordering Information

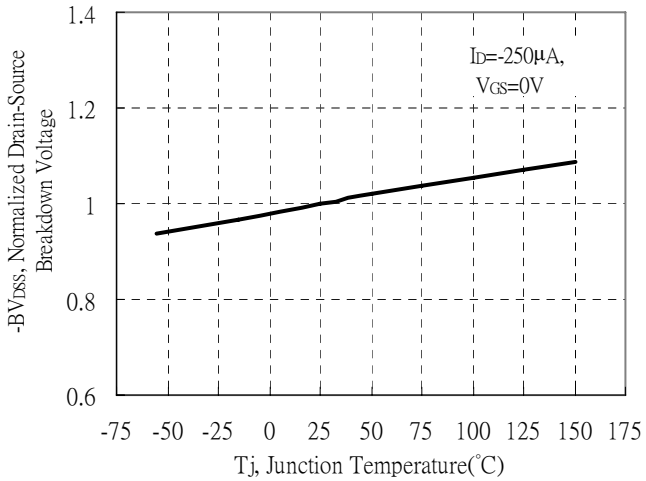
Device	Package	Shipping
BSS84S6R-0-T1-G	SOT-363 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel

Typical Characteristics

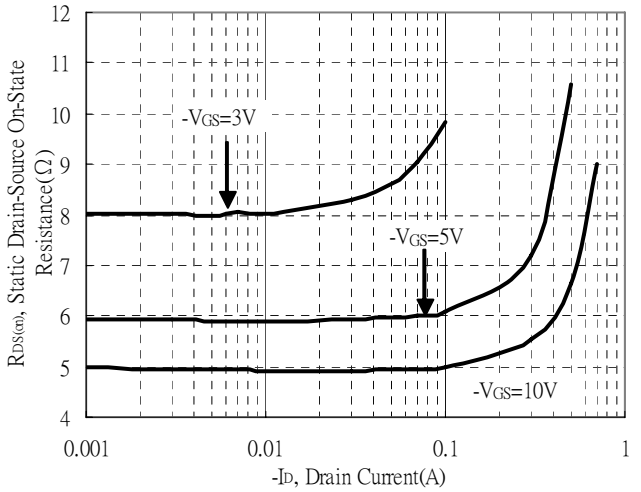
Typical Output Characteristics



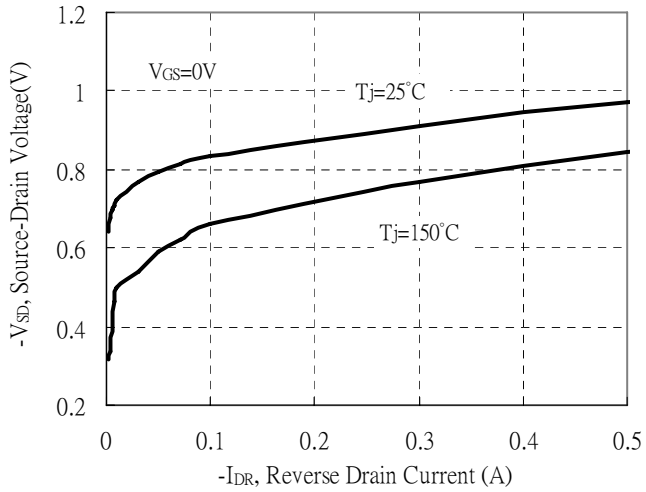
Brekdown Voltage vs Ambient Temperature



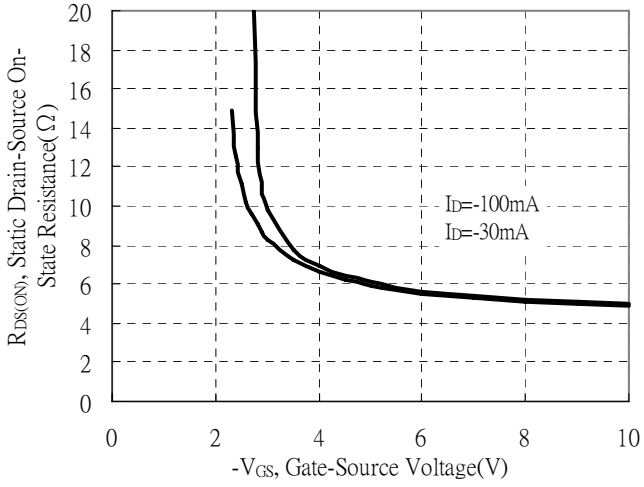
Static Drain-Source On-State resistance vs Drain Current



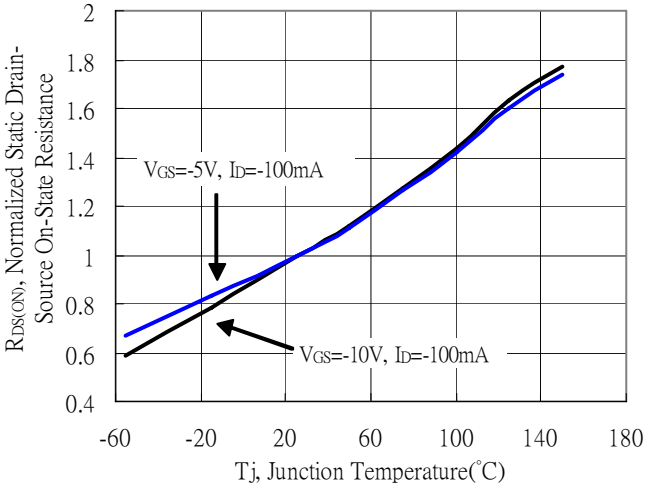
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

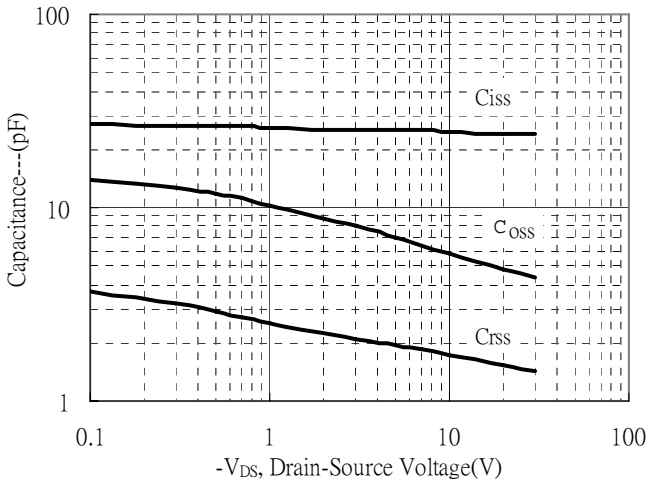


Drain-Source On-State Resistance vs Junction Tempearture

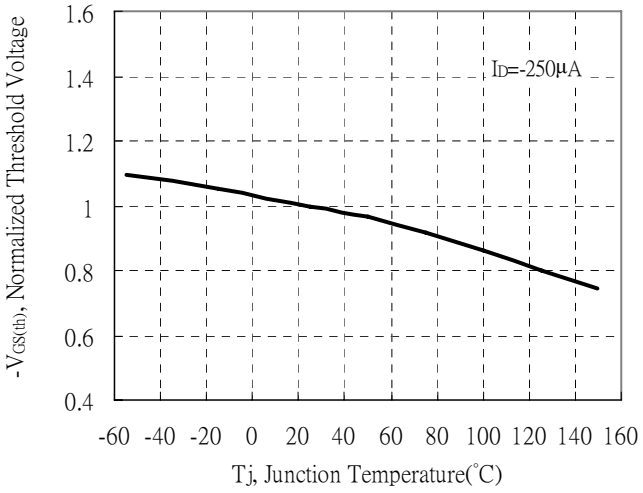


Typical Characteristics(Cont.)

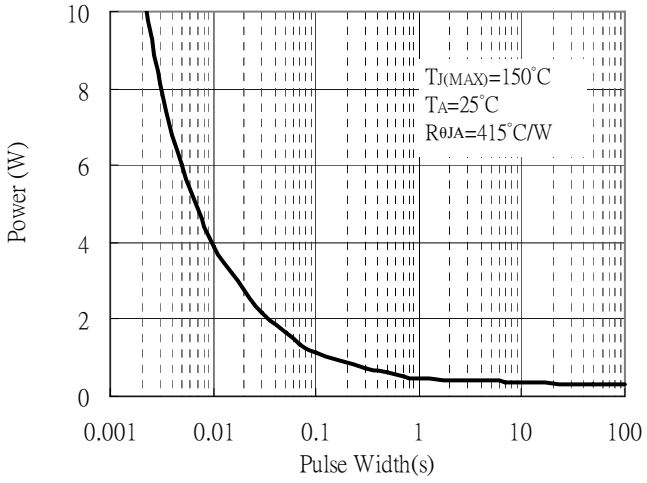
Capacitance vs Drain-to-Source Voltage



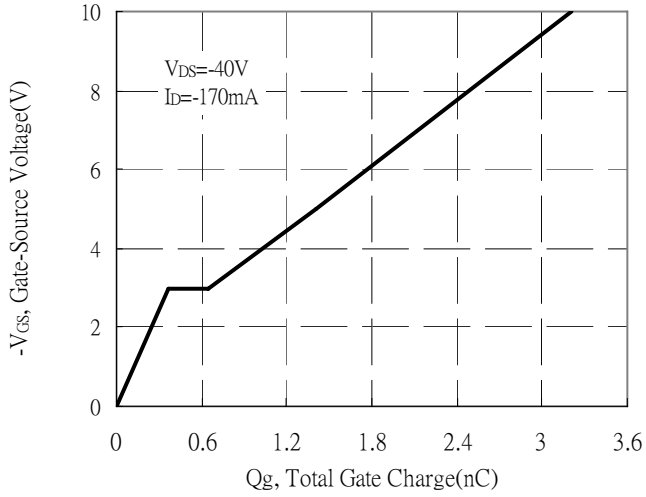
Threshold Voltage vs Junction Tempearture



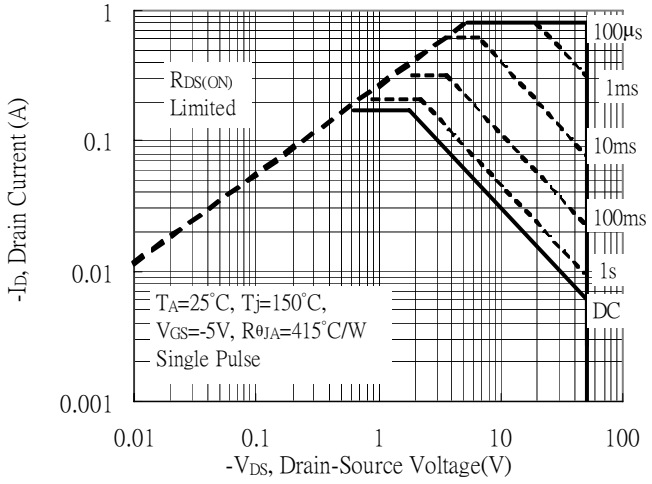
Single Pulse Power Rating, Junction to Ambient
(Note on page 1)



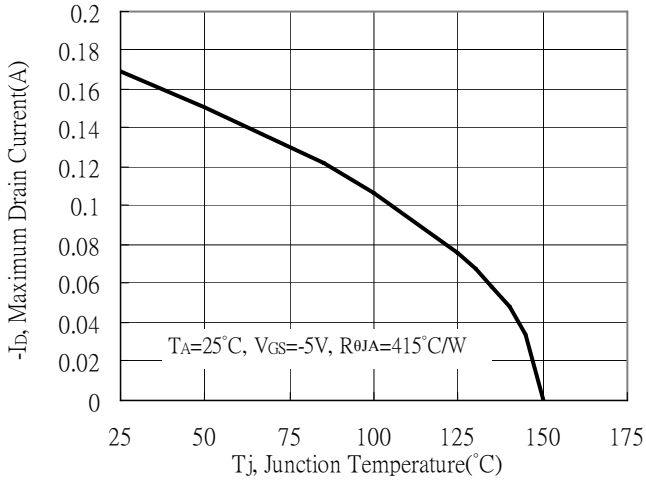
Gate Charge Characteristics



Maximum Safe Operating Area

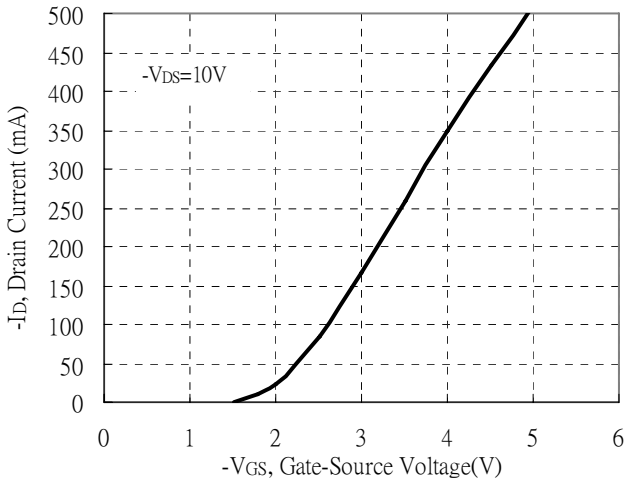


Maximum Drain Current vs JunctionTemperature

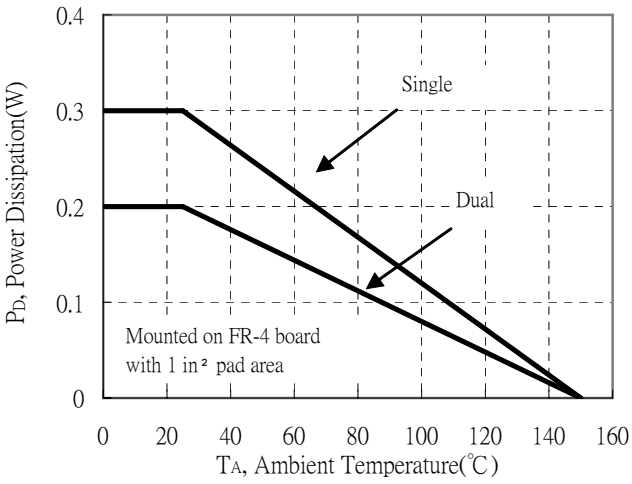


Typical Characteristics(Cont.)

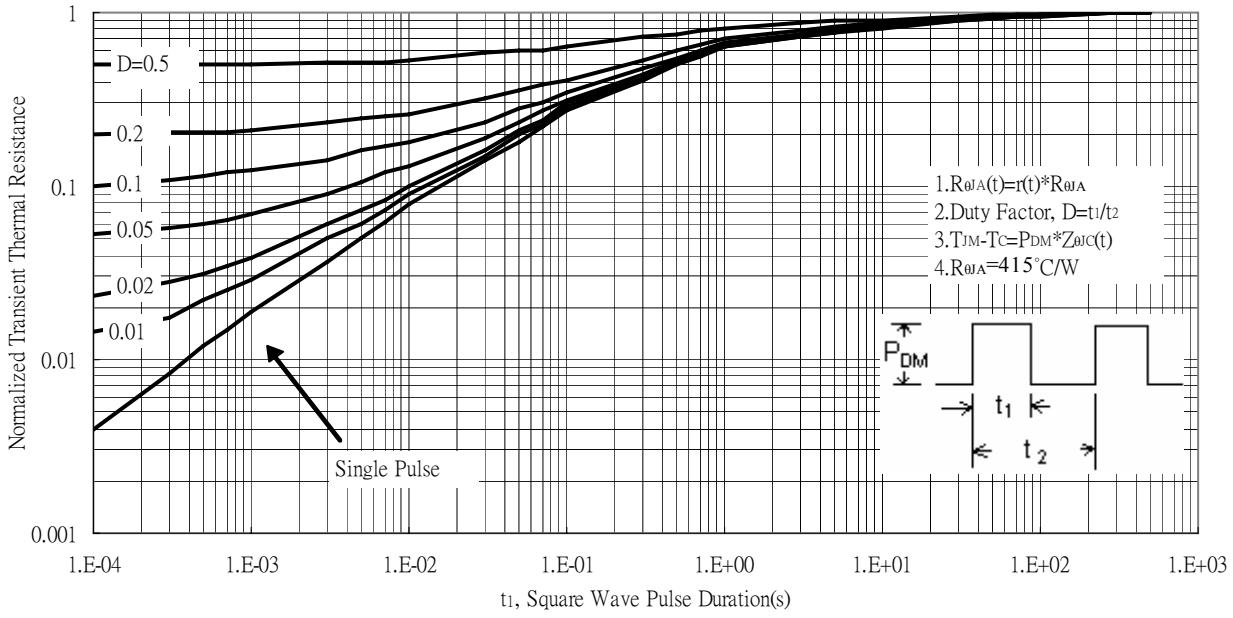
Typical Transfer Characteristics



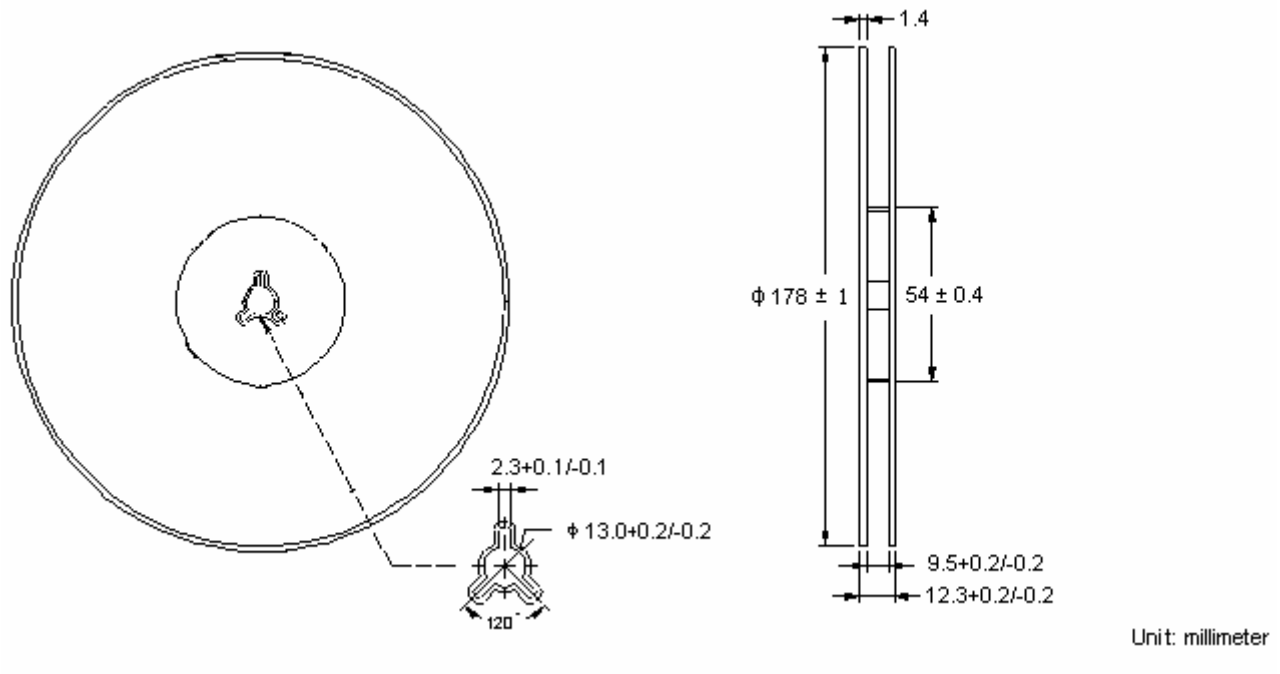
Power Derating Curves



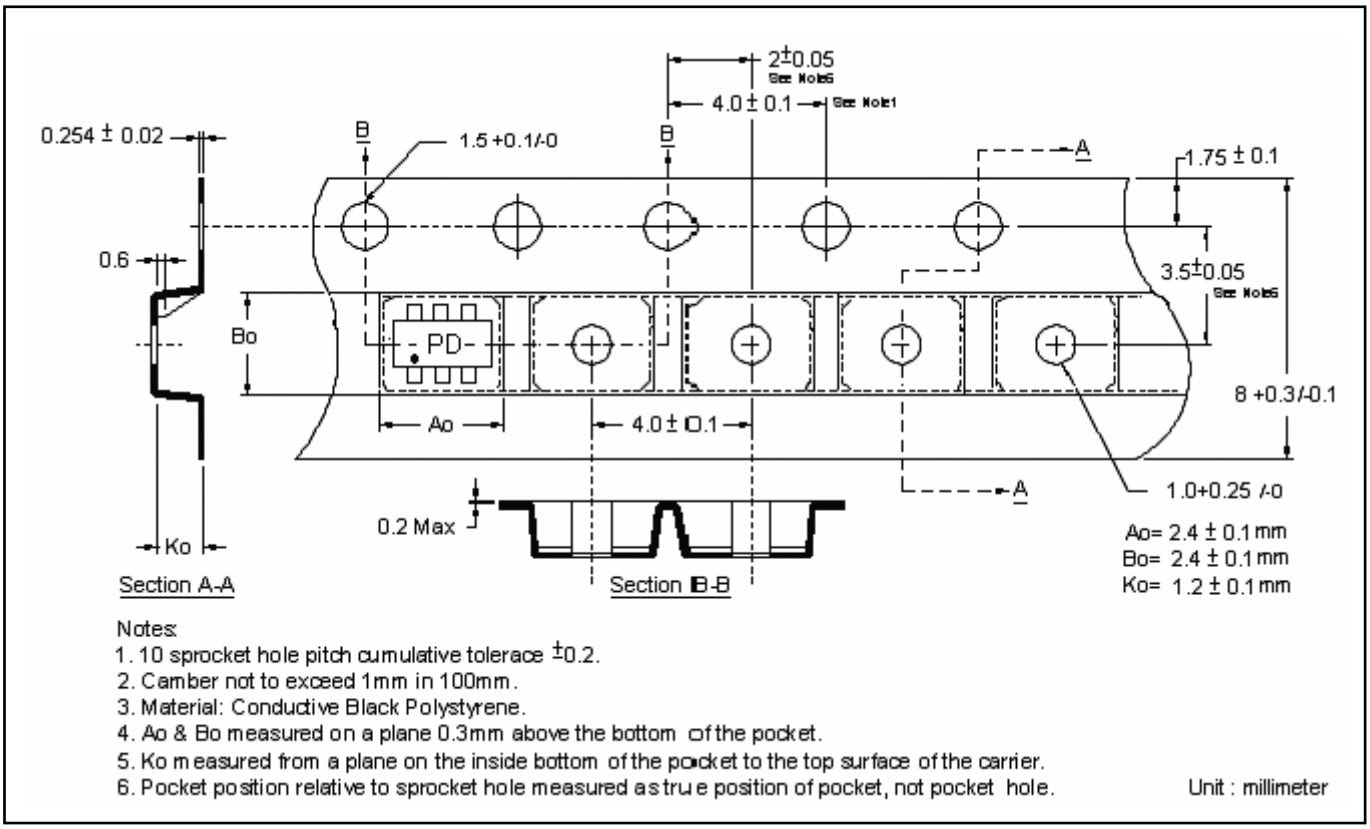
Transient Thermal Response Curves



Reel Dimension



Carrier Tape Dimension



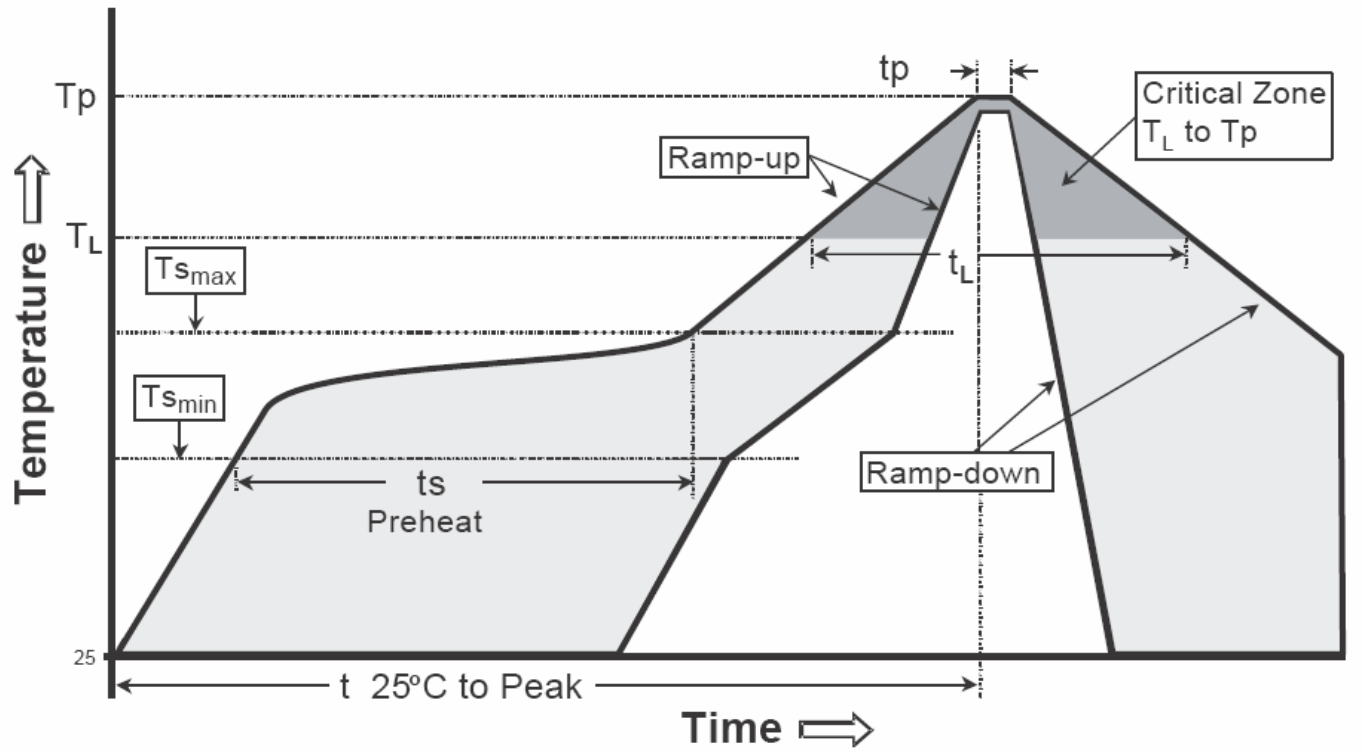
Notes:
1. 10 sprocket hole pitch cumulative tolerace ±0.2.
2. Camber not to exceed 1mm in 100mm.
3. Material: Conductive Black Polystyrene.
4. Ao & Bo measured on a plane 0.3mm above the bottom of the pocket.
5. Ko measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
6. Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.

Unit : millimeter

Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

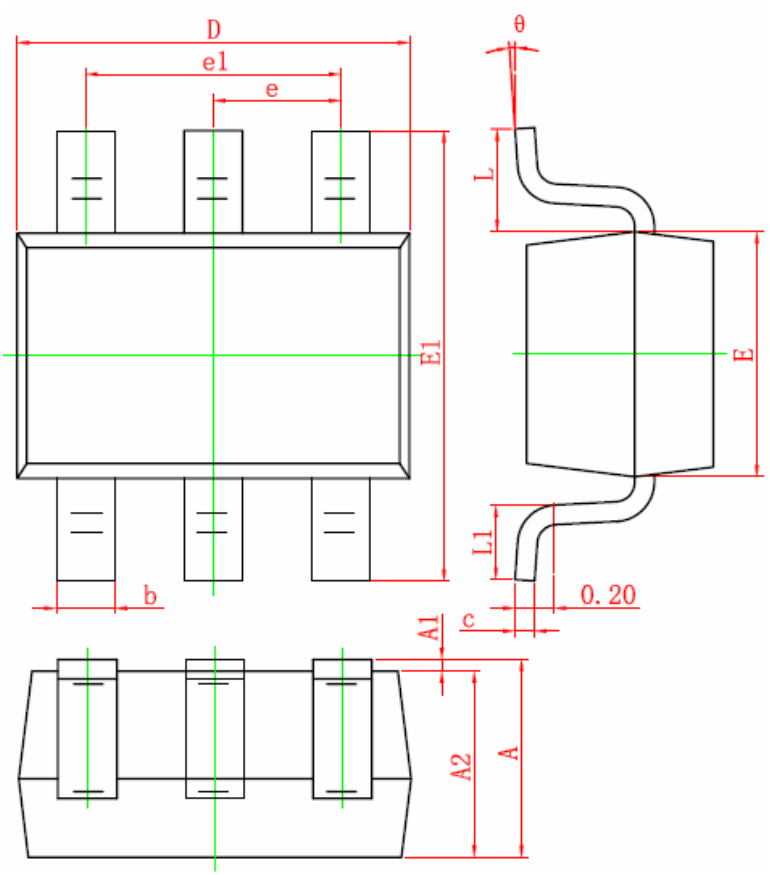
Recommended temperature profile for IR reflow



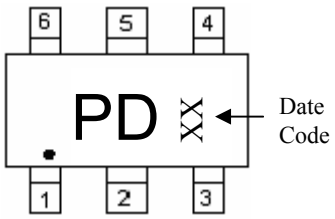
Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat −Temperature Min(T_S min) −Temperature Max(T_S max) −Time($t_{s\ min}$ to $t_{s\ max}$)	 100°C 150°C 60-120 seconds	 150°C 200°C 60-180 seconds
Time maintained above: −Temperature (T_L) − Time (t_L)	 183°C 60-150 seconds	 217°C 60-150 seconds
Peak Temperature(T_P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

SOT-363 Dimension



Marking:



6-Lead SOT-363R Plastic
Surface Mounted Package
CYStek Package Code: S6R

Style:
Pin 1. Source1 (S1)
Pin 2. Gate1 (G1)
Pin 3. Drain2 (D2)
Pin 4. Source2 (S2)
Pin 5. Gate2 (G2)
Pin 6. Drain1 (D1)

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.900	1.100	0.035	0.043	E1	2.150	2.450	0.085	0.096
A1	0.000	0.100	0.000	0.004	e	0.650 TYP		0.026 TYP	
A2	0.900	1.000	0.035	0.039	e1	1.200	1.400	0.047	0.055
b	0.150	0.350	0.006	0.014	L	0.525 REF		0.021 REF	
c	0.080	0.150	0.003	0.006	L1	0.260	0.460	0.010	0.018
D	2.000	2.200	0.079	0.087	θ	0°	8°	0°	8°
E	1.150	1.350	0.045	0.053					

Notes : 1.Controlling dimension : millimeters.
2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material :

- Lead : Pure tin plated.
- Mold Compound : Epoxy resin family, flammability solid burning class:UL94V-0.