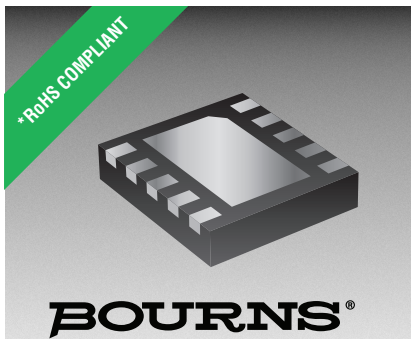




Features

- Working voltage 3.3 V
- SMT - DFN package
- Low capacitance - 4 pF
- IEC 61000-4-2 (ESD)
- IEC 61000-4-4 (EFT)
- IEC 61000-4-5 (Surge)

Applications

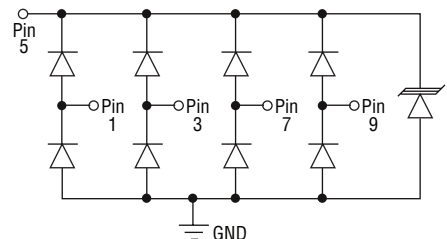
- FireWire, T1/E1, T3/E3 chip side protection
- Digital Visual Interface (DVI)
- Ethernet 10/100/1000 Base T
- High speed port protection
- Portable electronics

CDDFN10-3304NA - TVS/Steering Diode Array

General Information

The CDDFN10-3304NA device provides ESD, EFT and Surge protection for high speed data ports, assisting compliance with IEC 61000-4-2 (ESD), IEC 61000-4-4 (EFT) and IEC 61000-4-5 (Surge) requirements. The Transient Voltage Suppressor array, protecting up to 4 data lines, offers a Working Peak Voltage of 3.3 V.

The DFN-10 packaged device will mount directly onto the industry standard DFN-10 footprint. Bourns® Chip Diodes are easy to handle with standard pick and place equipment.



Absolute Maximum Ratings, $T_A = 25^\circ\text{C}$ (Unless Otherwise Noted)

Parameter	Symbol	CDDFN10-3304NA	Unit
Peak Pulse Current ($t_p = 8/20 \mu\text{s}$) per IEC 61000-4-5	I_{PP}	25	A
ESD Protection per IEC 61000-4-2			
Contact Discharge		± 30	kV
Air Discharge		± 30	kV
EFT Protection per IEC 61000-4-4 @ 5/50 ns		40	A
Storage Temperature	T_{STG}	-55 to +150	$^\circ\text{C}$
Operating Temperature	T_{OPR}	-55 to +125	$^\circ\text{C}$

Electrical Characteristics (@ $T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

Parameter	Symbol	Min.	Typ.	Max.	Unit
Breakdown Voltage @ 1 mA	V_{BR}	3.9			V
Reverse Working Peak Voltage	V_{RWM}			3.3	V
Leakage Current ¹ @ V_{RWM}	I_D			1	μA
Clamping Voltage ² @ $I_P = 5 \text{ A } 8/20 \mu\text{s}$	V_C			12	V
Clamping Voltage ² @ $I_P = 15 \text{ A } 8/20 \mu\text{s}$	V_C			15	V
Clamping Voltage ² @ $I_P = 25 \text{ A } 8/20 \mu\text{s}$	V_C			18	V
Junction Capacitance ² @ 0 V 1 MHz	C_D		4.0	4.6	pF
Junction Capacitance ³ @ 0 V 1 MHz	C_{IO}		1.5	2.3	pF

Note 1: Pin 5 to ground.

Note 2: Pin 1,3,7 or 9 to ground.

Note 3: Between Pin 1,3,7 and 9.

*RoHS Directive 2002/95/EC Jan. 27, 2003 including annex and RoHS Recast 2011/65/EU June 8, 2011.

Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

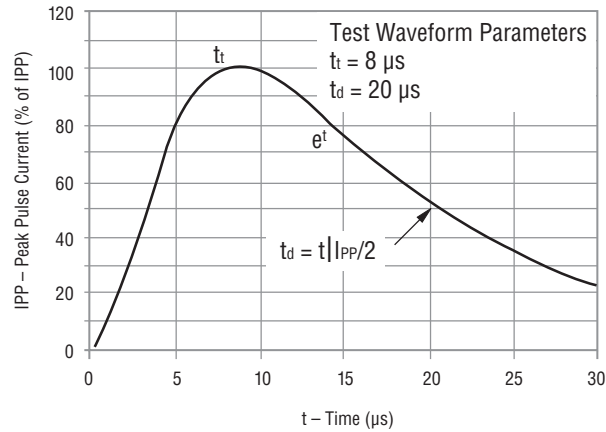
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CDDFN10-3304NA - TVS/Steering Diode Array

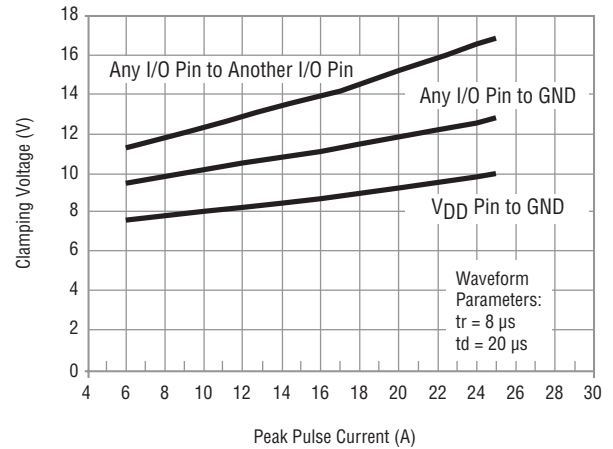
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Rating & Characteristic Curves

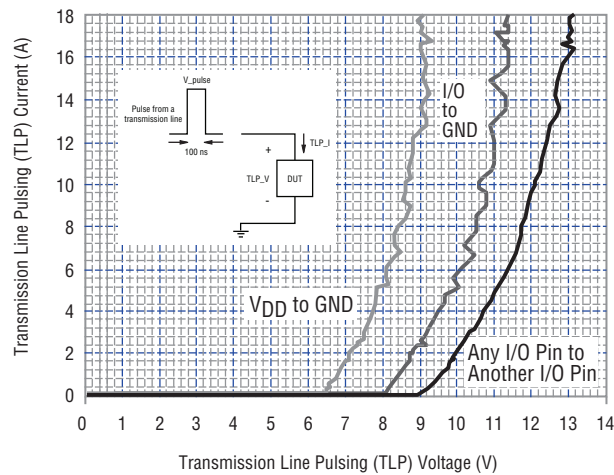
Pulse Waveform



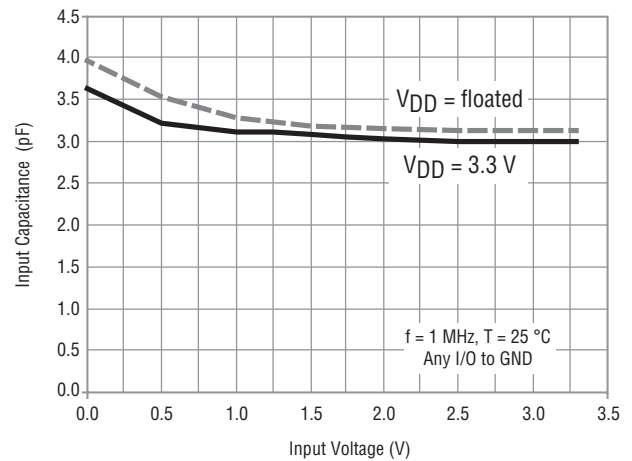
Typical Clamping Voltage vs Peak Pulse Current



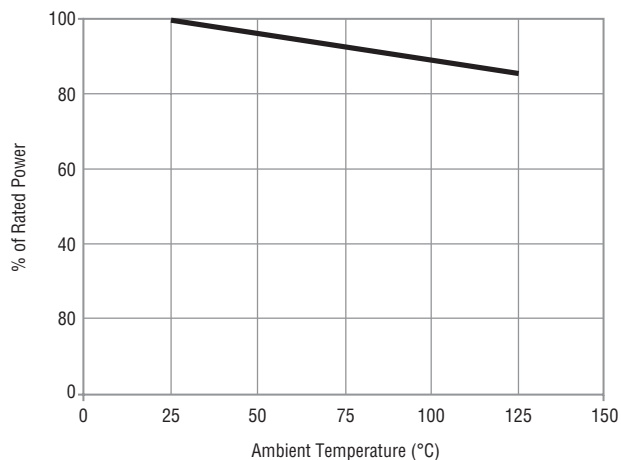
Typical Transmission Line Pulsing (TLP) Measurement



Typical Voltage vs. Capacitance



Typical Power Derating Curve



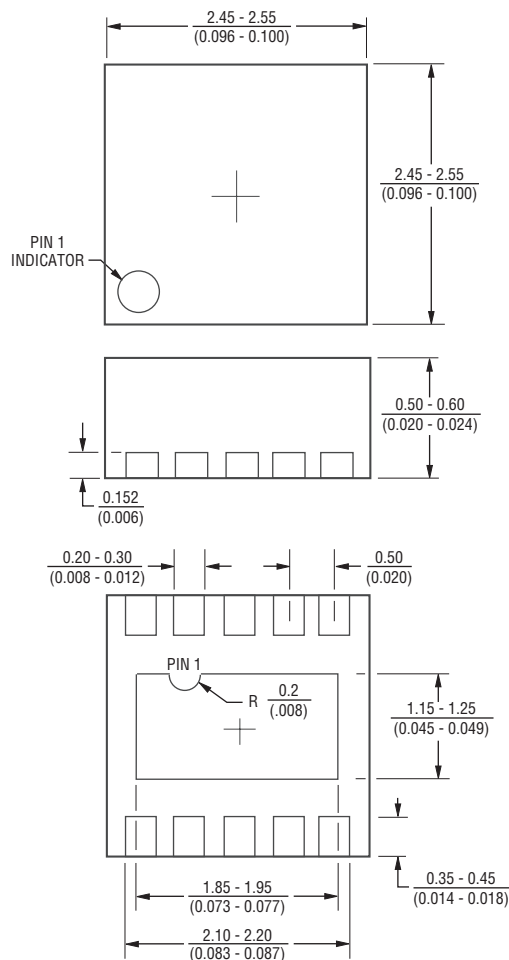
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CDDFN10-3304NA - TVS/Steering Diode Array

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Product Dimensions

This is a molded DFN10 package with lead free Nickel-Palladium-Gold (Ni/Pd/Au) on the lead frame. It has a flammability rating of UL 94V-0.

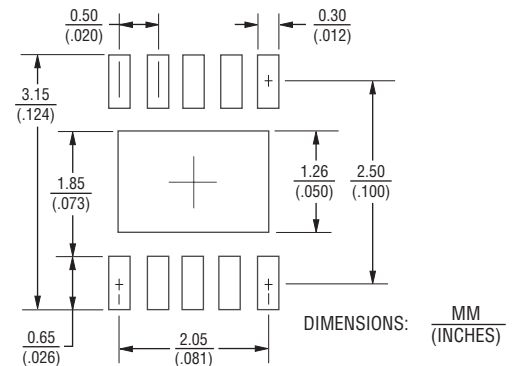


How to Order

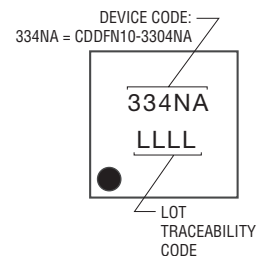
CD DFN10 - 33 04 NA

Common Diode _____
 Chip Diode _____
 Package _____
 DFN10 = DFN-10 Package
 Reverse Working Peak Voltage _____
 33 = 3.3 V_{RWM} (Volts)
 Number of Lines _____
 04 = 4 Data Lines
 Suffix _____
 NA = Low Capacitance

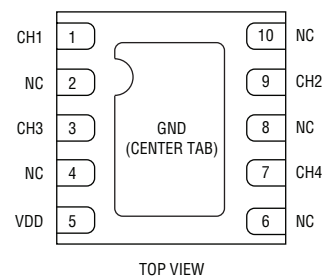
Recommended Footprint



Typical Part Marking



Pin Out



Pin	Function
1	I/O
2	N.C.
3	I/O
4	N.C.
5	V _{DD}
6	N.C.
7	I/O
8	N.C.
9	I/O
10	N.C.
CENTER TAB	GROUND

Environmental Specifications

Moisture Sensitivity Level.....3
 ESD Classification (HBM).....3B

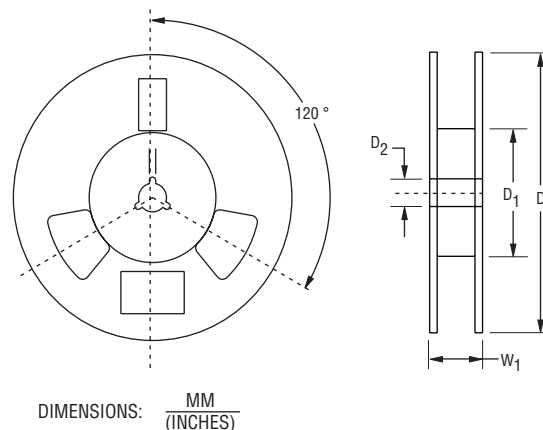
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The product will be dispensed in tape and reel format (see diagram below).



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