

‡ o U TVS For ESD Protection

FEATURES

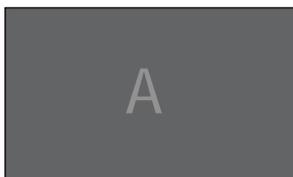
- Package optimized for high-speed lines
- Ultra-small package (1.0mmx0.6mmx0.4mm)
- Protects one data, control or power line
- Low capacitance
- Low leakage current
- Low clamping voltage
- Each I/O pin can withstand over 1000 ESD strikes for $\pm 8\text{kV}$ contact discharge
- MSL 3

MECHANICAL DATA

- Case : Molded, plastic DFN1006-2L
- Polarity : Shown follows
- Epoxy : UL94-V0 rated flame retardant

DFN1006-2L PACKAGE

Marking : M



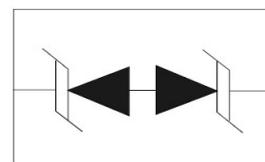
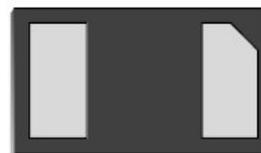
APPLICATIONS

- Portable Electronics
- Desktops, Servers and Notebooks
- Cellular Phones
- MP3 Ports
- Digital Ports
- Subscriber Identity Module (SIM) card

IEC COMPATIBILITY

- IEC 61000-4-2 (ESD) $\pm 15\text{kV}$ (Air), $\pm 8\text{kV}$ (Contact)
- EC 61000-4-4 (EFT) 40A (5/50 ns)
- Cable Discharge Event (CDE)

SCHEMATIC & PIN CONFIGURATION



DESCRIPTION

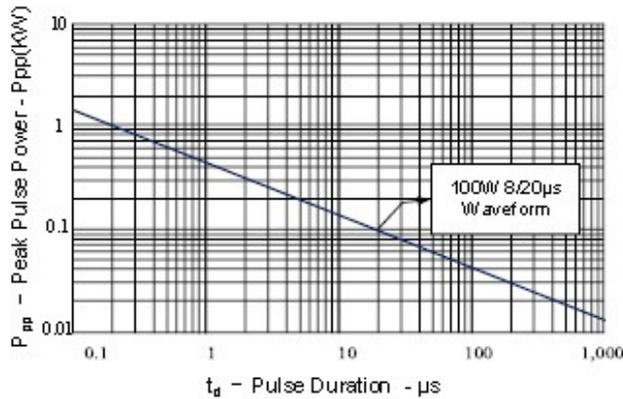
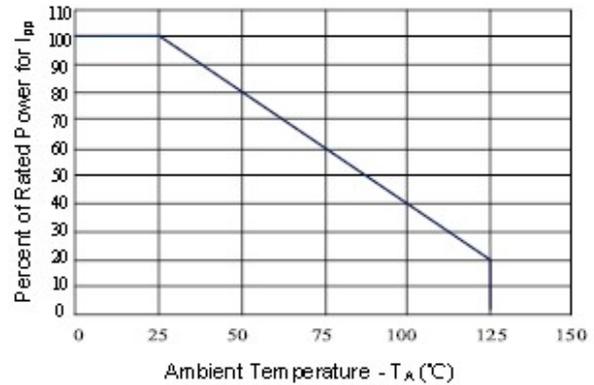
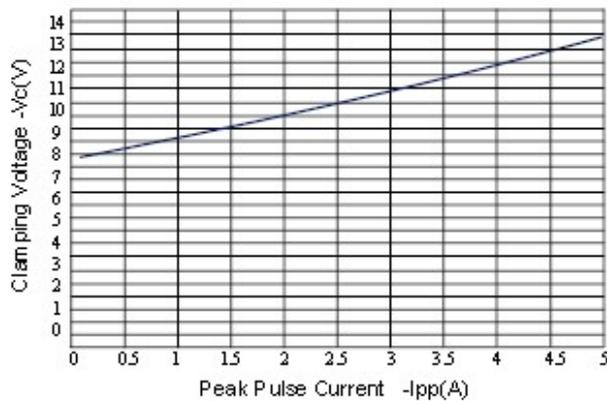
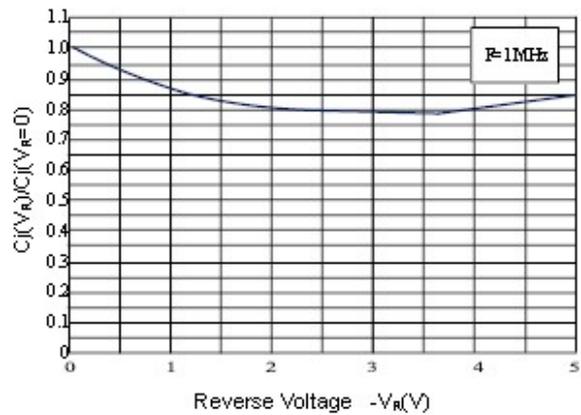
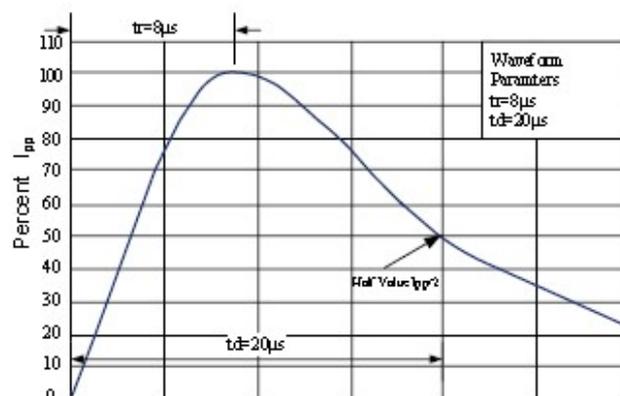
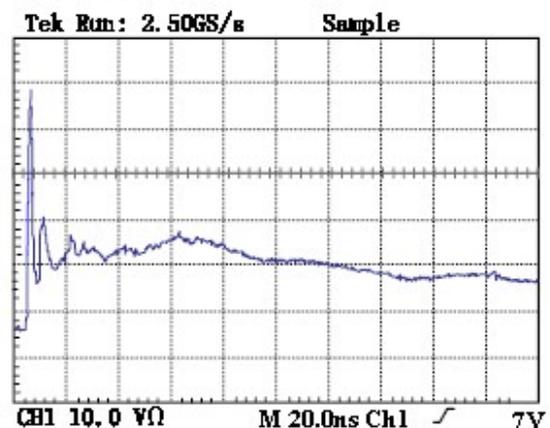
- SESD05CD is a low-capacitance Transient Voltage Suppressor (TVS) designed to provide electrostatic discharge (ESD) protection for data, control or power lines. With typical capacitance of 8pF only, SESD05CD is designed to protect parasitic-sensitive systems against over-voltage and over-current transient events. It complies with IEC 61000-4-2 (ESD), Level 4 ($\pm 15\text{kV}$ air, $\pm 8\text{kV}$ contact discharge), IEC 61000-4-4 (electrical fast transient - EFT) (40A, 5/50 ns), very fast charged device model (CDM) ESD and cable discharge event (CDE), etc.
- SESD05CD uses ultra-small DFN1006 package. Each SESD05CD device can protect one data line. It offers system designers flexibility to protect single data line where space is a premium concern.

**ABSOLUTE MAXIMUM RATING**

Symbol	Parameter	Value	Units
P _{PP}	Peak Pulse Power (8/20μs)	100	W
T _j	Operating Temperature	-55/+125	°C
T _{STG}	Storage Temperature	-55/+150	°C

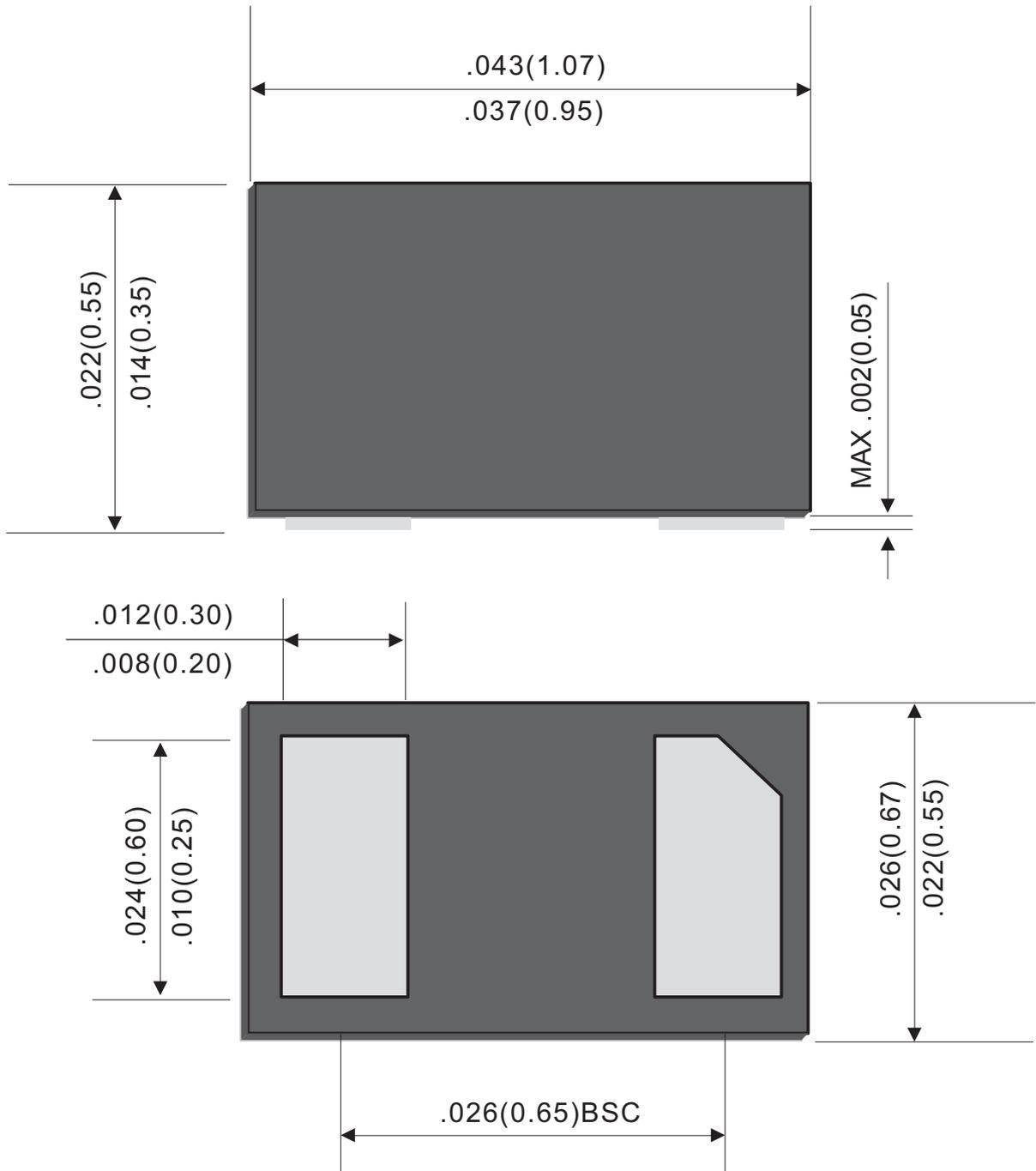
ELECTRICAL CHARACTERISTICS (T_{amb}=25°C)

Symbol	Parameter	Test Condition	Min	Typ	Max	Units
V _{RWM}	Reverse Stand-Off Voltage				5.0	V
V _{BR}	Reverse Breakdown voltage	I _T =1mA	6.0			V
I _R	Reverse leakage current.	V _{RWM} =5V			1	μA
I _{PP}	Peak Pulse Current	t _p =8/20us			5	A
V _C	Clamping Voltage	I _{PP} =1A, t _p =8/20us I _{PP} =5A, t _p =8/20us		13	9.5 15	V
C _J	Junction Capacitance	V _R =0V,f=1MHz		8	15	pF

ELECTRICAL CHARACTERISTICS CURVE
Figure 1: Peak Pulse Power Vs Pulse Time

Figure 2: Power Derating Curve

Figure 3: Clamping Voltage vs. Peak Pulse Current

Figure 4: Normalized Junction Capacitance vs. Reverse Voltage

Figure 5: Pulse Waveform

Figure 6: ESD Clamping (8kV Contact per IEC 61000-4-2)


Outline Drawing

DFN1006-2L



Dimensions in inches and (millimeters)

Rev.B-1

Ordering Information:

Device PN	Packing
SESD05CD -T ⁽¹⁾ H ⁽²⁾ -WS	Tape&Reel: 10 Kpcs/Reel

Note: (1) Packing code, Tape & Reel

(2) Halogen free product for packing code suffix "H"

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