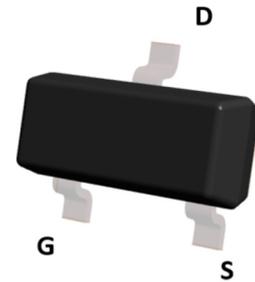
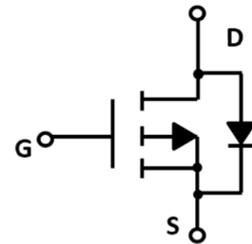
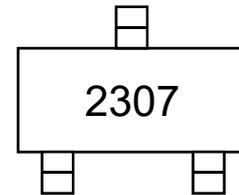


Description

- Trench Power LV MOSFET technology
- High density cell design for Low $R_{DS(ON)}$
- High Speed switching


Top View

Circuit Diagram

Marking (Top View)

MOSFET Product Summary		
$V_{DS}(V)$	$R_{DS(on)}(m\Omega)$	$I_D(A)$
-30	70@ $V_{GS} = -10V$	-3.0
	100@ $V_{GS} = -4.5V$	

Applications

- Battery protection
- Load switch
- Power management

Absolute maximum rating@25°C

Rating	Symbol	Value	Units
Drain-source Voltage	V_{DS}	-30	V
Gate-source Voltage	V_{GS}	± 20	V
Drain Current @ $T_A = 25^\circ C$	I_D	-3.0	A
Pulsed Drain Current ¹⁾	I_{DM}	-15	A
Total Power Dissipation @ $T_A=25^\circ C$	P_D	1.2	W
Thermal Resistance Junction-to-Ambient @ Steady State ²⁾	$R_{\theta JA}$	105	$^\circ C/W$
Junction and Storage Temperature Range	T_J, T_{STG}	-55~+150	$^\circ C$

Notes:

 1) Pulse Test: Pulse Width $\leq 300\mu s$, Duty cycle $\leq 2\%$.

2) Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.

Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0V, I_D = -250\mu A$	-30	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -30V, V_{GS} = 0V$	-	-	-1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$	-0.8	-1.4	-2.0	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = -10V, I_D = -3.0A$	-	70	85	m Ω
		$V_{GS} = -4.5V, I_D = -2.0A$	-	100	140	
Diode Forward Voltage	V_{SD}	$I_S = -3.0A, V_{GS} = 0V$	-	-0.8	-1.2	V
Maximum Body-Diode Continuous Current	I_S		-	-	-3.0	A
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{DS} = -15V, V_{GS} = 0V,$ $f = 1MHz$	-	580	-	pF
Output Capacitance	C_{oss}		-	98	-	
Reverse Transfer Capacitance	C_{rss}		-	74	-	
Switching Parameters						
Total Gate Charge	Q_g	$V_{GS} = -10V, V_{DS} = -15V,$ $I_D = -3.0A$	-	6.8	-	nC
Gate Source Charge	Q_{gs}		-	1.0	-	
Gate Drain Charge	Q_{gd}		-	1.4	-	
Turn-on Delay Time	$t_{D(on)}$	$V_{GS} = -10V, V_{DD} = -15V,$ $RL = 15\Omega, I_D = -1.0A,$ $R_{GEN} = 2.5\Omega$	-	14	-	ns
Turn-on Rise Time	t_r		-	61	-	
Turn-off Delay Time	$t_{D(off)}$		-	19	-	
Turn-off Fall Time	t_f		-	10	-	

Typical Characteristics

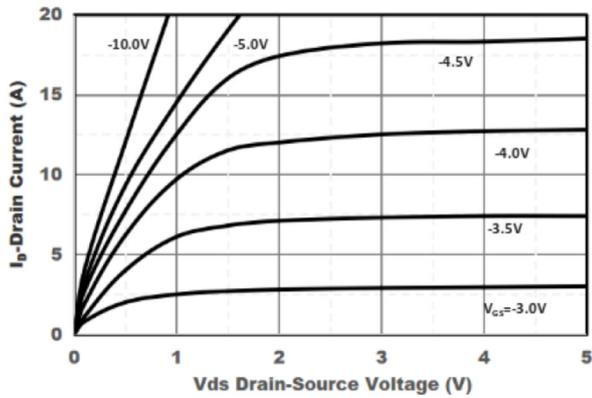


Figure1. Output Characteristics

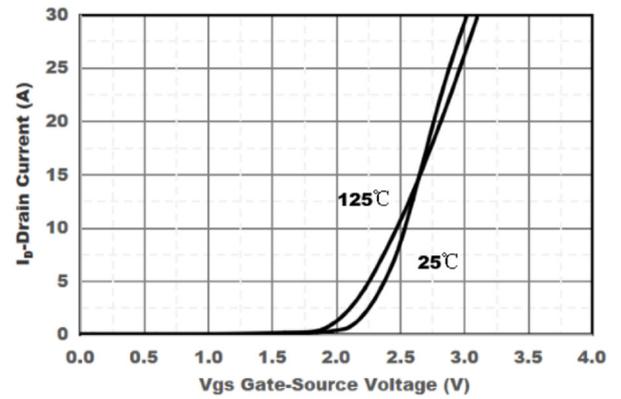


Figure2. Transfer Characteristics

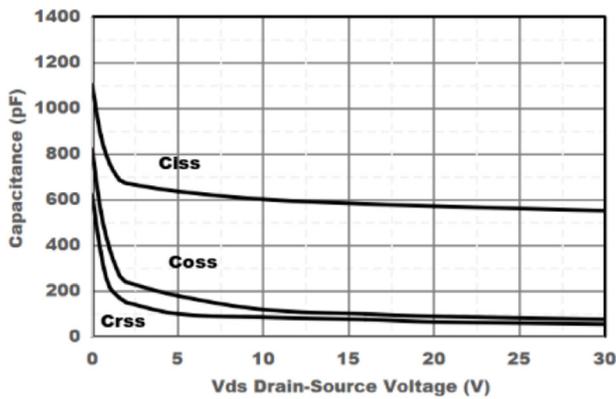


Figure3. Capacitance Characteristics

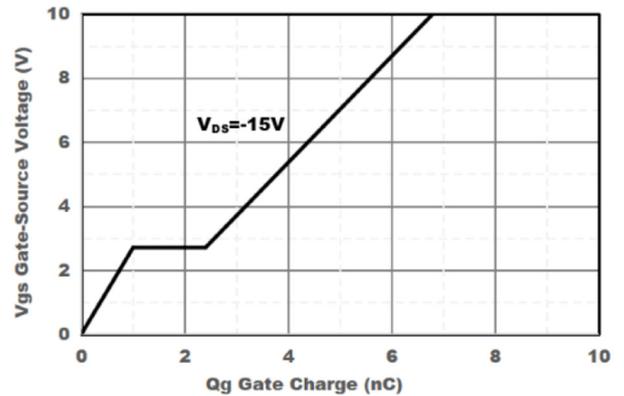


Figure4. Gate Charge

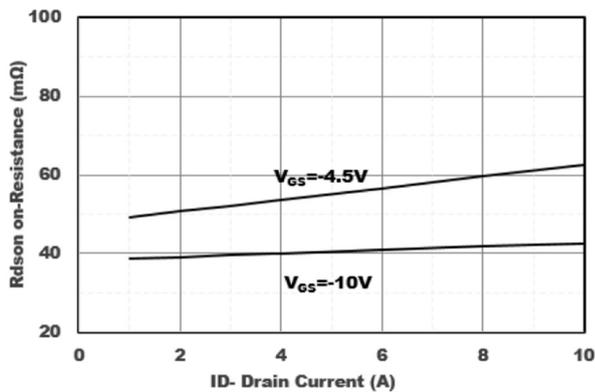


Figure5. Drain-Source on Resistance

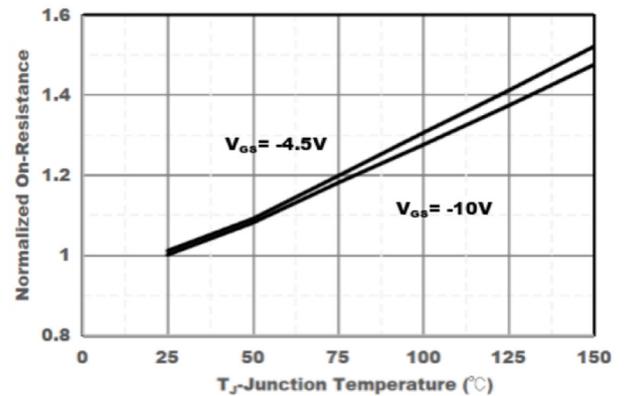


Figure6. Drain-Source on Resistance

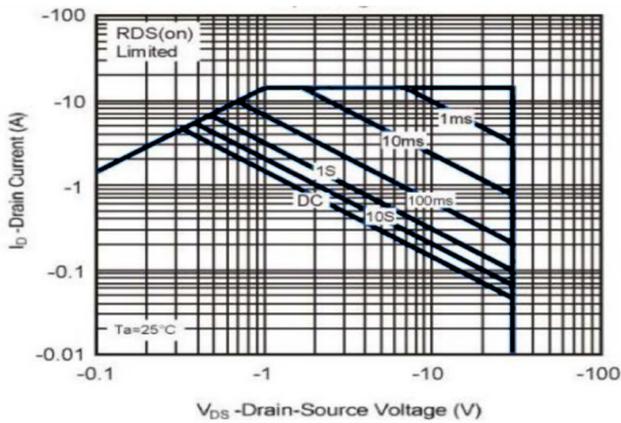


Figure7. Safe Operation Area

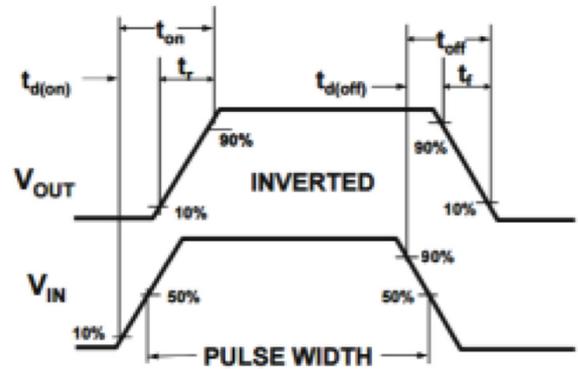
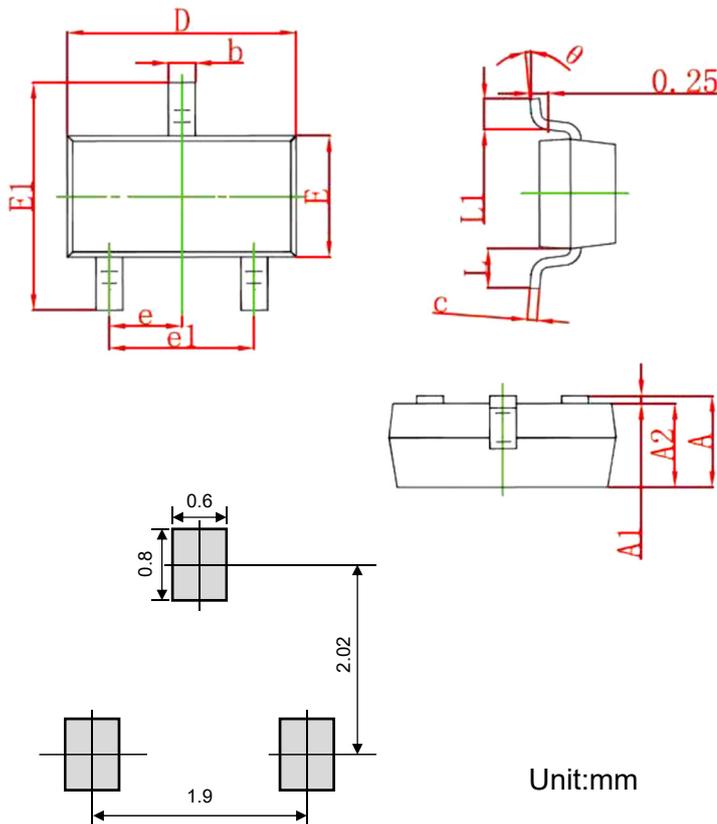


Figure8. Switching wave

Product dimension (SOT-23)



Suggested PCB Layout

Dim	Millimeters		Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 Typ.		0.037 Typ.	
e1	1.800	2.000	0.071	0.079
L	0.550 Ref.		0.022 Ref.	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

Ordering information

Device	Package	Reel	Shipping
PPMT2307	SOT-23 (Pb-Free)	7"	3000 / Tape & Reel

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